



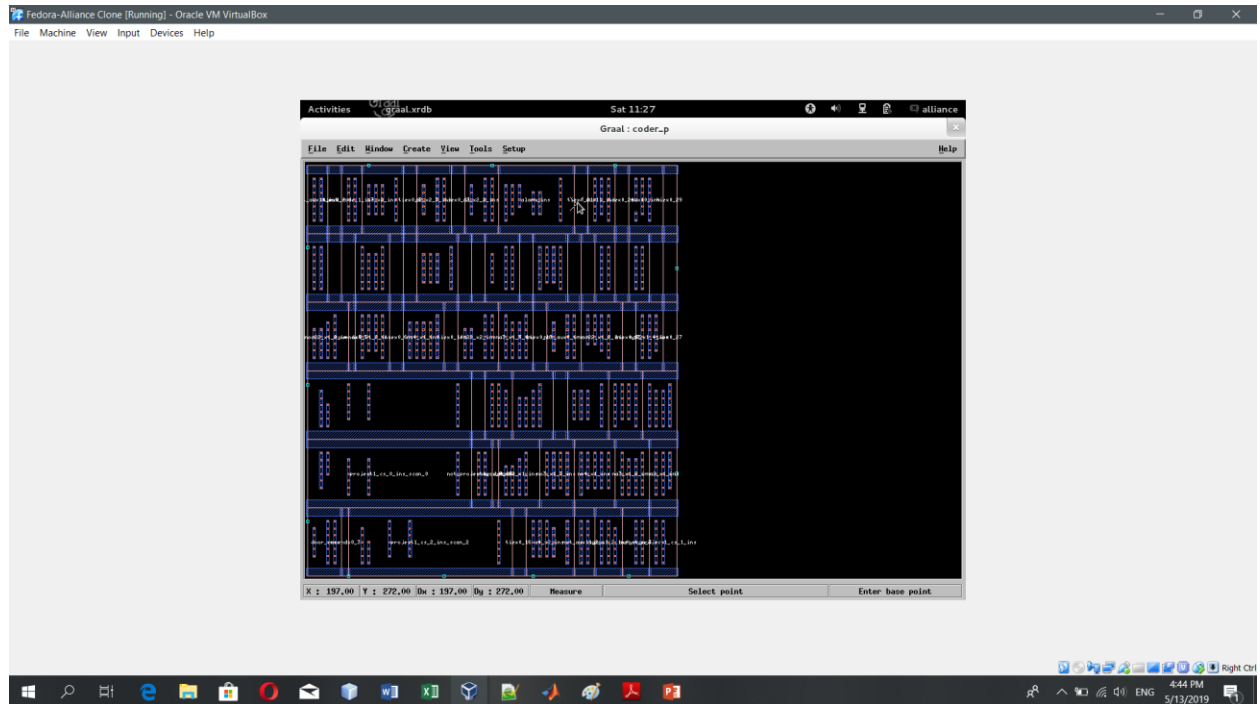
Phase 3

Name: Mohand Mahmoud Farag

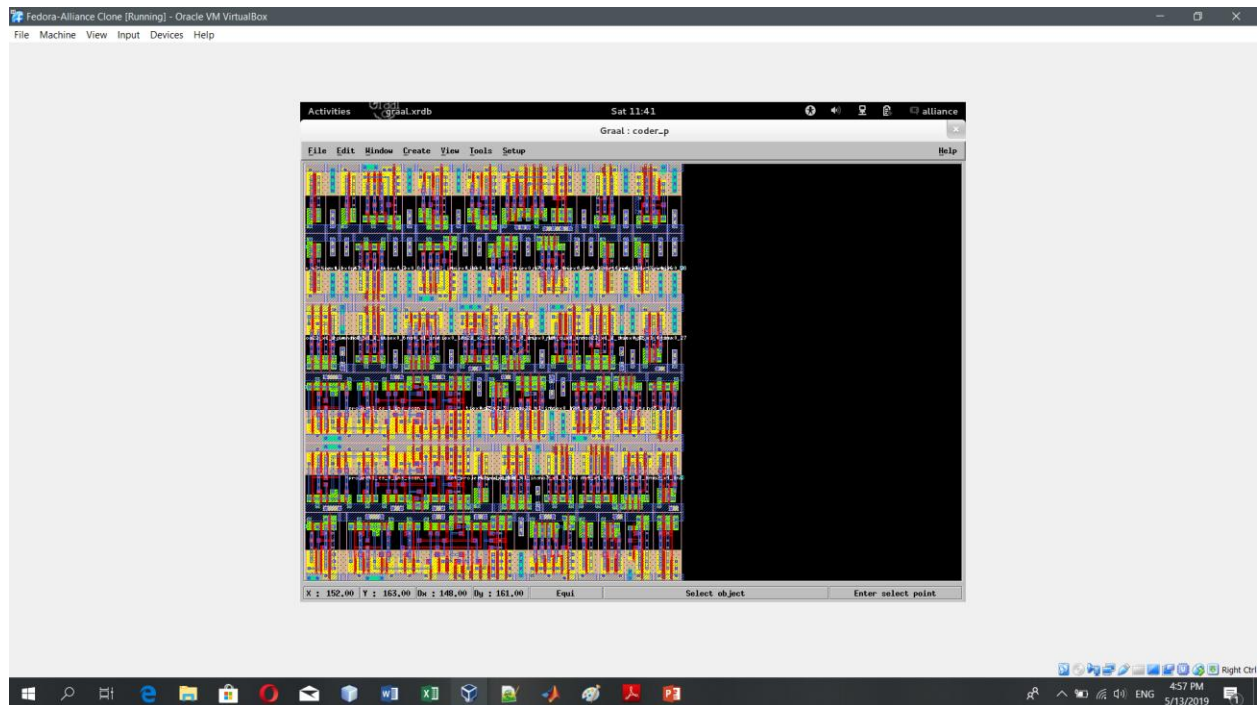
ID: 16p6042

Email: Mohand.zawrah98@yahoo.com

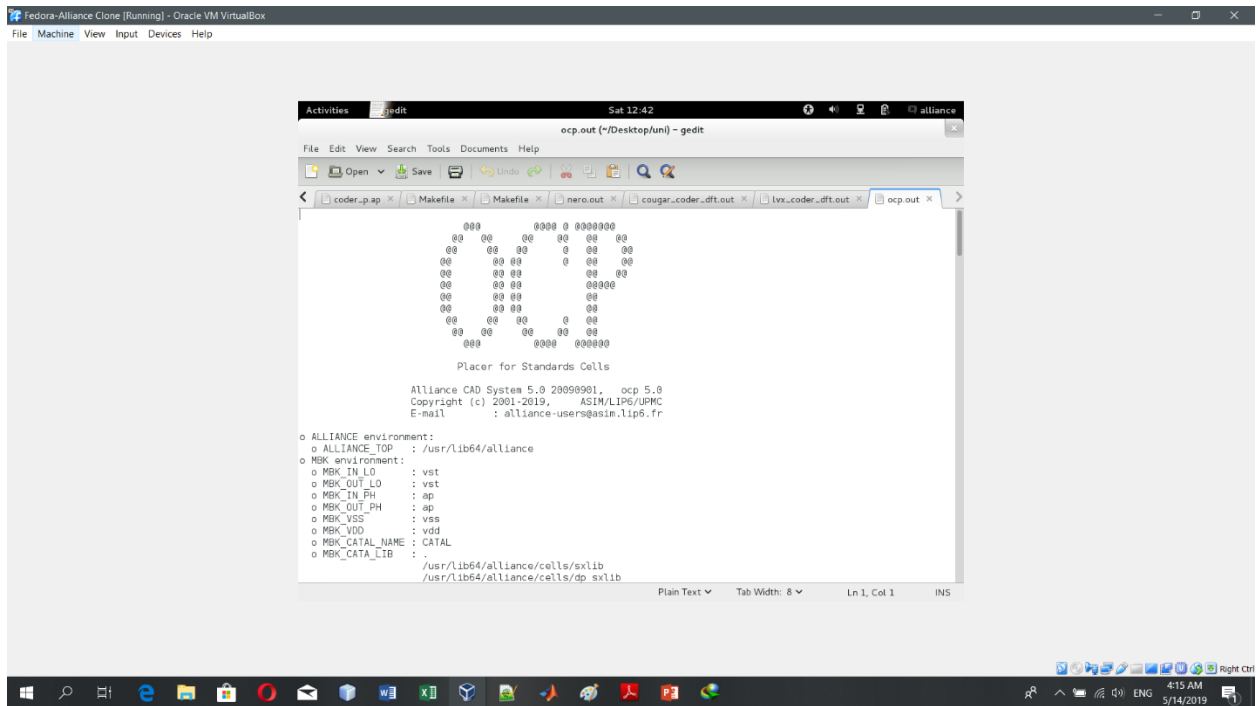
ocp output:



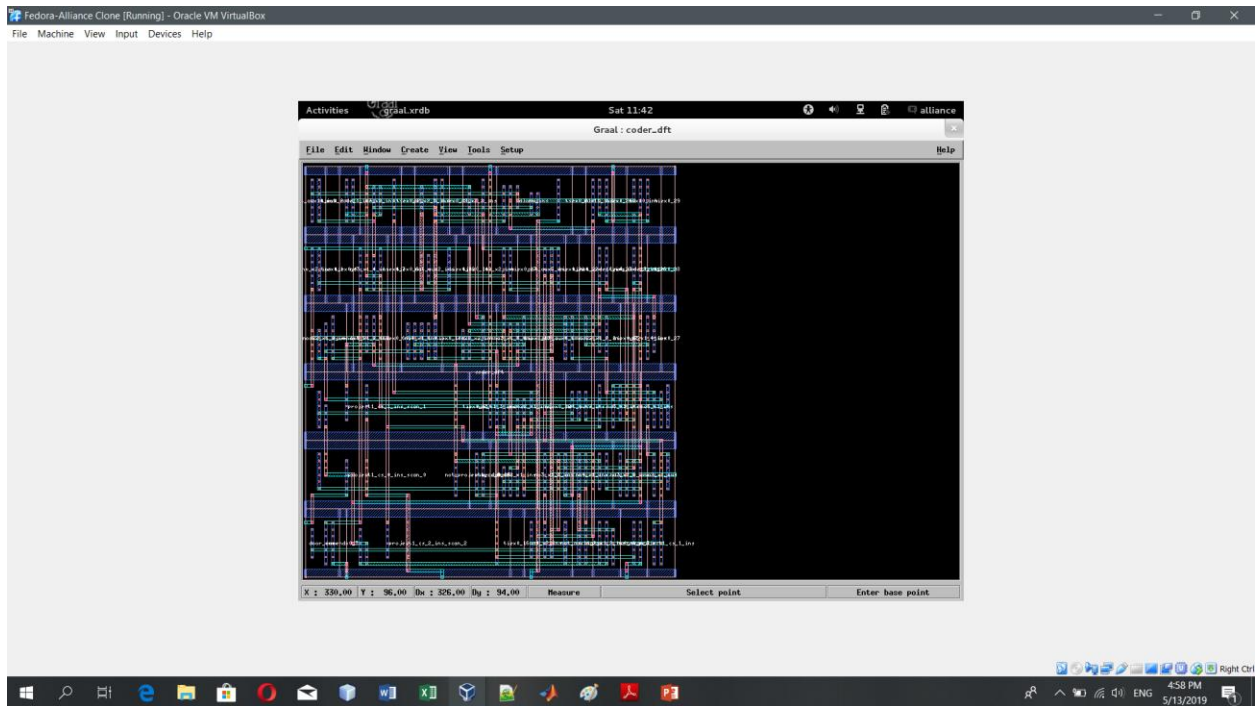
Flatted:



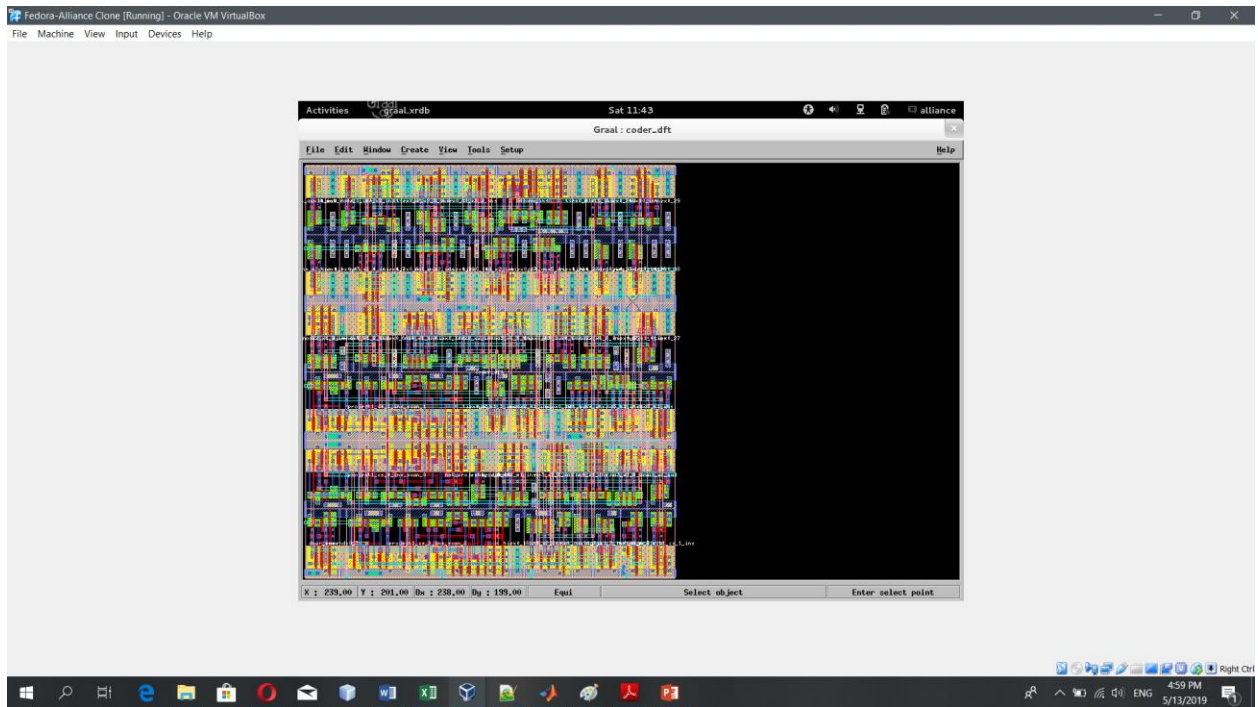
Ocp file output:



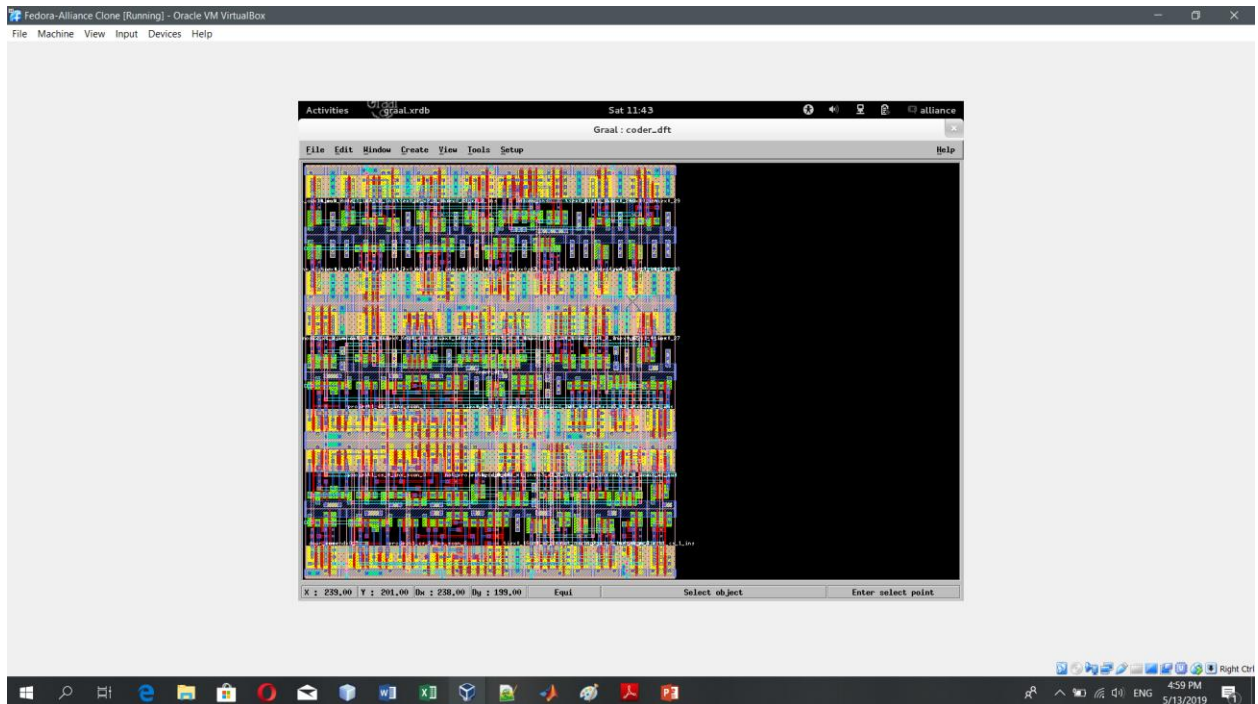
Nero output:



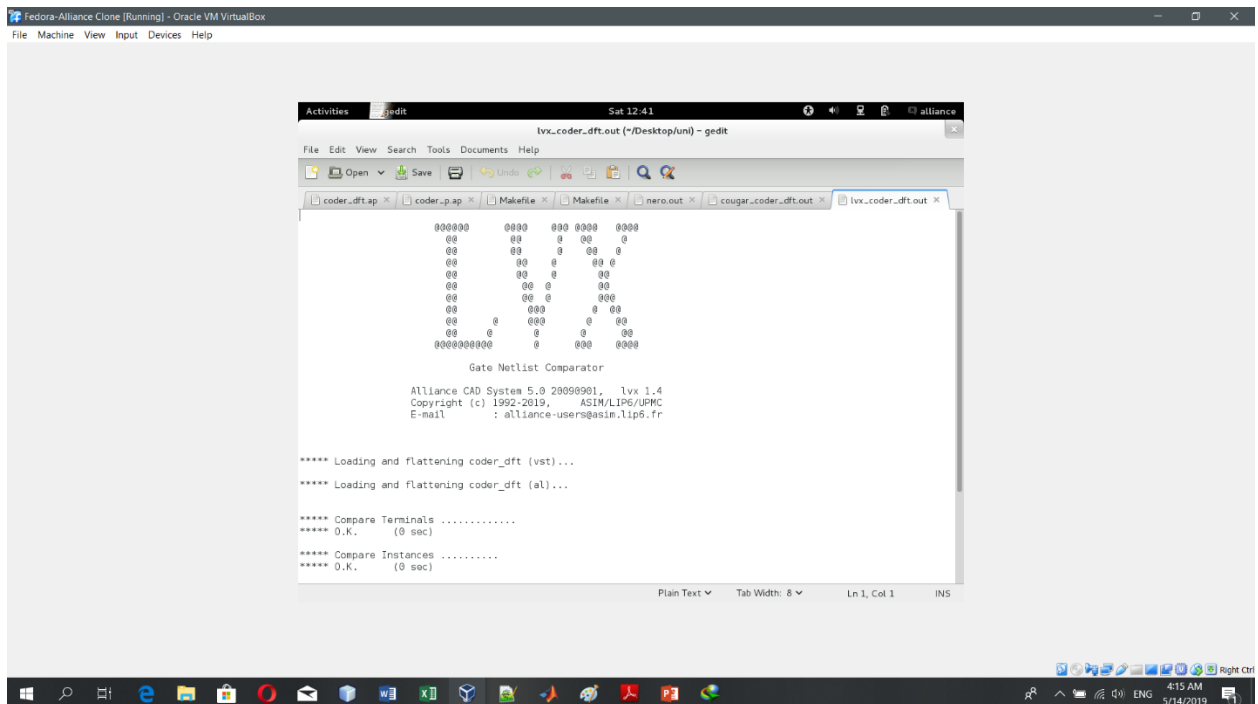
Flatted:



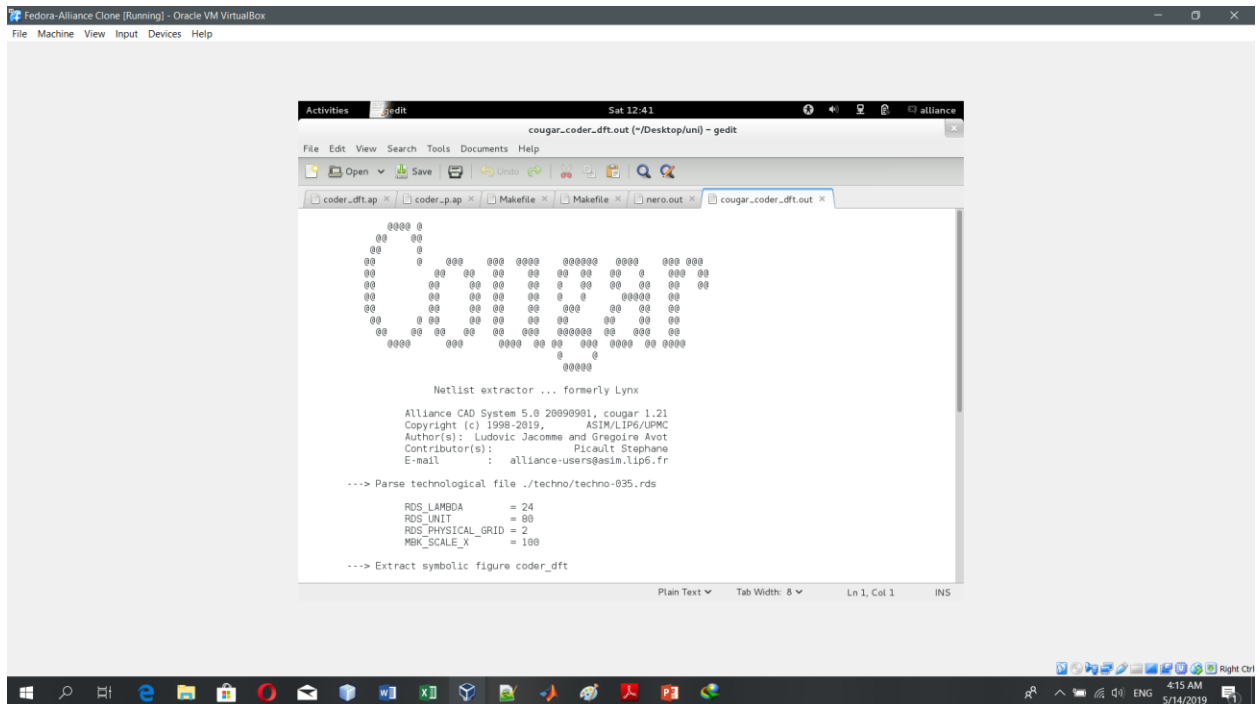
Nero file output:



Lvx file output:



Cougar file output:



The screenshot shows a Fedora VM window titled "Fedora-Alliance Clone [Running] - Oracle VM VirtualBox". Inside the VM, a gedit editor window titled "cougar_coder_dft.out (~/.Desktop/unl) - gedit" displays the output of a Netlist extractor. The output includes a header with "0000 0" and "00 0", followed by a list of parameters: RDS_LAMBDA = 24, RDS_UNIT = 80, RDS_PHYSICAL_GRID = 2, and MBK_SCALE_X = 100. The output also includes a copyright notice for Alliance CAD System 5.0 20090901, cougar 1.21, and a list of authors: Ludovic Jacome and Gregoire Avot.

```
0000 0
00 0
00 0 000 000 0000 00000 0000 000 000
00 00 00 00 00 00 00 0 000 00
00 00 00 00 00 0 00 00 00 00
00 00 00 00 00 0 0 00000 00
00 00 00 00 00 000 00 00 00
00 0 00 00 00 00 00 00 00
00 00 00 00 000000 00 000 00
0000 000 0000 00 000 00000
0
00000

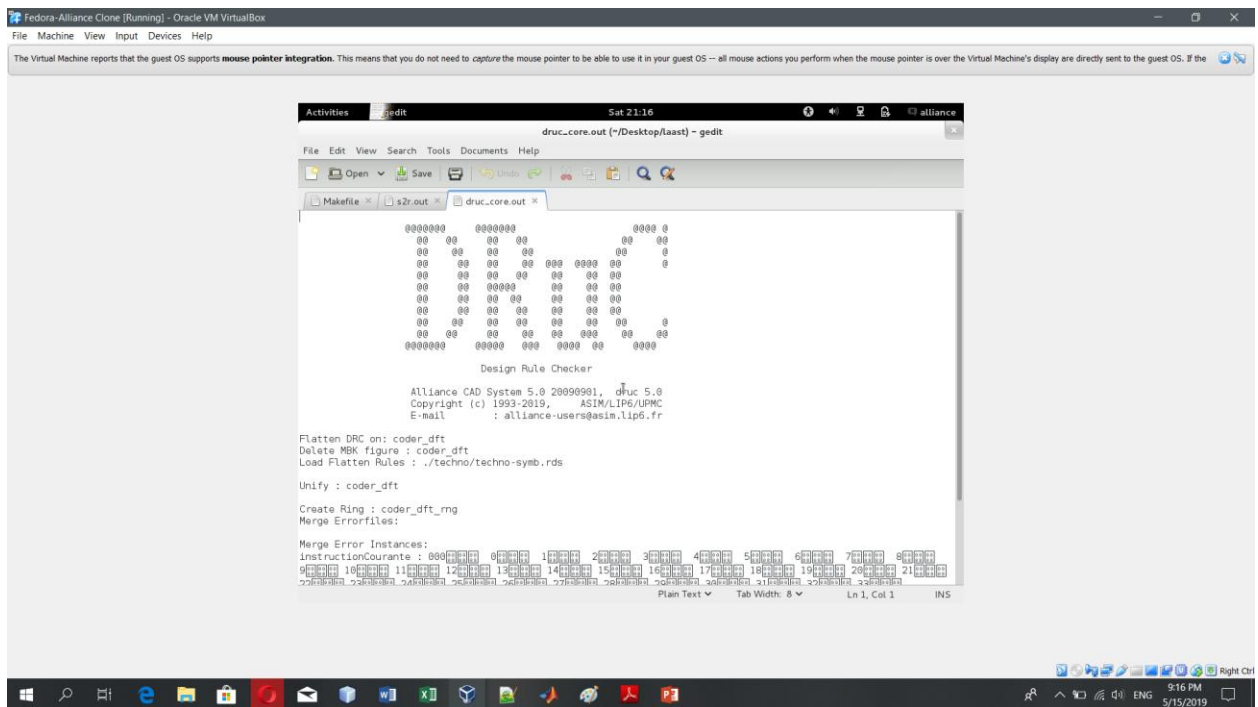
Netlist extractor ... formerly Lynx
Alliance CAD System 5.0 20090901, cougar 1.21
Copyright (c) 1998-2019, ASIM/LIP6/UPMC
Author(s): Ludovic Jacome and Gregoire Avot
Contributor(s): Picault Stephane
E-mail : alliance-users@asim.lip6.fr

--> Parse technological file ./techno/techno-835.rds

RDS_LAMBDA = 24
RDS_UNIT = 80
RDS_PHYSICAL_GRID = 2
MBK_SCALE_X = 100

--> Extract symbolic figure coder_dft
```

Druc file output:



The screenshot shows a Fedora VM window titled "Fedora-Alliance Clone [Running] - Oracle VM VirtualBox". Inside the VM, a gedit editor window titled "druc_core.out (~/.Desktop/laast) - gedit" displays the output of a Design Rule Checker. The output includes a header with "0000000 0000000" and "0000 0", followed by a list of instructions: Flatten DRC on: coder_dft, Delete MBK figure: coder_dft, Load Flatten Rules: ./techno/techno-symb.rds, Unify: coder_dft, Create Ring: coder_dft_rmg, Merge Errorfiles: Merge Error Instances: InstructionCourante: 000, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21.

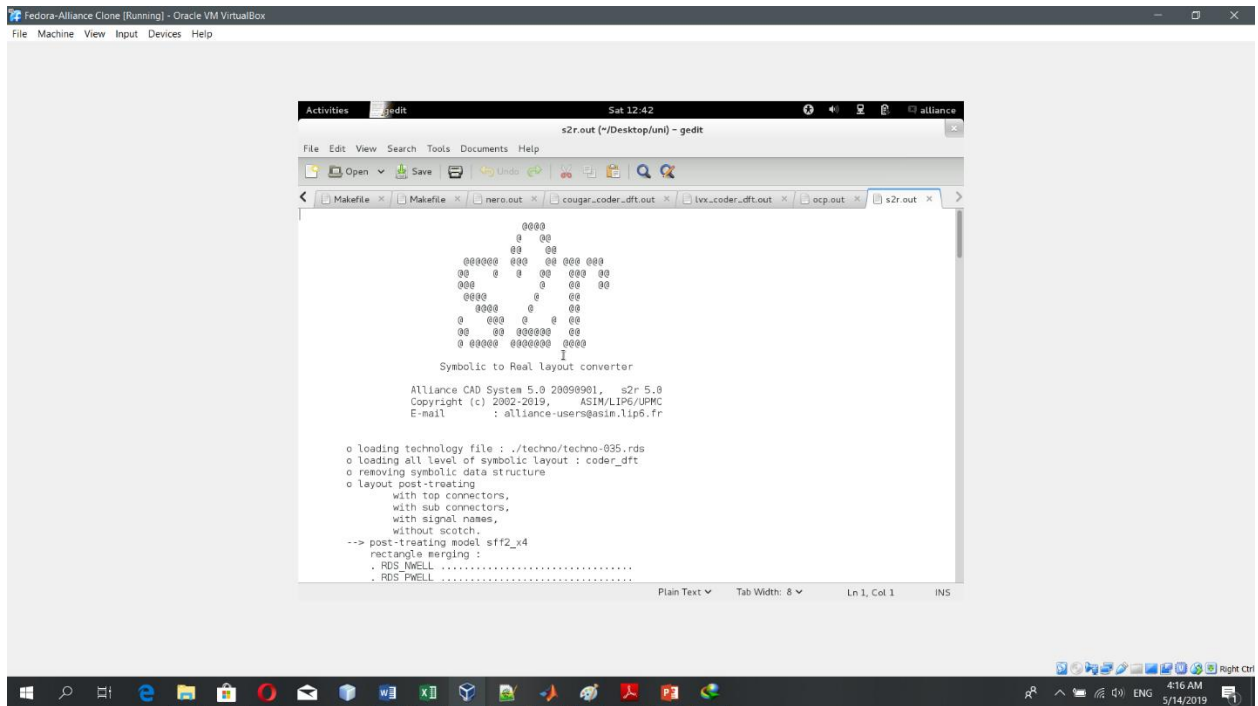
```
0000000 0000000 0000 0
00 00 00 00 00 00 00 0
00 00 00 00 00 0000 00 0
00 00 00 00 00 0000 00 0
00 00 00000 00 00 00
00 00 00 00 00 00 00
00 00 00 00 00 00 0
00 00 00 00 00 00 00
0000000 00000 000 0000 00 0000

Design Rule Checker
Alliance CAD System 5.0 20090901, druc 5.0
Copyright (c) 1993-2019, ASIM/LIP6/UPMC
E-mail : alliance-users@asim.lip6.fr

Flatten DRC on: coder_dft
Delete MBK figure: coder_dft
Load Flatten Rules: ./techno/techno-symb.rds

Unify: coder_dft
Create Ring: coder_dft_rmg
Merge Errorfiles:
Merge Error Instances:
InstructionCourante: 000 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21
```

S2R output file:



The screenshot shows a Fedora VM window titled "Fedora-Alliance Clone [Running] - Oracle VM VirtualBox". Inside the VM, a gedit editor window is open, displaying the contents of the file "s2r.out (~/.Desktop/unl) - gedit". The editor window has a menu bar (File, Edit, View, Search, Tools, Documents, Help) and a toolbar with icons for Open, Save, Undo, Redo, and other functions. The file list at the bottom of the editor shows several files: Makefile, Makefile, nero.out, cougar_coder_dft.out, lxx_coder_dft.out, ocp.out, and s2r.out. The main text area of the editor contains the following content:

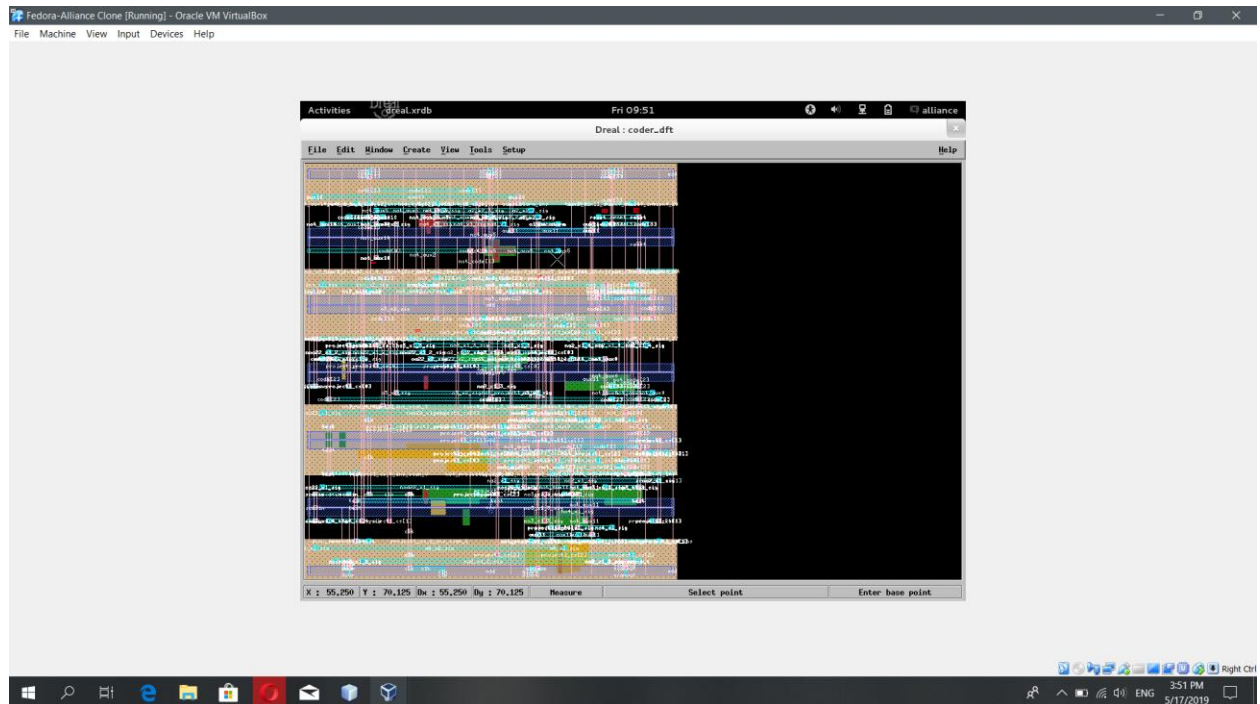
```
Symbolic to Real layout converter

Alliance CAD System 5.0 20090901, s2r 5.0
Copyright (c) 2002-2019, ASIM/LIP6/UPMC
E-mail : alliance-users@asim.lip6.fr

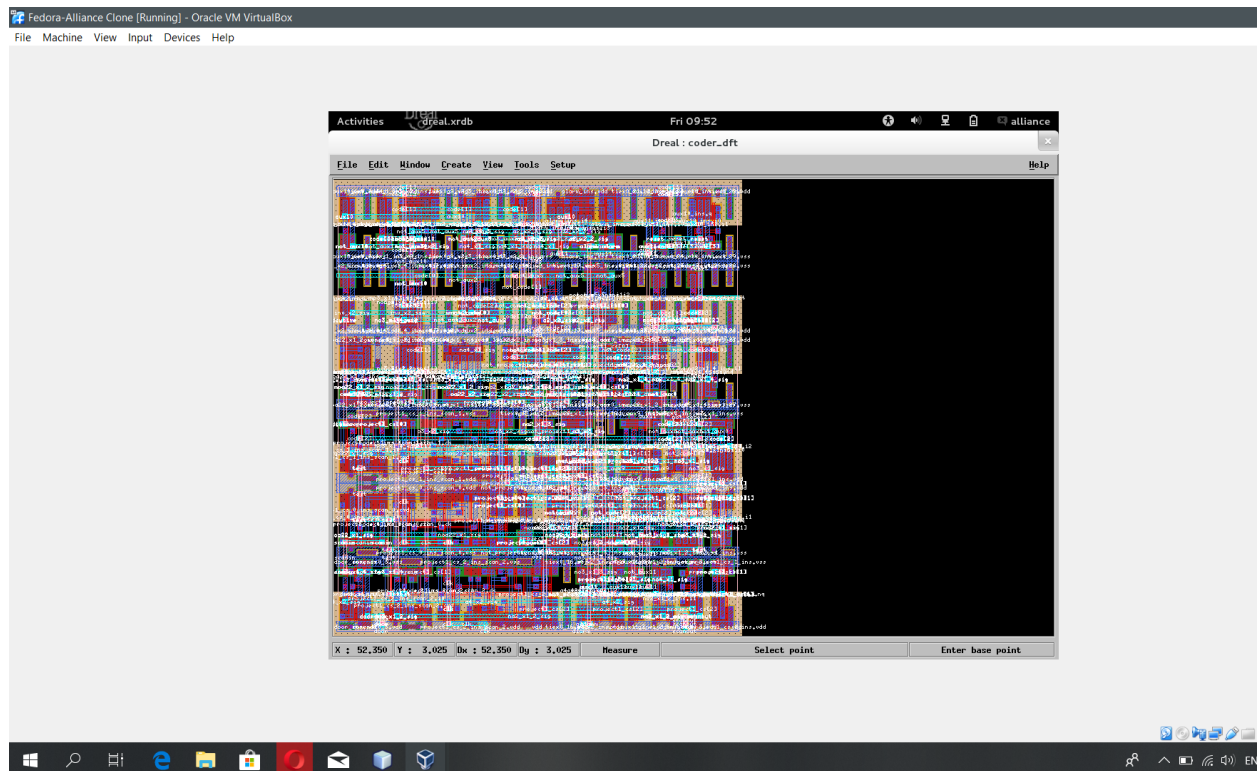
o loading technology file : ./techno/techno-035.rds
o loading all level of symbolic layout : coder_dft
o removing symbolic data structure
o layout post-treating
  with top connectors,
  with sub connectors,
  with signal names,
  without scotch.
--> post-treating model sff2_x4
  rectangle merging :
    . RDS IWELL .....
    . RDS PWELL .....
```

The status bar at the bottom of the editor window shows "Plain Text", "Tab Width: 8", "Ln 1, Col 1", and "INS". The taskbar at the bottom of the VM window shows various application icons and the system clock indicating 4:16 AM on 5/14/2019.

Dreal output:



Flatted:



Pin file:

LEFT(# IOs from bottom to top

(IOPIN scanin.0);

(IOPIN daytime.0);

(IOPIN code(0).0);)

TOP (# IOs from left to right

(IOPIN code(1).0);

(IOPIN code(2).0);

(IOPIN code(3).0);)

RIGHT(# IOs from bottom to top

(IOPIN test.0);

(IOPIN reset.0);)

BOTTOM (# IOs from left to right

(IOPIN door.0);

(IOPIN clk.0);

(IOPIN alarm.0);

(IOPIN scanout.0);)