REGISTER ALLOCATION

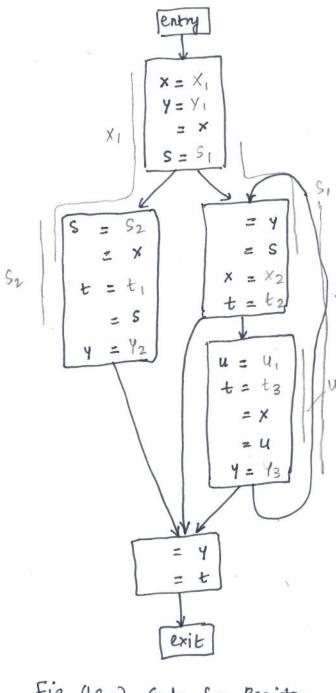


Fig (1a.) Code for Register

Allocation Question

Marged Live Ranges:

 x_1, x_2 y {41,42,43} // equivalence 51,52 t Stirtz, t33 // equivalence

class

Fig (16.) Interference Graph

edors = {A, B, C, D}

5-clique: {y, 22, S1, t, u3

(B XI	Ç	2) D	
		DA.		
CE			(52)	C
lline	(t)B	70	Spill	

Post	Nodes	Edges	Depun
10' × 5	Ain .	S1, S2, 21, 22, t,	u []
0	1	S1, S2, y [[x2	, t, u]
10'x 2	1	1 100	, a
4	31 :	21, 22, 8, 6, 4	6723
5	S2 :	$x_1, y, u, t \mid L^2$ x_1, x_2, y, t, u $x_1, y, t \mid L^2$	S1, 22,4]
10'x A	t:	S1, S2, 4, 2,4 E	x, 7
10'y =	2 u:	S1, 22, y, t [[2	1,52)
and and	7		and the same of th