

M'crofor cessor

- 2) Microferocessor fi Data and Perogram å tode star Et Hennory registers Eld El
  - 3) Memory, Infent/Output and Microprocessor at Hay Data Transfer at M2 312 And Pager 212
    - 4) Microfrocessor Fi Access time अधिक होता ही

M'cro controller

1) Microfrocessor 27 forgrammable 1) UE single Chip Microcomputer chip Etal & GRET CPU on Tational Etal & GRET ART specific on THE THE FIRST SPECIFIC ON THE THE FIRST SPECIFIC ON THE COURT OF THE FIRST STATE OF THE COURT OF TH

- 2) Microcontroller fi Data and Program & Mer STAIT STAIT Memory Elds E's
- 3) LUN Data transfer an time रक या में ही निर्देश हीते ही
- 4) Microcontroller fi Access
  time comparitively and start
- Microforcesson Fi at That Pins & Microcontraller Fi 31/21/45 Pin Multifunction Eta Ei

8051:- (Specifications)

TE CHMOS technique 42 based Eldi &

> 8051, 0-24 MMz dat of frequency to the 3425ad 227 21

-> 6051, h 2, 16 bit Timer / Counter, 32 I/o lines and 4

8 bit bidirectional Port (Po-Ps) Eld E)

3 21h 64KB Data and 64KB Program memory to the external space of 34mour ETATES

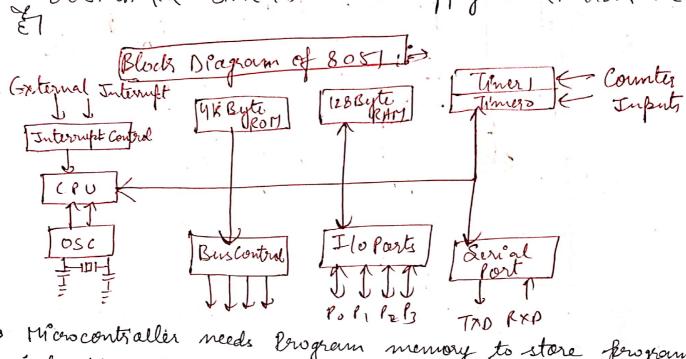
-> Seit Data storage & The ME Register Bomk Eld E' Total Hyan Bomk ti 3116 Registers Eld E)

> 8051 h Serial data communication Eld & Gran Par

JAN (that Duplex serial data receiver / Transmitter)

Pulse Width Modulation, DMA channel and H/D converter etc. Mi'trocontroller 8057 to specific function an Earl'a Z';

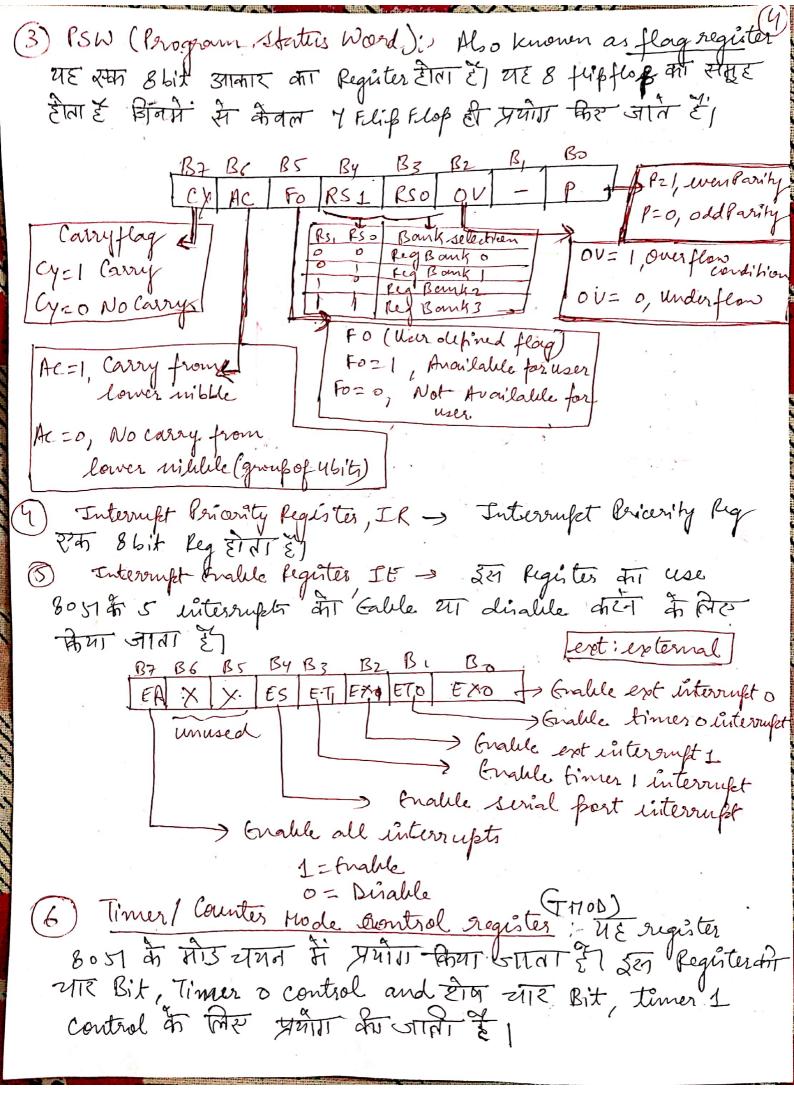
-> 8051 के लिए आवश्यक Power supply +3V हें +18Vवक होती



- M'crocontialler needs brogram memory to store foregroun instructions to perform defined task. 221 Memory 411
ROM diEd E, 8051 Fi 4k Ryte ROM (Read only Memory) ET d'El M'oro controller also requires data memory to store the operand I data on temposary basis. This is known as RAM. 80 ST Ji 128 byte RAM ( Found am Access Memory) FIFT E) Registers of 8057 mm

A Register = 5 21 Accumulator on the 2'i 27 41 Act of and Act of 2 27 41 Act of a ALU GIET Cope of all ABRO operations like Addition, Multiplication, Division, substraction etc at result at store that I

Disregister - Generally, Bregister AT HATT Data store 420 J. that state that at Aright 2012 Multiply & Divide State operation fight from State of the



Data Pointer legister, DPTR 30 16 bit register & fectors 5211 Hill in 16 bit address in Hold and in the fater of the fater of the fighter of the segister of the mory in lower address and higher other coldress in according 8-8 bits of DPL and DPH bregister fi Divide at 2 ft of use taken 511 at DPL-3 Data Pointer low DPH-3 Data Pointer ligh.

(8) Program Counter -> This is a 16 bit register, and 5111 micro controller in the execution that it that of the part of the segister o