

# Wheatstone bridge coffeemaker



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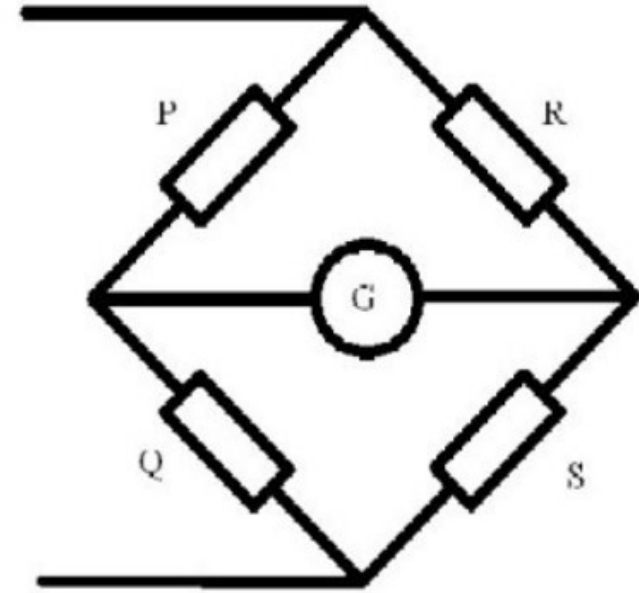
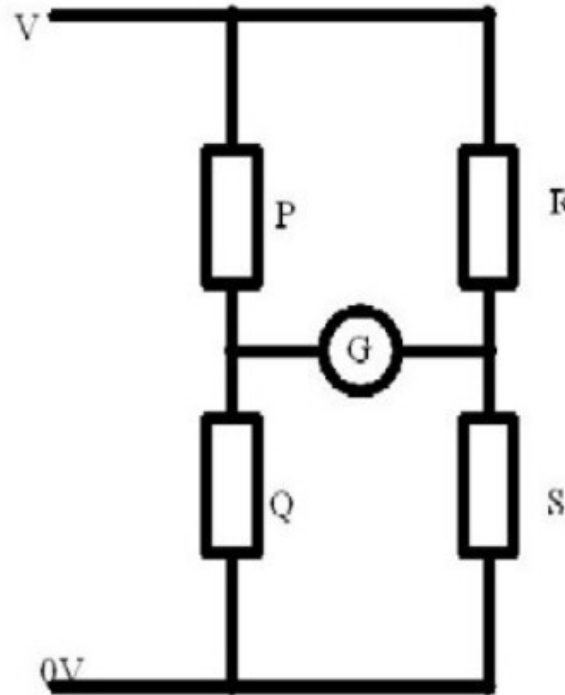


# Agenda

- Wheatstone bridge
- Opamp Component
- Opamp Comparator
- Hysteresis

# Principle

A Wheatstone Bridge consists of **two voltage-dividers**.



At **balance** there is no current that flows through a Galvanometer G ( G = a sensitive ampere meter).

# Working

Resistor  $R_x$  is connected as the fourth side of the circuit

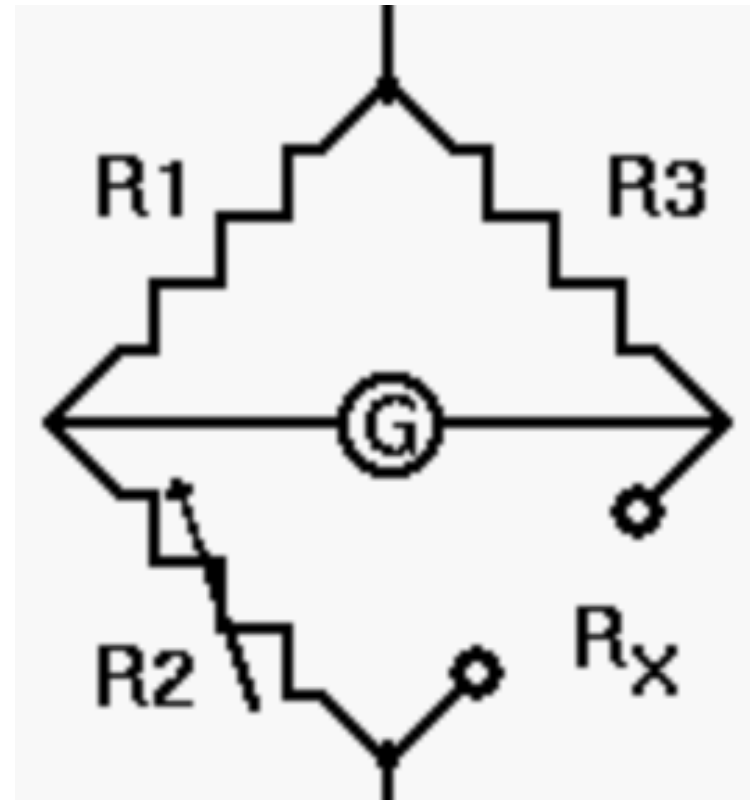
$R_2$  is adjusted until the galvanometer  $G$  reads **zero**.

At this point,

$$R_x/R_2 = R_3/R_1$$

or

$$R_x = R_2 \times R_3 / R_1.$$

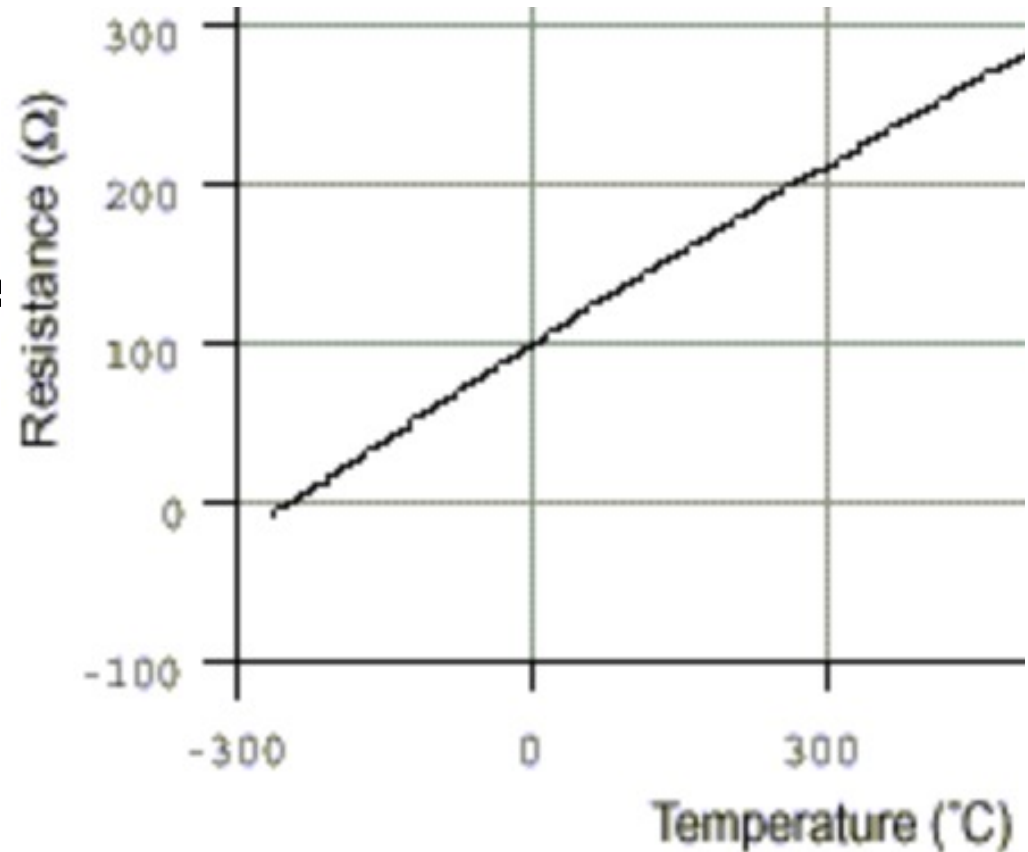


# PT-100

## Platinum resistance temperature detect

Pt-100

typical resistance  
of 100  $\Omega$  at 0°C



Resistance increases linear  
when temperature is increasing

# Sensitivity

$R_2 = PT-100 = 100 \text{ Ohm}$

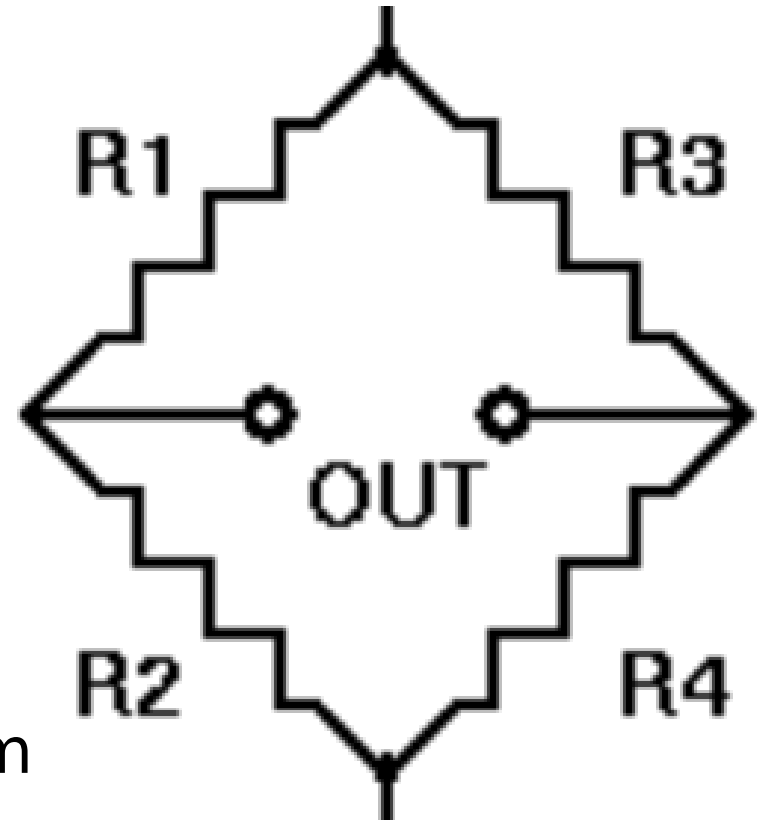
$R_1 = R_3 = R_4 = R_x$

Simulate with

$R_x = 1 \text{ Ohm}$ ,  $R_x = 10 \text{ Ohm}$

$R_x = 100 \text{ Ohm}$ ,  $R_x = 1k \text{ Ohm}$

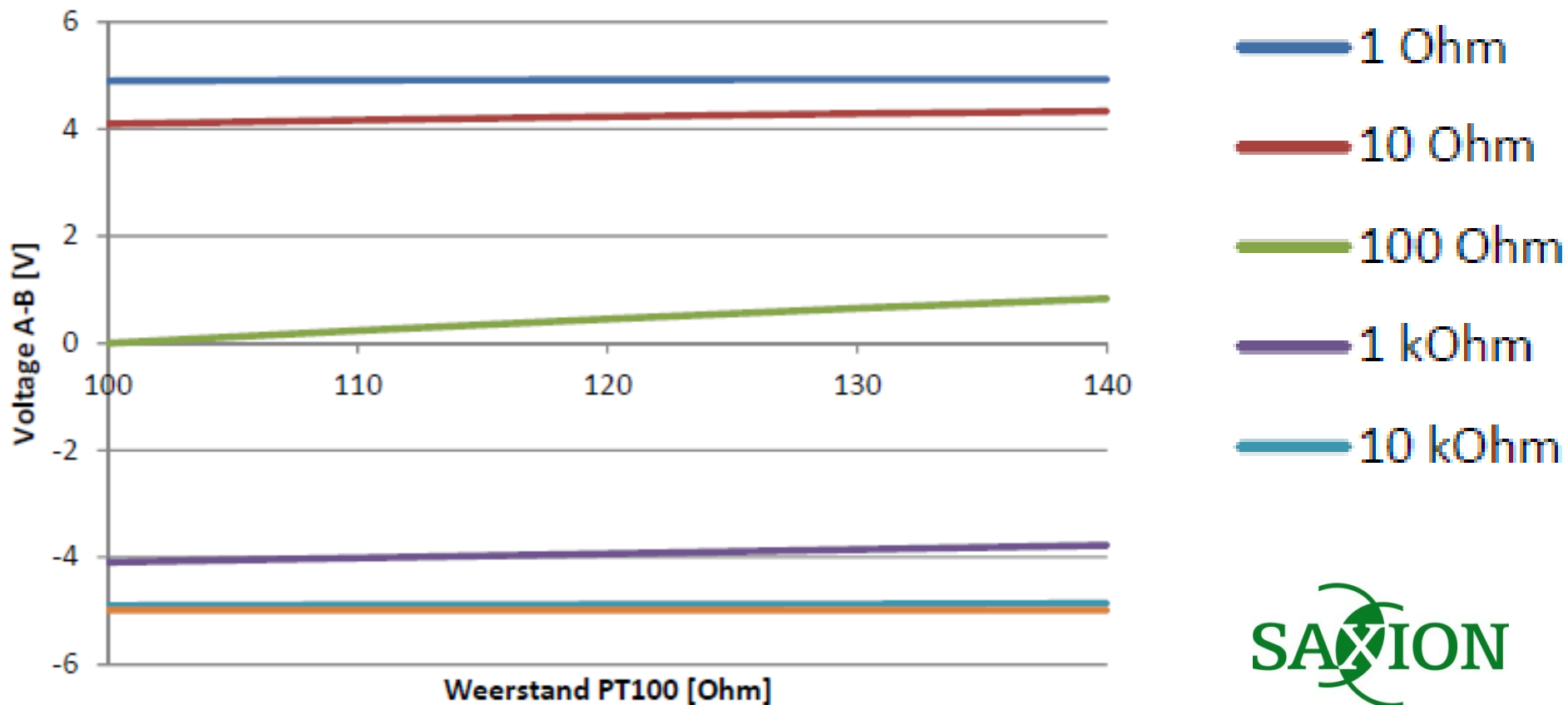
$R_x = 10 \text{ K Ohm}$



Find the resistor  $R_x$  for maximum sensitivity in  $U_{out}$  signal, conclusion?

# Simulation graph

Bridge Voltage = f (resistance PT-100)



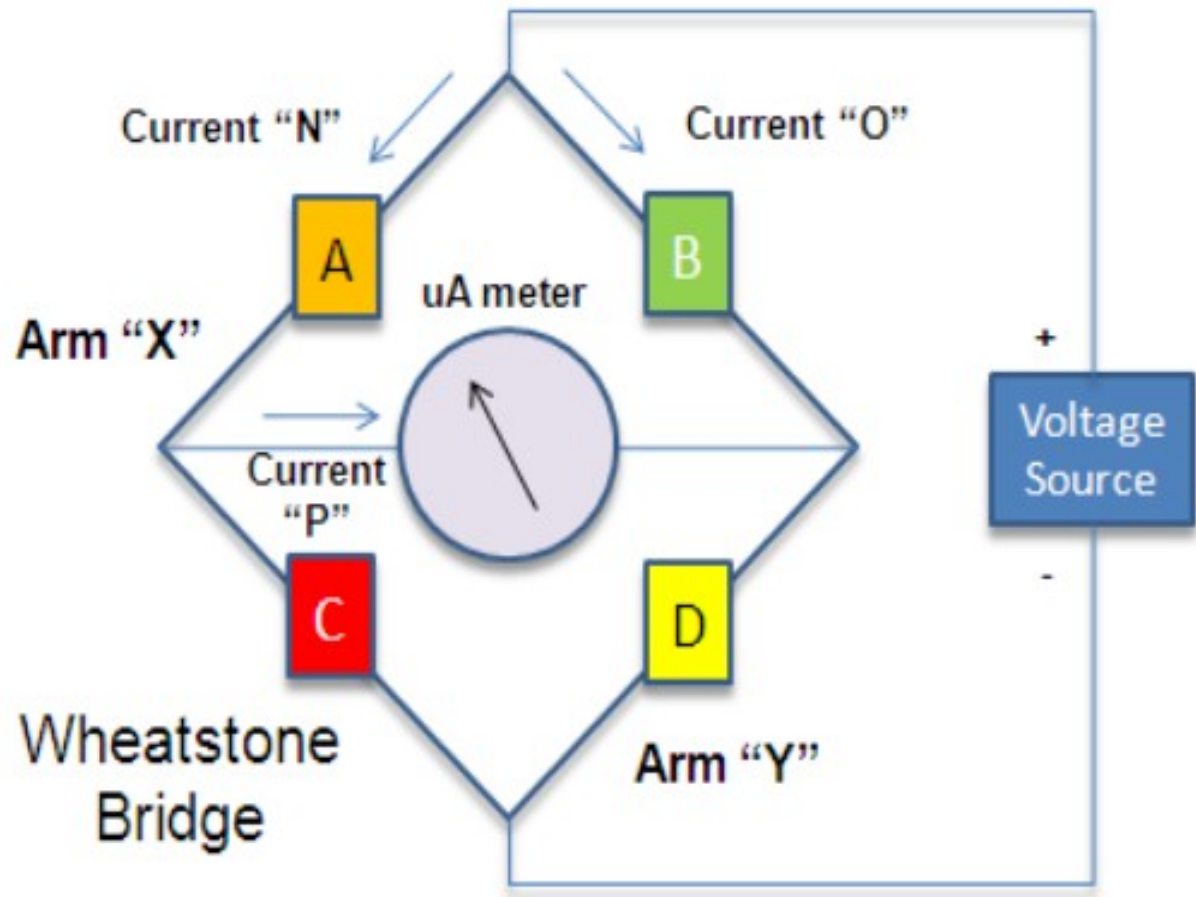
# Balance idea for coffeemaker

2 arms;

One for Reference

One for Measuring

**In balance  $A.D = B.C$**





# Opamp Component coffeemaker



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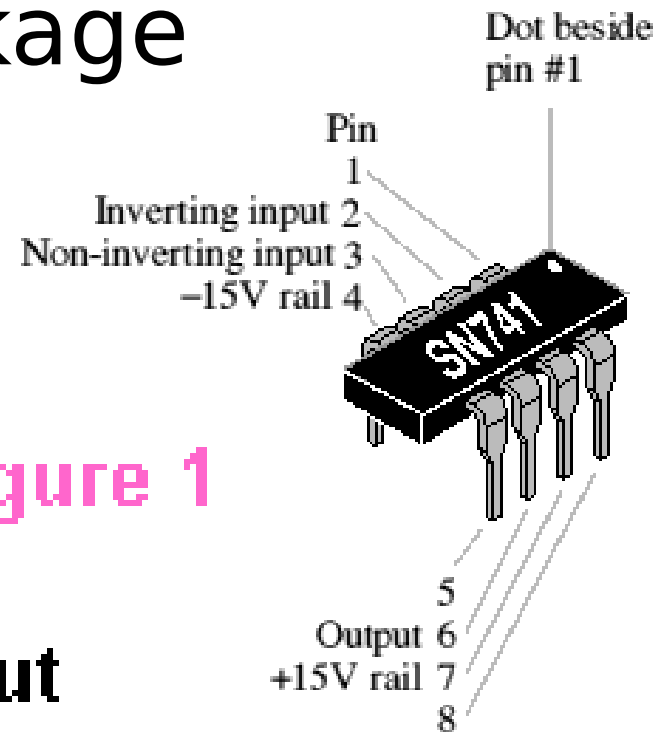
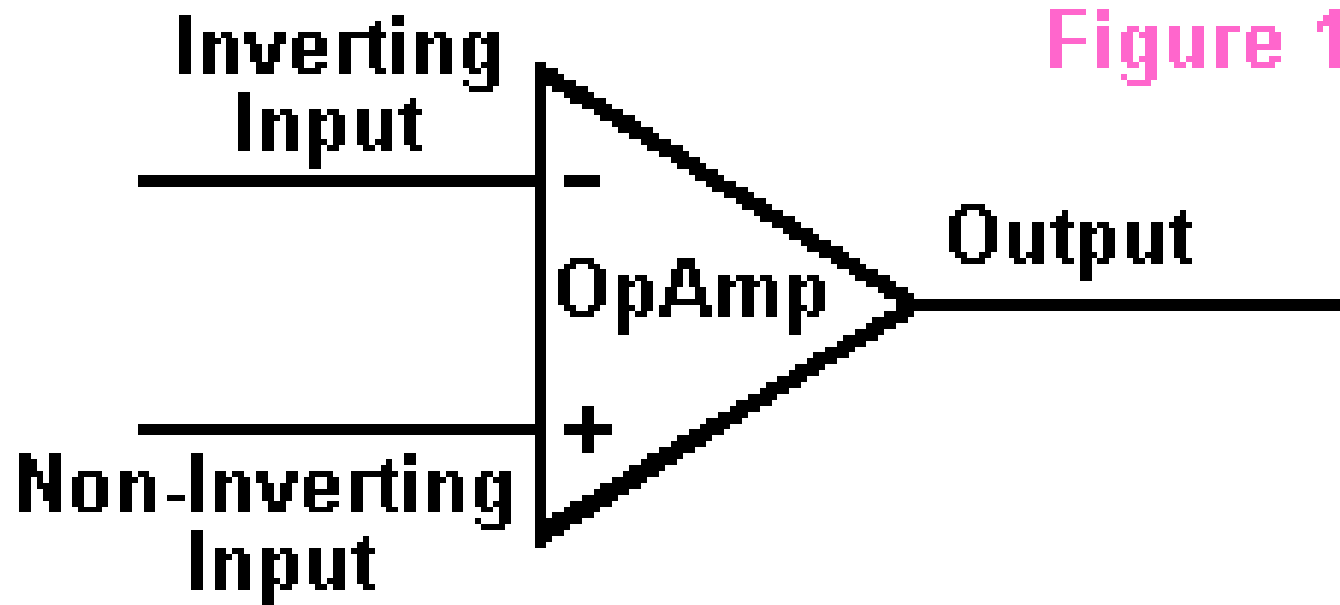
# OPerational **AMPlifier** **OPAMP** as **Comparator**

How does an OpAmp work open loop?

What is compare?

What is hysteresis?

# OPAMP, symbol and package



# OPAMP, connections

**+V = V<sub>cc</sub>**

= *most positive*  
power supply

**-V = V<sub>ee</sub>**

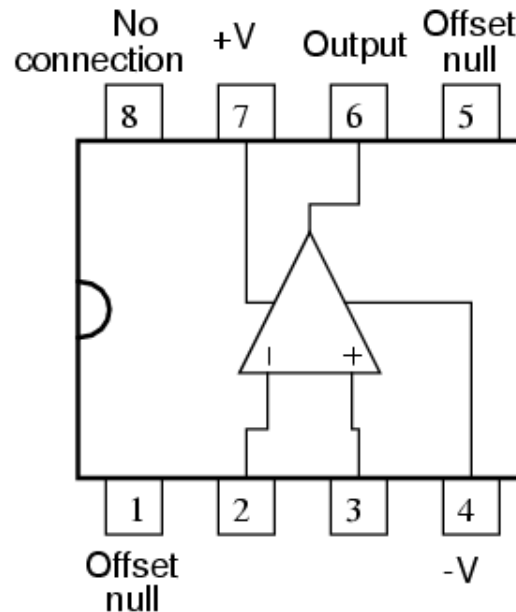
= *most negative*  
power supply

**V<sub>+</sub>** = non-inverting input

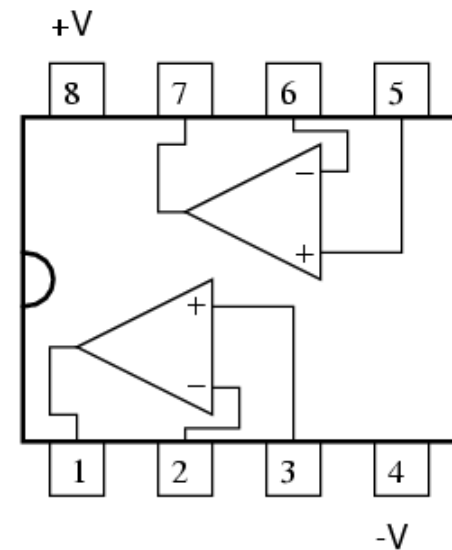
**V<sub>-</sub>** = inverting input

**Output** = output

Typical 8-pin "DIP" op-amp  
integrated circuit



Dual op-amp in 8-pin DIP



# OPAMP, internal

**+V = Vcc**

**-V = Vee**

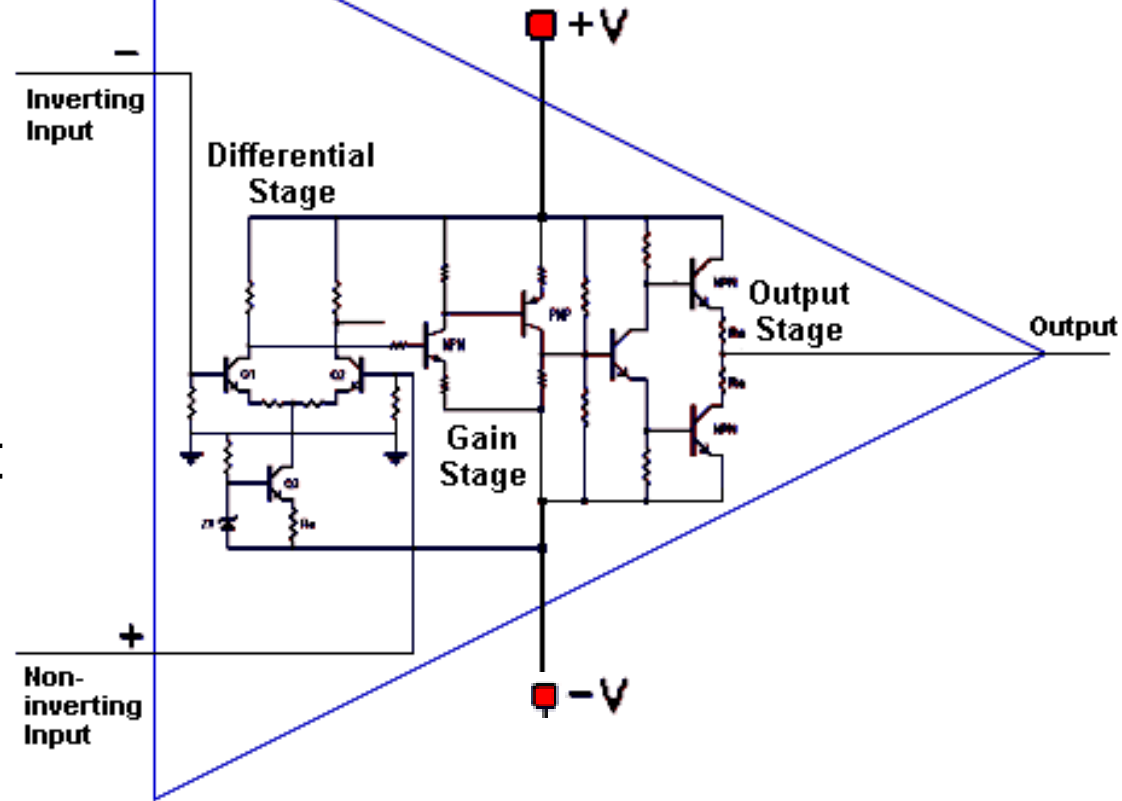
**V+** =

non-inverting input

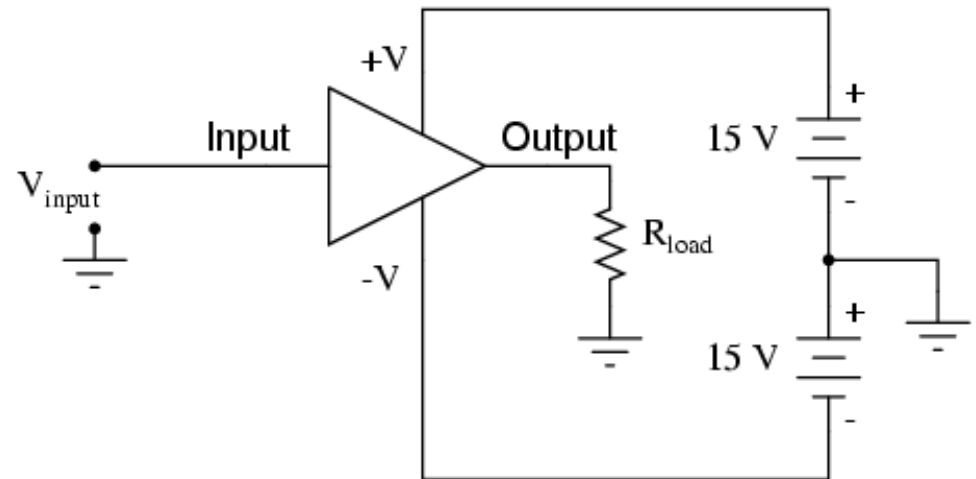
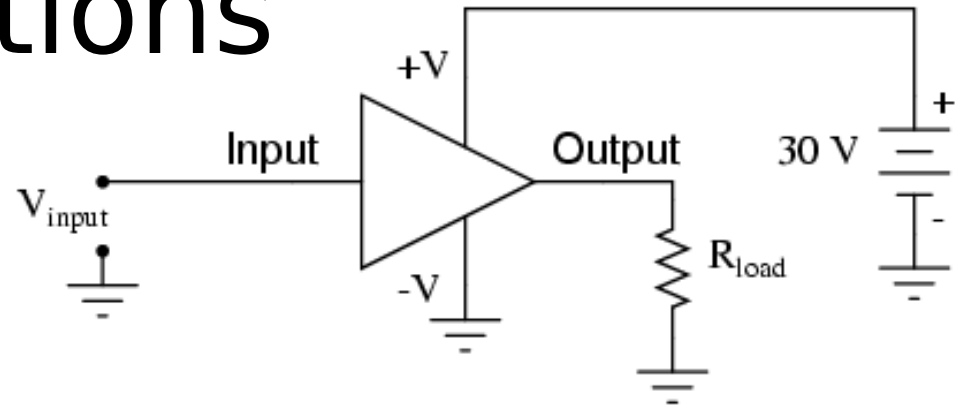
**V-** =

inverting input

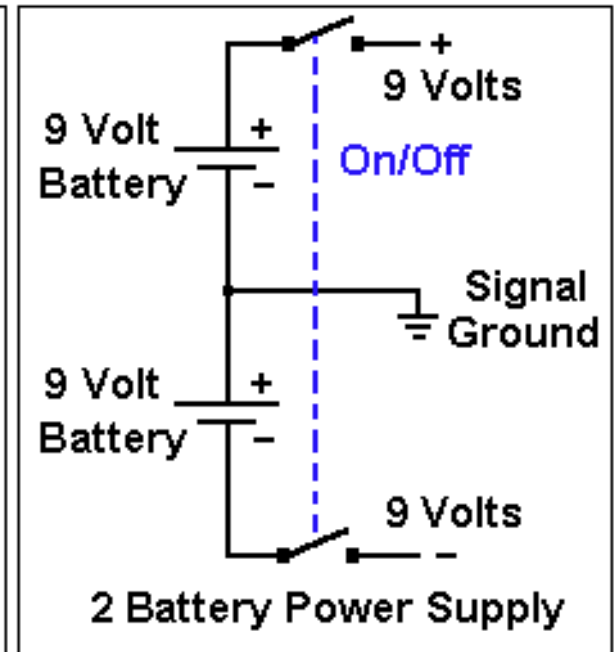
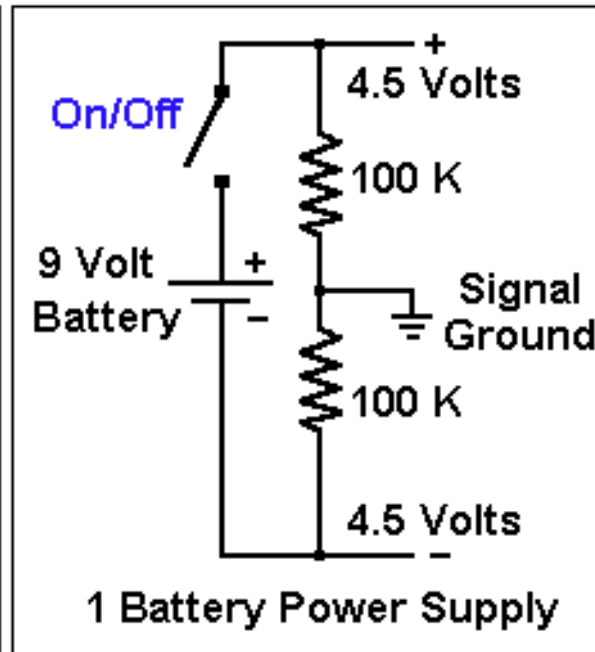
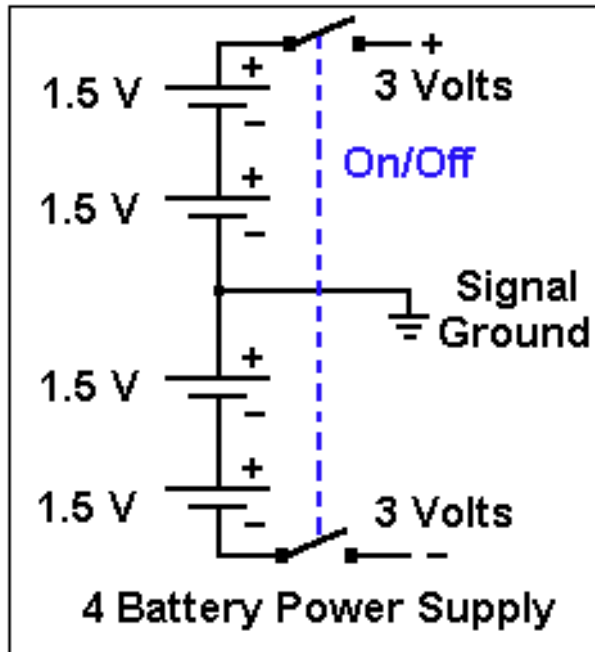
**Output** = output



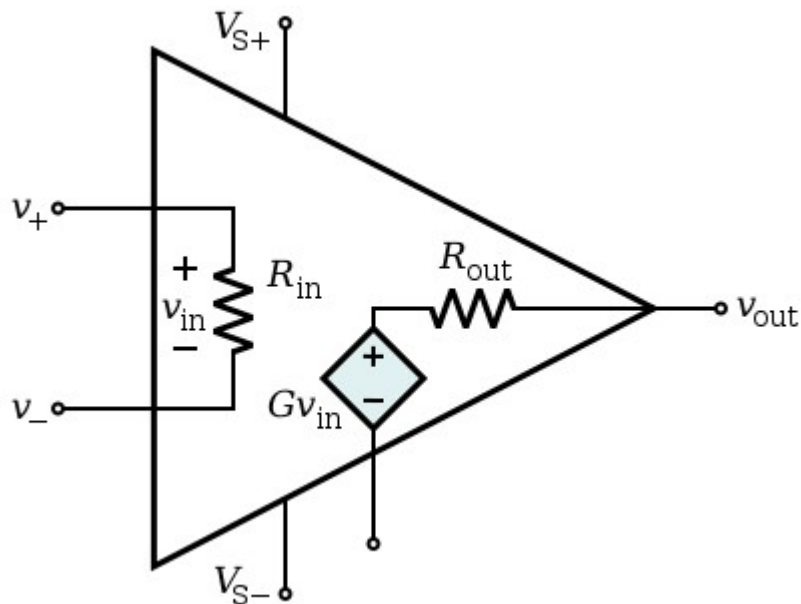
# Power connections



# OPAMP, $V_{cc}$ and $V_{ee}$ , examples



# OPAMP, remember **GOLDEN RULES**



- 1) The input impedance is infinite - i.e. no current ever flows into either input of the op-amp.
- 2) The output impedance is zero - i.e. the op-amp can drive any load impedance to any voltage.
- 3) The open-loop gain ( $A$ ) is infinite.
- 4) The bandwidth is infinite.
- 5) The output voltage is zero when the input voltage difference is zero.



# OPAMP, in real

## What You WANT

- 1) The input impedance is infinite - i.e. no current ever flows into either input of the op-amp.
- 2) The output impedance is zero - i.e. the op-amp can drive any load impedance to any voltage.
- 3) The open-loop gain (A) is infinite.
- 4) The bandwidth is infinite.
- 5) The output voltage is zero when the input voltage difference is zero.

## What You GET

NO, but it is often GIGA or TERA  $\Omega$ !

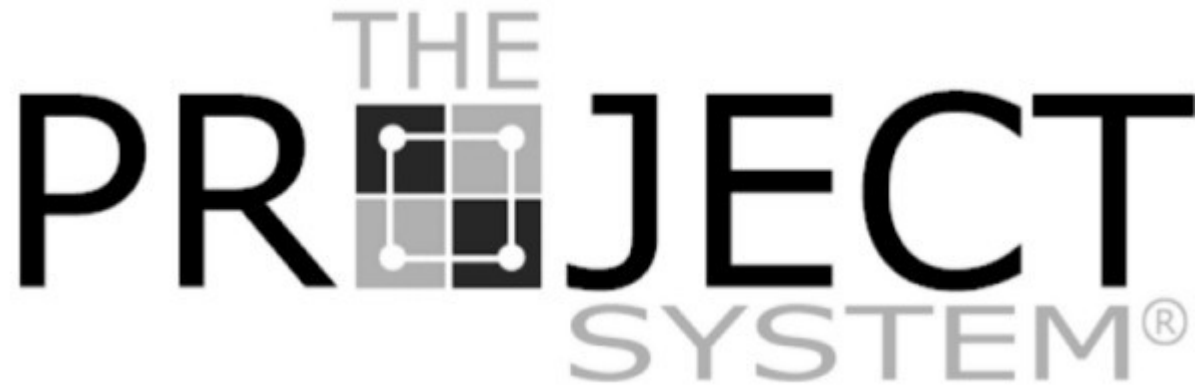
NO, but it can be a few ohms in many cases!

NO, but it is usually several million!

NO, usually several MHz.

NO, offset voltages exist, but can be trimmed.

# Opamp Comparator coffeemaker



Jan Bollen

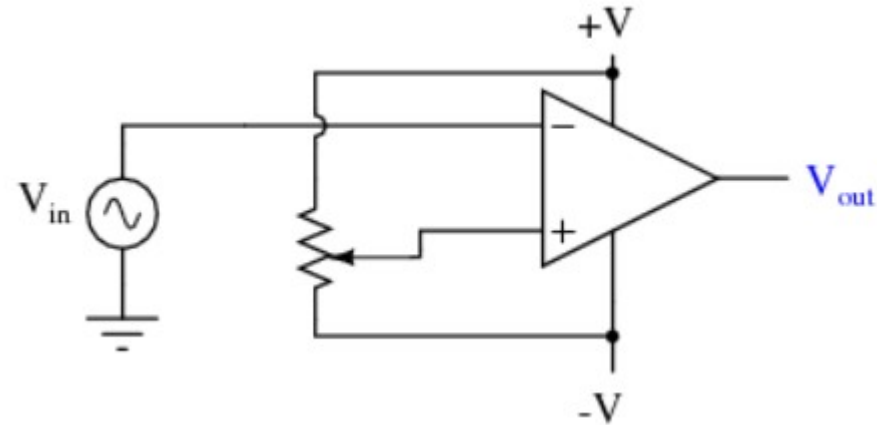
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# Comparator

**+V** = **V<sub>cc</sub>** = + 5 Volt

**-V** = **V<sub>ee</sub>** = - 5 Volt



R = voltage divider adjusted at + 1 Volt

**V<sub>+</sub>** = + 1 Volt

**V<sub>-</sub>** = sine wave of + 5 Volt top

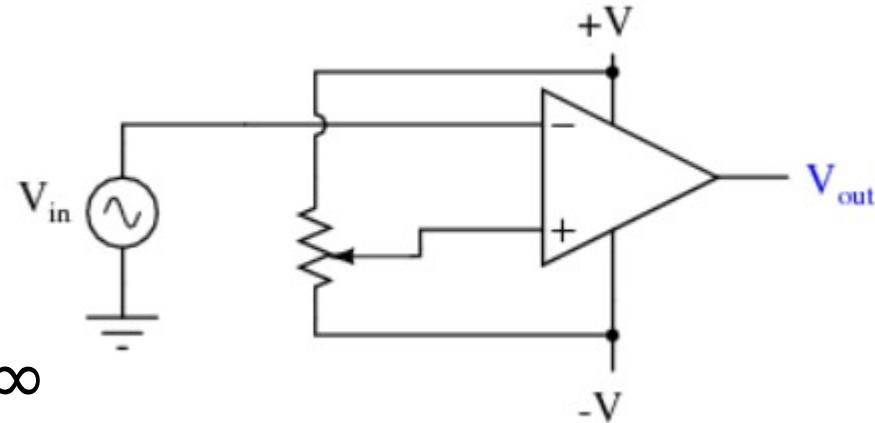
sine wave of 10 Volt top-top

Question; what will be the **output voltage?**

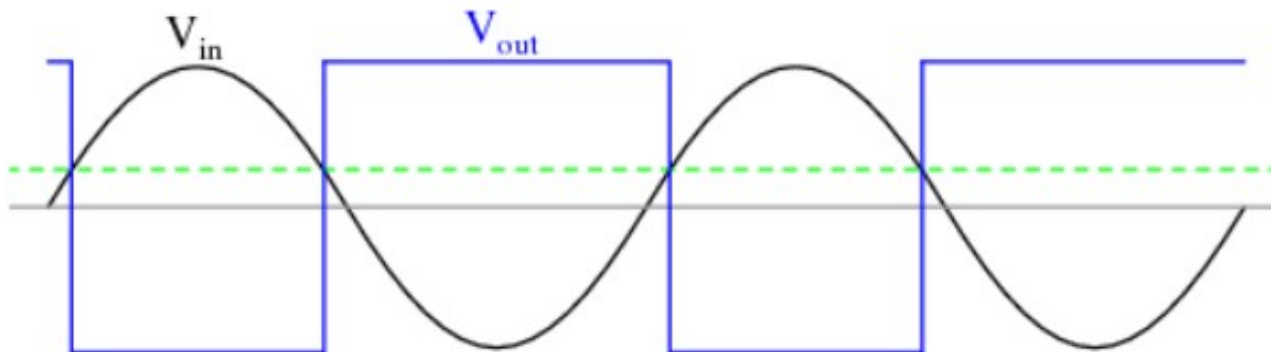
# Comparator

$$V_{out} = A (V_{+} - V_{-})$$

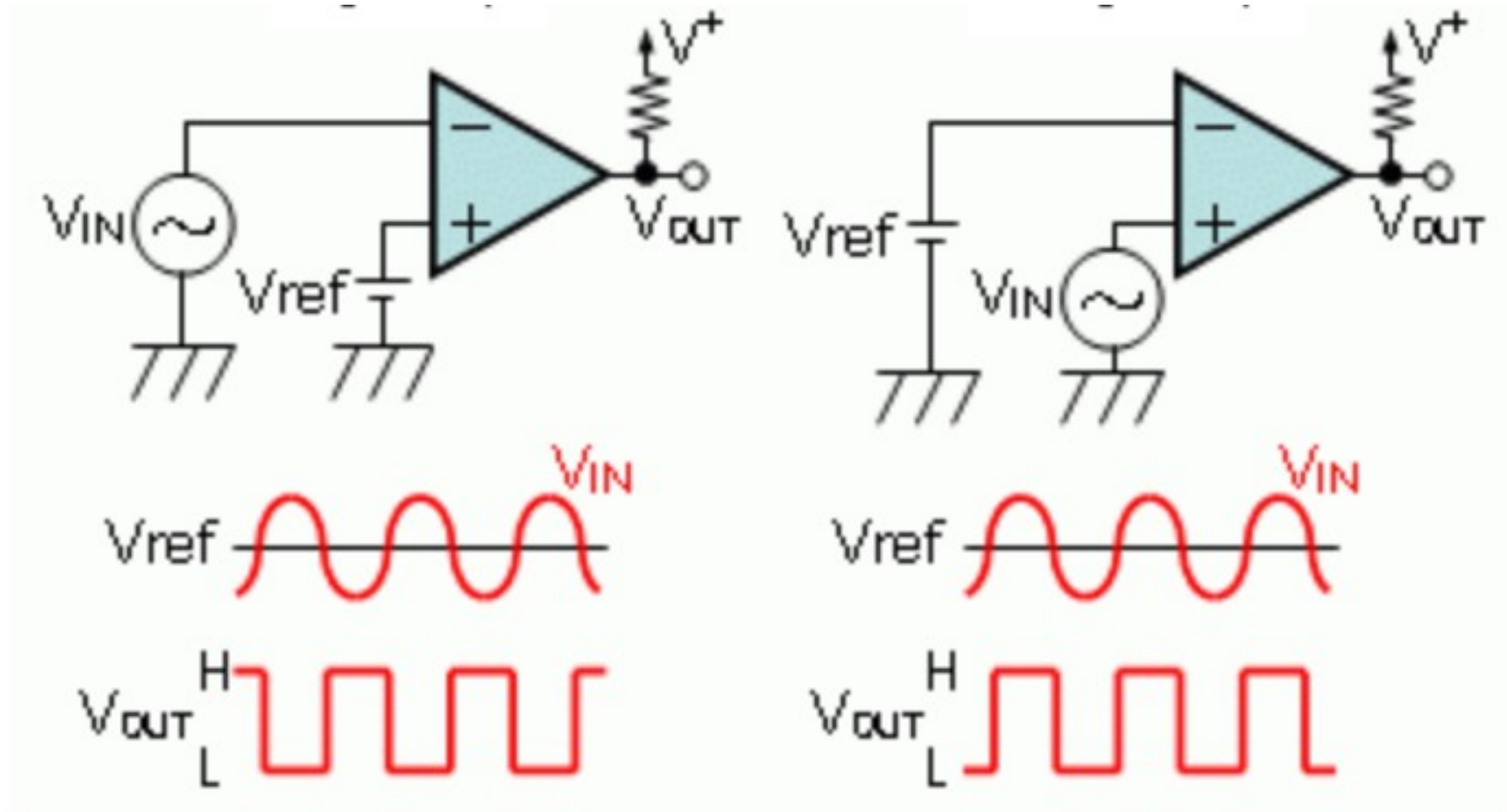
$A = \text{open-loop gain} = \infty$



Question; what will be the signals if we change the connections of  $V_{+}$  and  $V_{-}$



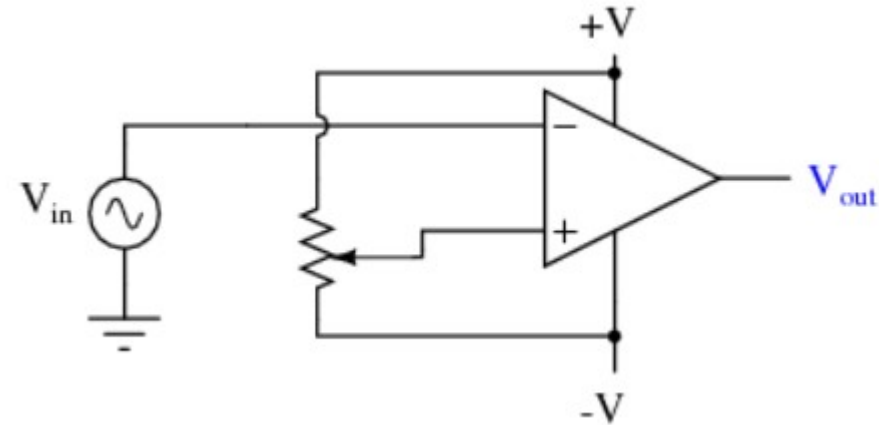
# What is the difference?



# Case

$+V = +15 \text{ Volt}$

$-V = -15 \text{ Volt}$



Question;

1 Is this an **inverting** comparator or an **non-inverting** comparator?

2 Can the output voltage be **equal to 0** (zero) volt?

3 Conclusion?

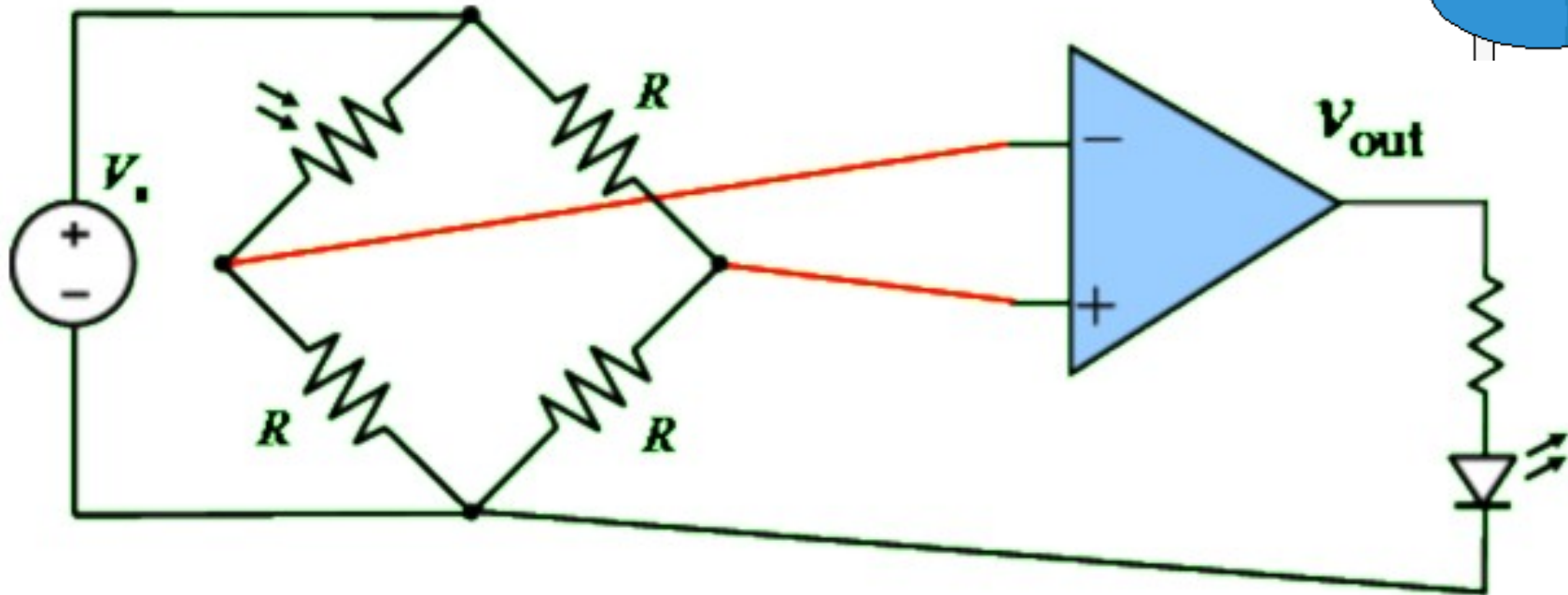
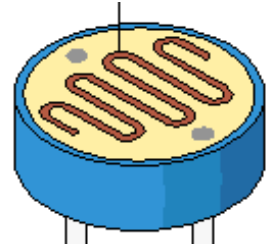
# Summary

A comparator compares 2 values, in our case

- 1     a reference value  
      (desired temperature / set point)
- 2     a measured value  
      (actual temperature)

And gives a logical output signal, indicating  
too hot (no heating, heating **off**) or  
too low (heating **on**)

# Light switch, example



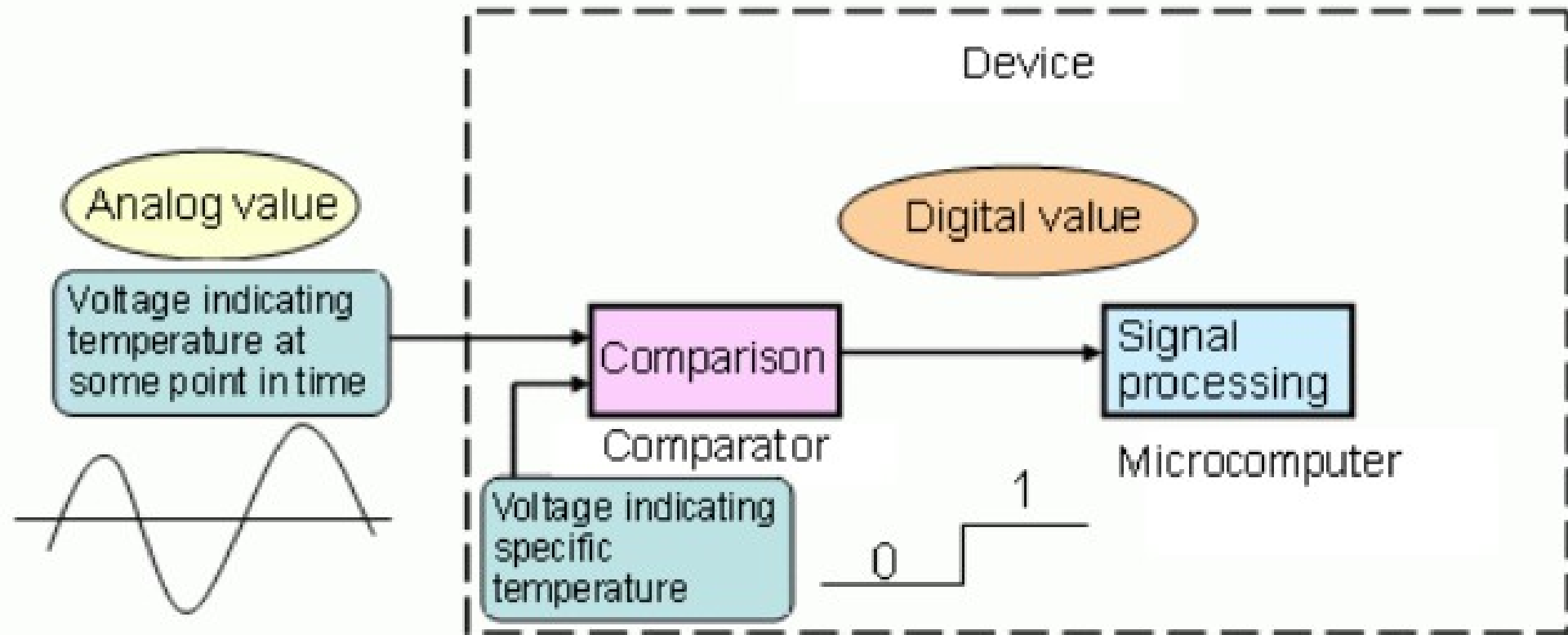
Explain how this circuit works.

If it is light is the value of LDR high or low

If it is dark is the value of LDR high or low



# Block diagram



# Opamp Hysteresis coffeemaker



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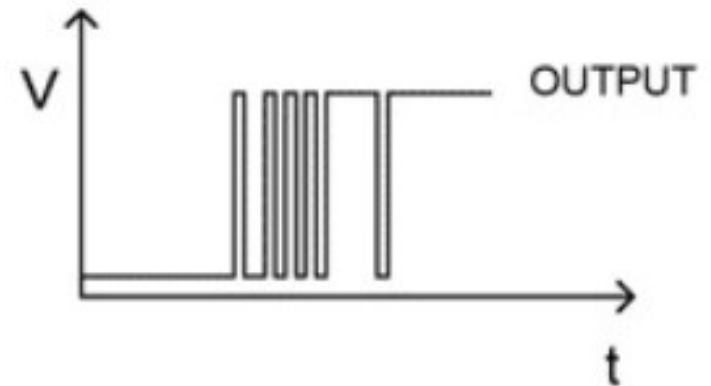
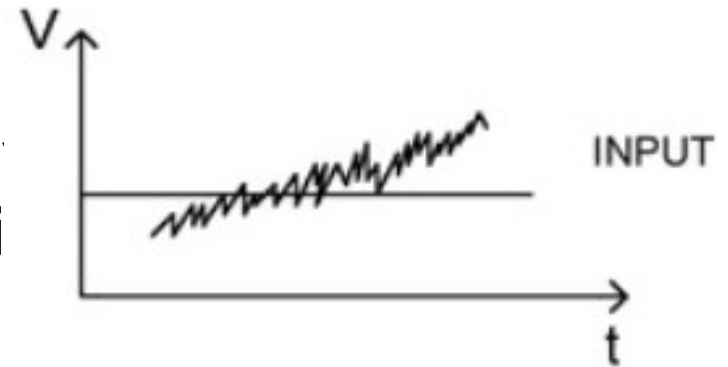


# On-Off-On-Off

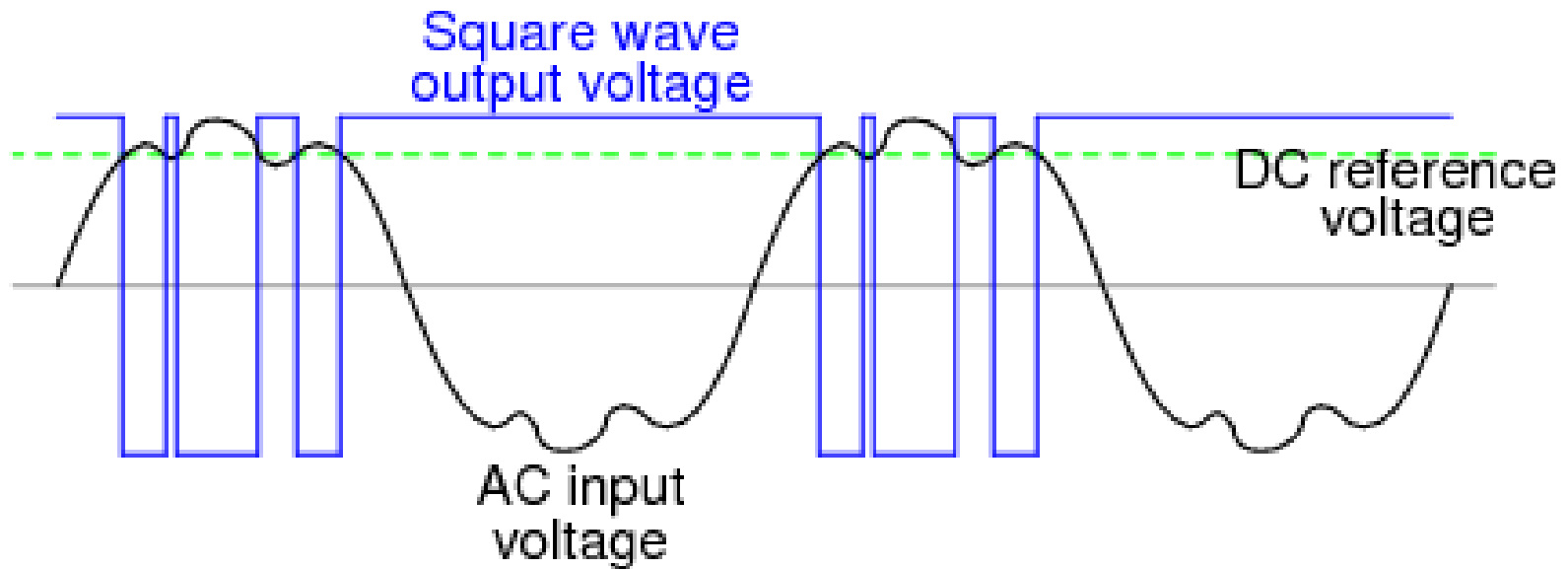
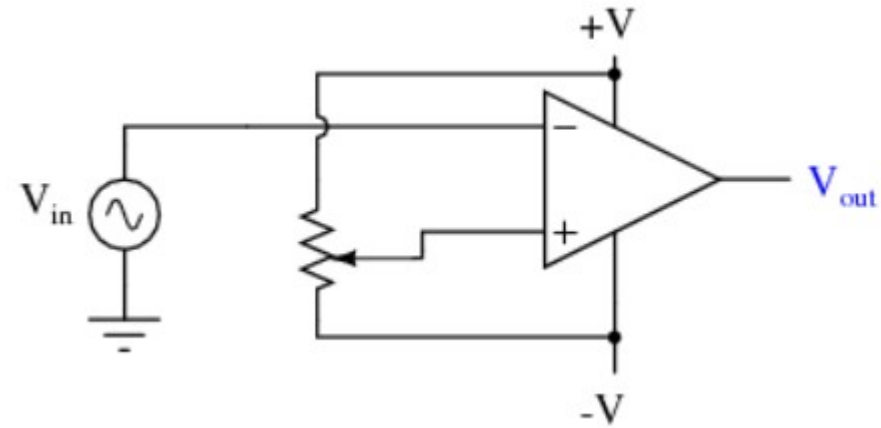
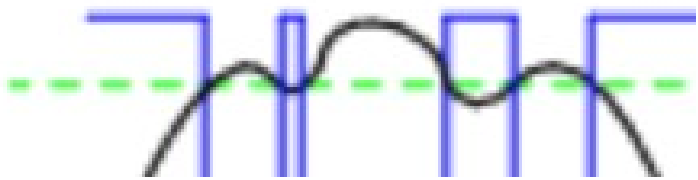
If temperature changes slightly around set point the heater will

on-off-on-off-on-off-on-off

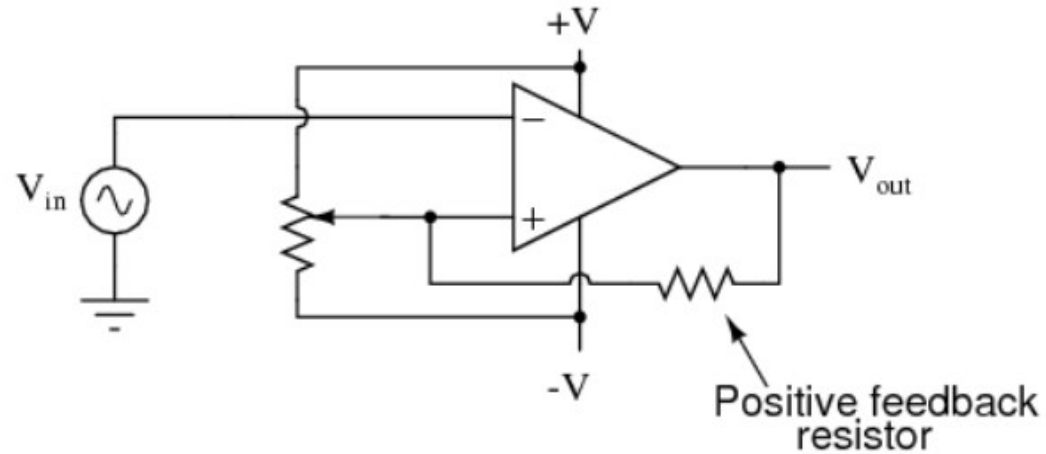
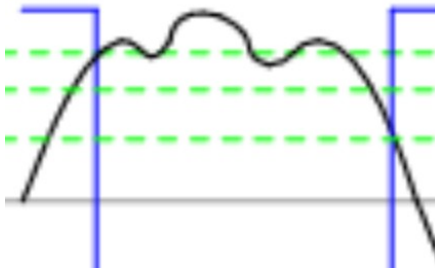
This is unwanted,  
then we use a [hysteresis](#).



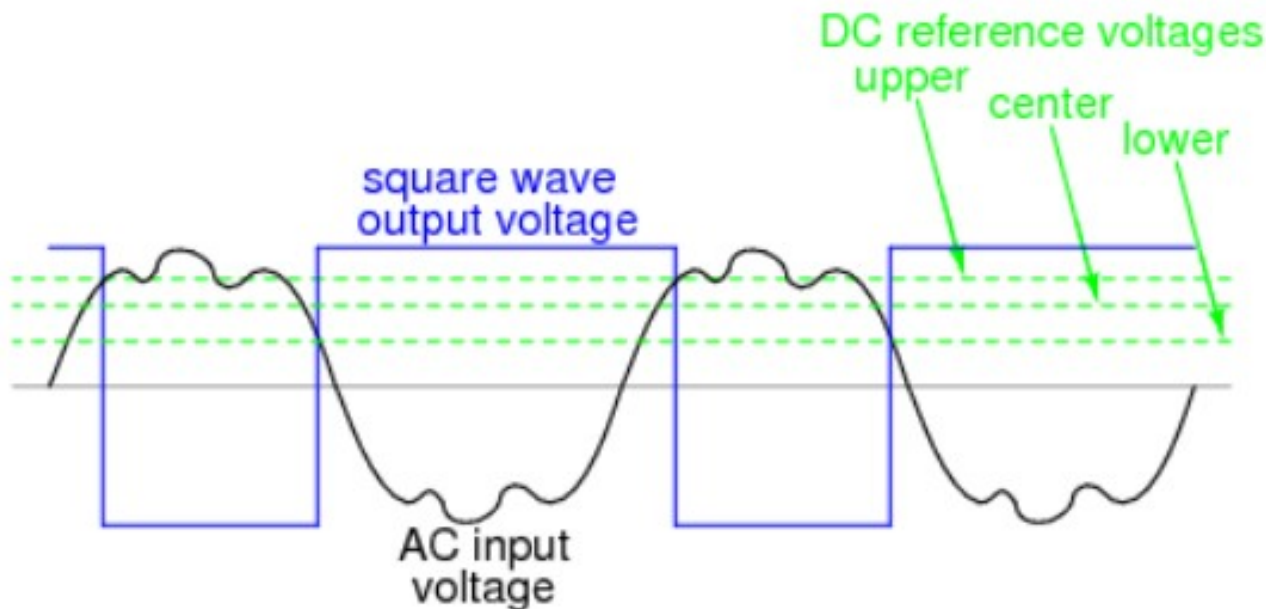
# On-Off-On-Off



# Hysteresis



**Changing reference level !!**



# Hysteresis

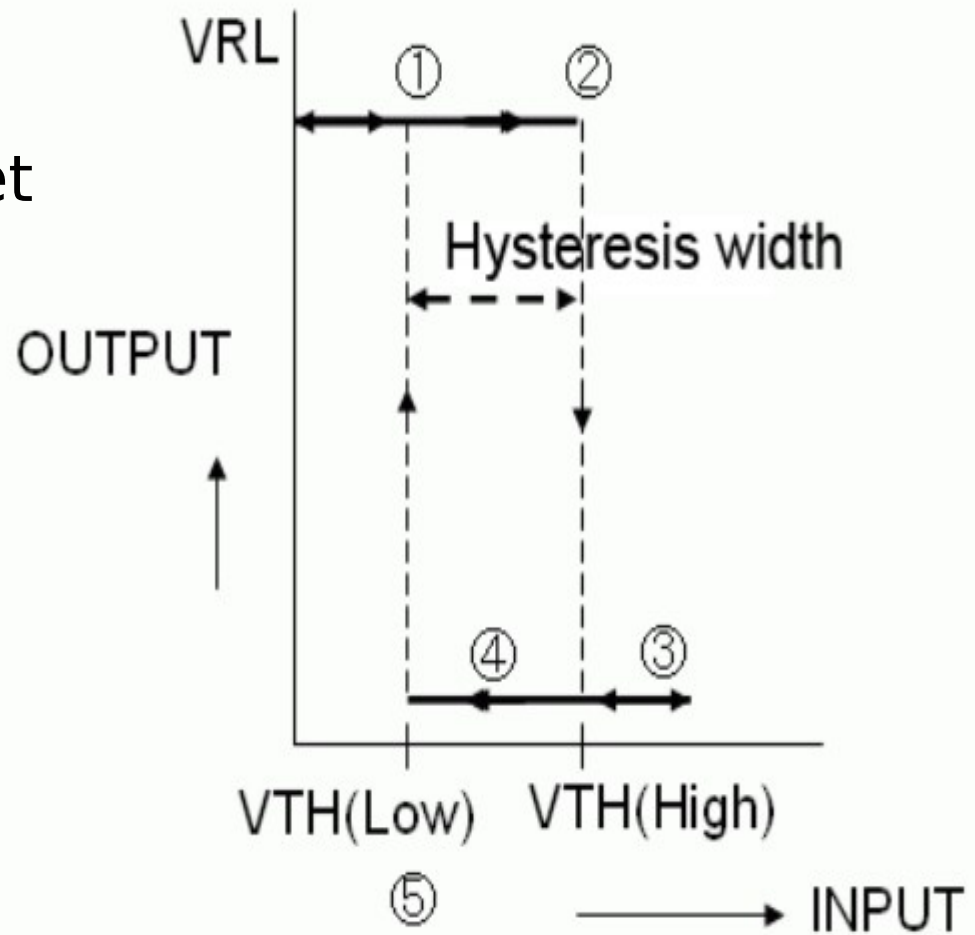
By using hysteresis we get  
2 new threshold voltages

$V_{TH\ high}$

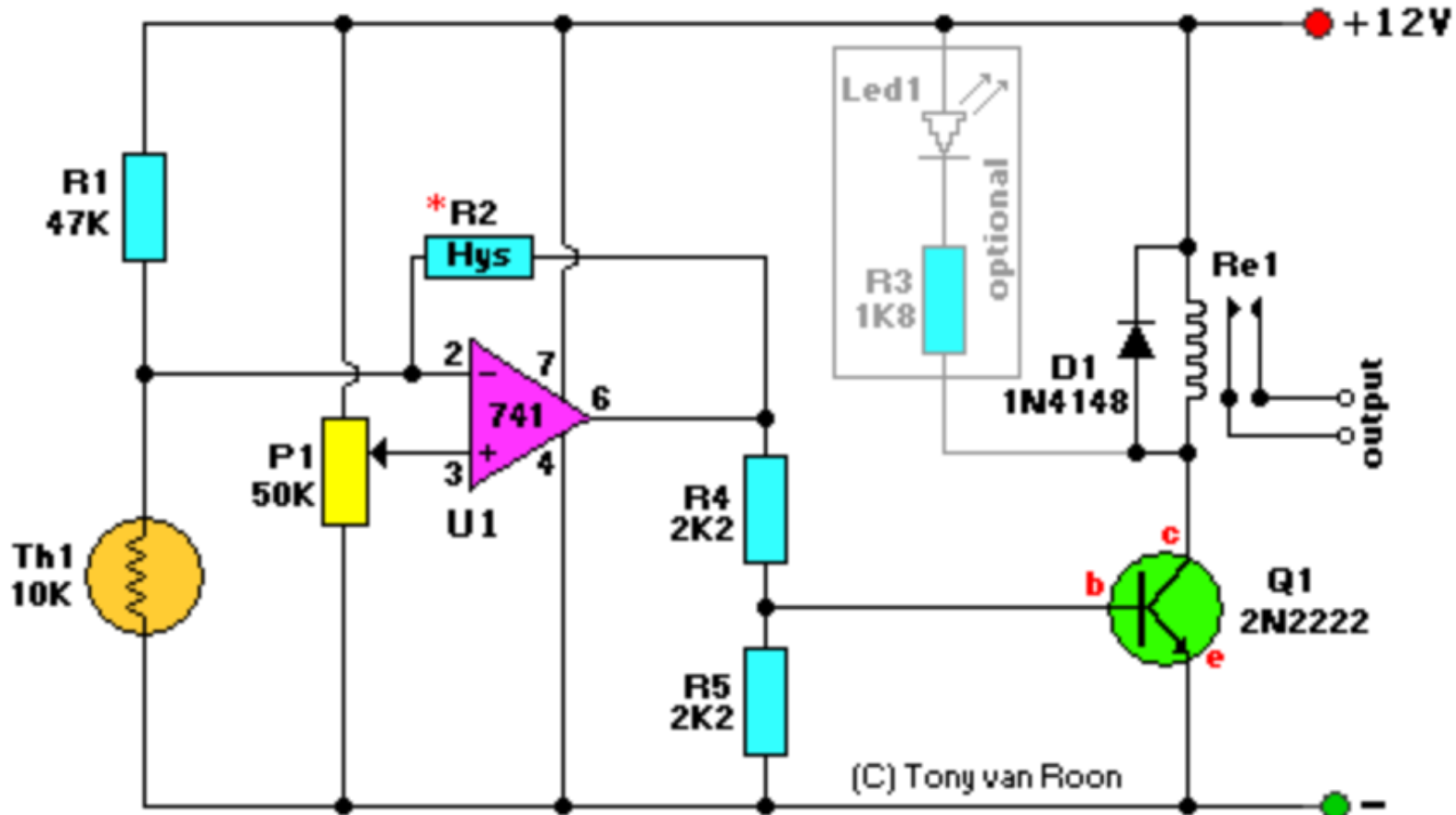
$V_{TH\ low}$

In between is the  
Hysteresis width  
or dead zone,

Noise is permitted here

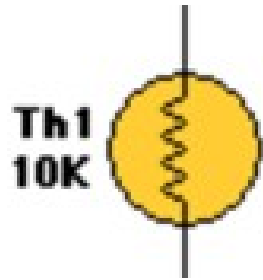


# Heat Sensor

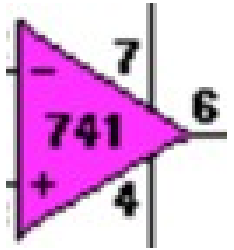


\* **R2** = Hysteresis. If relay 'chatters' make value approx. 150 - 390 K

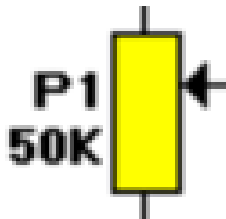
(C) Tony van Roon



The Thermistor **NTC (Negative Temperature Coefficient)** is 10K. The resistance lowers as the surrounding temperature increases which affects the output (pin 6) and energizes the small relay and Led1.



**P1** is a regular trimmer potentiometer and adjusts a certain **range of temperatures**.



**R2** is optional in case your relays tends to 'chatter' a bit. It provides **hysteresis** when the set temperature of the thermistor reaches its threshold point.

