**Jubail University College**

**Department of Computer Science & Engineering**

**SEMESTER 401**

CS 206 DIGITAL LOGIC DESIGN

LAB 7

Name : ID:

Q1 Using the simulator, implement 2 X 4 Decoder with Enable.

Q2. Using two of the 2 X 4 Decoder implemented in Q1, implement the Boolean function presented in the following table:

|  |  |  |  |
| --- | --- | --- | --- |
| x | y | z | F |
| 0 | 0 | 0 | 0 |
| 0 | 0 | 1 | 1 |
| 0 | 1 | 0 | 1 |
| 0 | 1 | 1 | 0 |
| 1 | 0 | 0 | 0 |
| 1 | 0 | 1 | 1 |
| 1 | 1 | 0 | 0 |
| 1 | 1 | 1 | 1 |