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LUCRARE DE DIPLOMĂ

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BACHELOR THESIS

Building a "Router on a chip" using
Freescale's t1040 platform

Scientific Adviser:

Prof. Magnificus Academicus

Author:

Matei Pavaluca

Bucharest, 2014

Maecenas elementum venenatis dui, sit amet
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luctus a convallis sed, lacinia sed ligula.
Integer arcu metus, lacinia vitae posuere ut,
tempor ut ante.

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Notations and Abbreviations

CS – Computer Science

UPB – University Politehnica of Bucharest

Chapter 1

Introduction

This is just a demo file. It should not be used as a sample for a thesis.

TODO:

Remove this line (this is a TODO)

1.1 Project Description

1.1.1 Project Scope

This thesis presents the **MySuperProject**.

This is an example of a footnote ¹. You can see here a reference to [Section 1.1.2](#).

Here we have defined the CS abbreviation. and the UPB abbreviation.

The main scope of this project is to qualify xLuna for use in critical systems.

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1.1.2 Project Objectives

We have now included [Figure 1.1](#).

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¹www.google.com

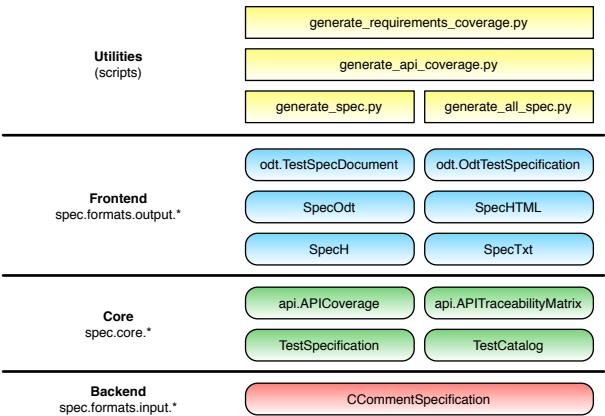


Figure 1.1: Reporting Framework

pretium. Nam quis tortor sed ante varius sodales. Etiam lacus arcu, placerat sed laoreet a, facilisis sed nunc. Nam gravida fringilla ligula, eu congue lorem feugiat eu.

We can also have citations like [1].

1.1.3 Related Work

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We are now discussing the **Ultimate answer to all knowledge**. This line is particularly important it also adds an index entry for *Ultimate answer to all knowledge*.

1.1.4 Demo listings

We can also include listings like the following:

```
1 CSRCS = app.c
2 SRC_DIR = ..
3 include $(SRC_DIR)/config/application.cfg
```

Listing 1.1: Application Makefile

Listings can also be referenced. References don't have to include chapter/table/figure numbers... so we can have hyperlinks [like this](#).

1.1.5 Tables

We can also have tables... like [Table 1.1](#).

Table 1.1: Generated reports - associated Makefile targets and scripts

Generated report	Makefile target	Script
Full Test Specification	full_spec	generate_all_spec.py
Test Report	test_report	generate_report.py
Requirements Coverage	requirements_coverage	generate_requirements_coverage.py
API Coverage	api_coverage	generate_api_coverage.py

Appendix A

Project Build System Makefiles

A.1 Makefile.test

```
1  # Makefile containing targets specific to testing
2
3  TEST_CASE_SPEC_FILE=full_test_spec.odt
4  API_COVERAGE_FILE=api_coverage.csv
5  REQUIREMENTS_COVERAGE_FILE=requirements_coverage.csv
6  TEST_REPORT_FILE=test_report.odt
7
8
9  # Test Case Specification targets
10
11 .PHONY: full_spec
12 full_spec: $(TEST_CASE_SPEC_FILE)
13     @echo
14     @echo "Generated_full_Test_Case_Specification_into_\"$^\"
15     @echo "Please_remove_manually_the_generated_file."
16
17 .PHONY: $(TEST_CASE_SPEC_FILE)
18 $(TEST_CASE_SPEC_FILE):
19     $(TEST_ROOT)/common/tools/generate_all_spec.py --format=odt
20     -o $@ $(TEST_ROOT)/functional-tests $(TEST_ROOT)/
21     performance-tests $(TEST_ROOT)/robustness-tests
22 #
23 # ...
```

Listing A.1: Testing Targets Makefile (Makefile.test)

Bibliography

- [1] International Organization for Standardization. Iso/iec 26300:2006 open document format.
http://std.dkuug.dk/keld/iso26300-odf/is26300/iso_iec_26300:2006_e.pdf, December 2006.

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Ultimate answer to all knowledge, [2](#)