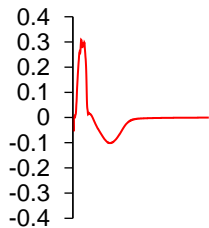
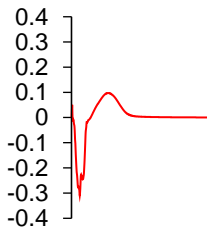
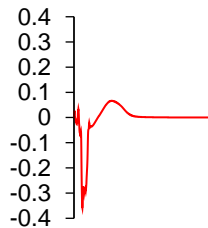
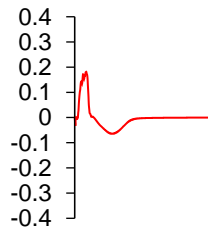


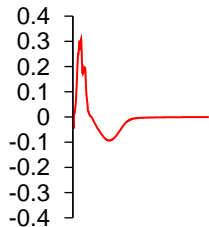
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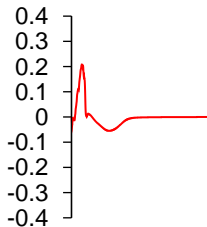
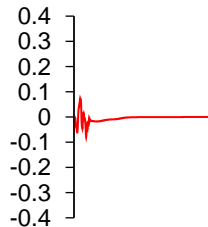
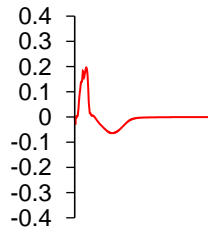
aVR

V<sub>1</sub>V<sub>4</sub>

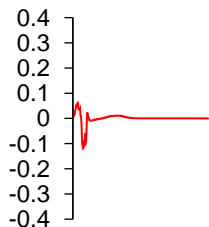
II



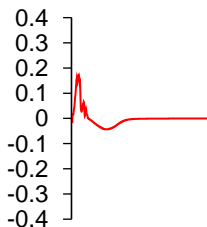
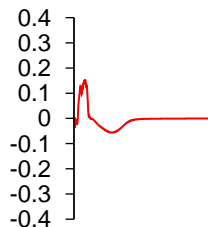
aVL

V<sub>2</sub>V<sub>5</sub>

III



aVF

V<sub>3</sub>V<sub>6</sub>