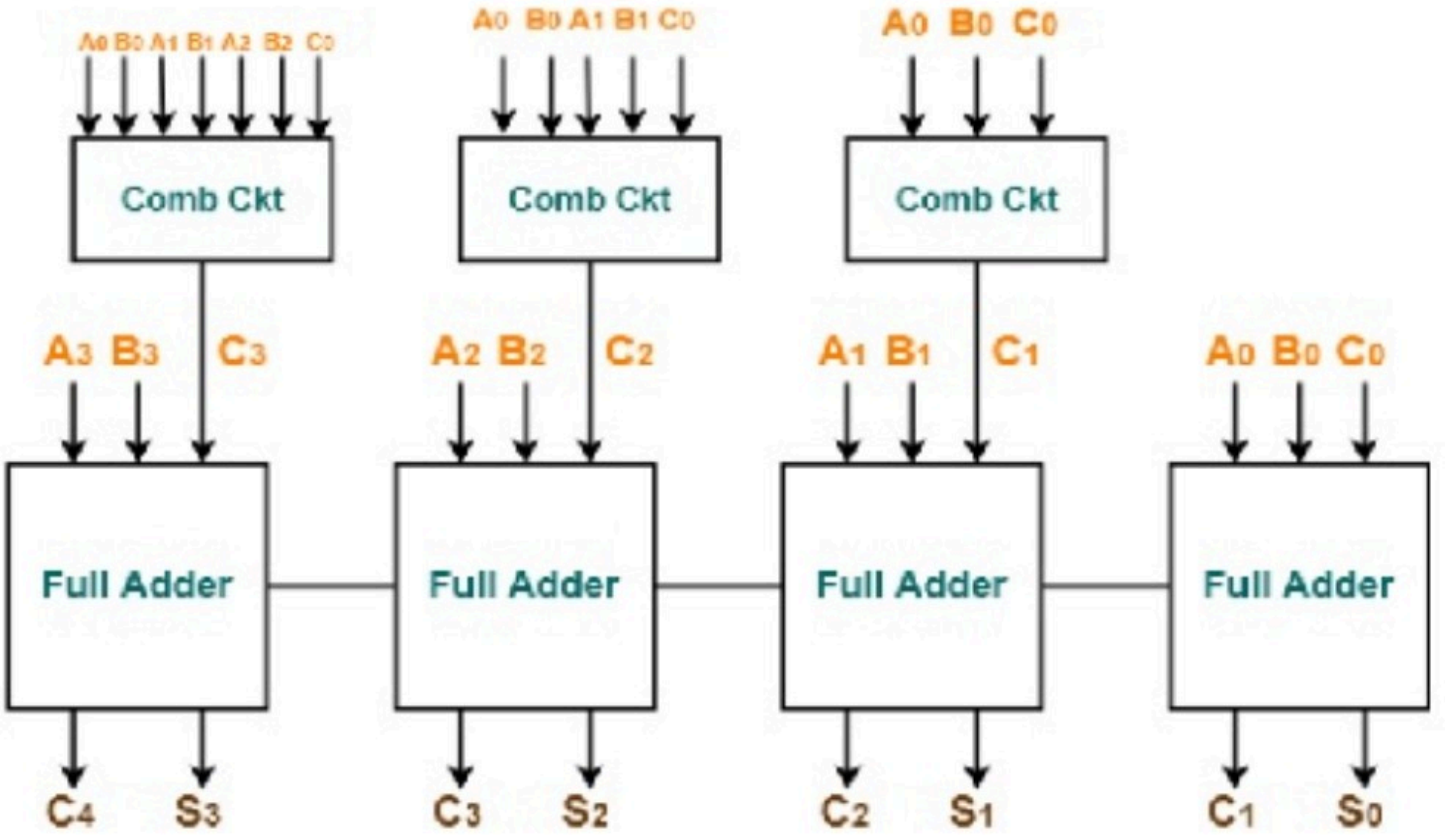
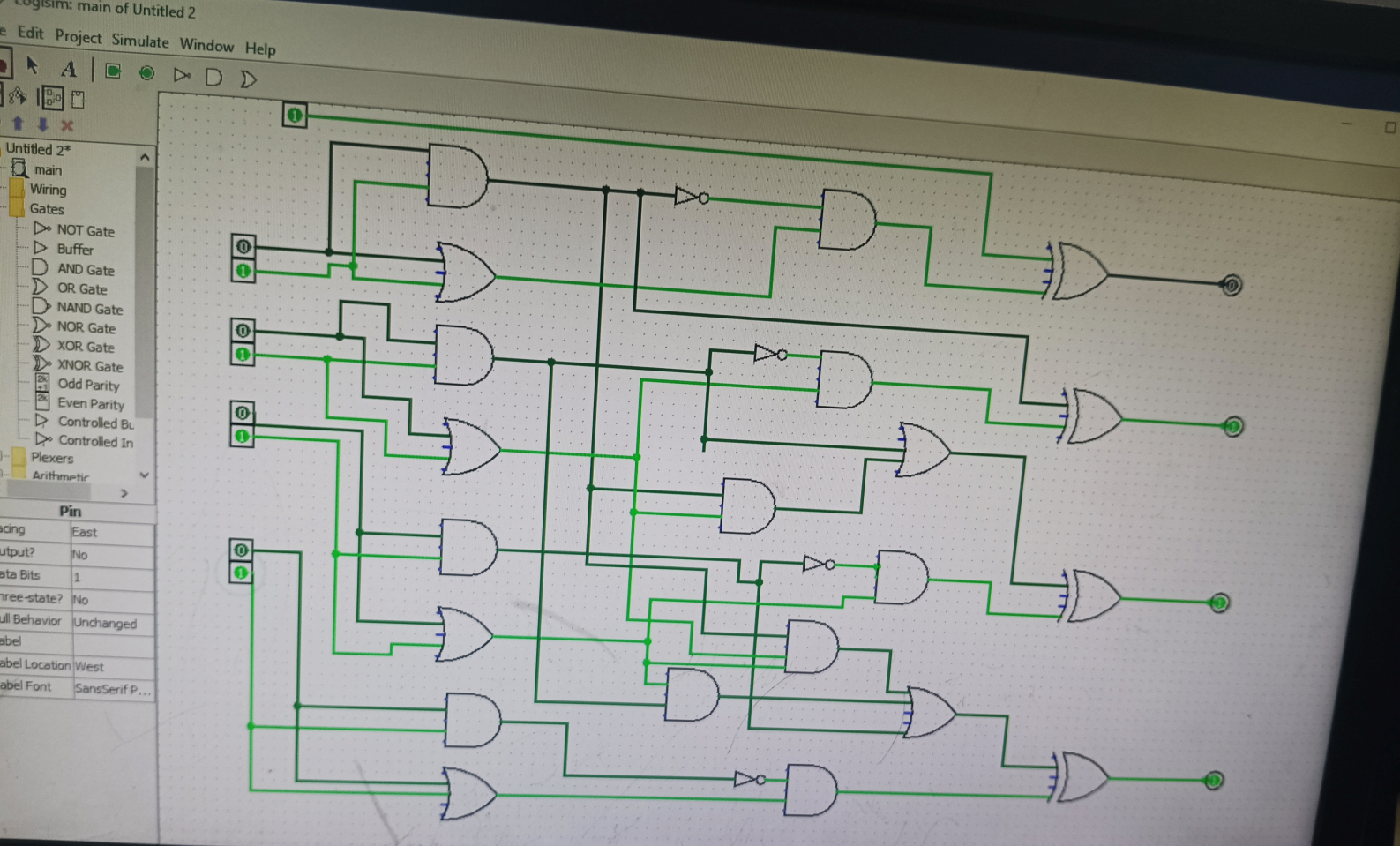


4-bit-Carry-Look-ahead-Adder-Logic-Diagram

A	B	Ci	C i +1	Condition
0	0	0	0	No carry generate
0	0	1	0	
0	1	0	0	
0	1	1	1	No carry propagate
1	0	0	0	
1	0	1	1	
1	1	0	1	Carry generate
1	1	1	1	

Carry-Look-ahead-Adder-Truth-Table


Logisim: main of Untitled 2
Edit Project Simulate Window Help
Untitled 2*
main
Wiring
Gates
NOT Gate
Buffer
AND Gate
OR Gate
NAND Gate
NOR Gate
XOR Gate
XNOR Gate
Odd Parity
Even Parity
Controlled Bu
Controlled In
Plexers
Arithmetic
Pin
East
No
1
No
Unchanged
West
SansSerif P...