OPERATING SYSTEMS

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ROLLNO: 19CSE012

CSE-A- II-year

DATE: 06-05-2021

PART-A

All the addresses need to be added by 250.
So, the addresses would be 300, 328, 400, 402, 404
respectively.

2. The system's medernet negester must have 64 bits.

PART-B

1)

A page fault occurs when an access to a page that has not been brought into marn memory takes place. The operating system verifies the memory access, aborting the program if it is invaled. If it is valid, a free frame is located and I/O is requested to read the needed page into the free frame.

Upon completion IT I/O, the process table and page table are updated and the instruction is restarted.

Situation 1: When LPU generates fewer page faults than LPV																							
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