

CUT Q



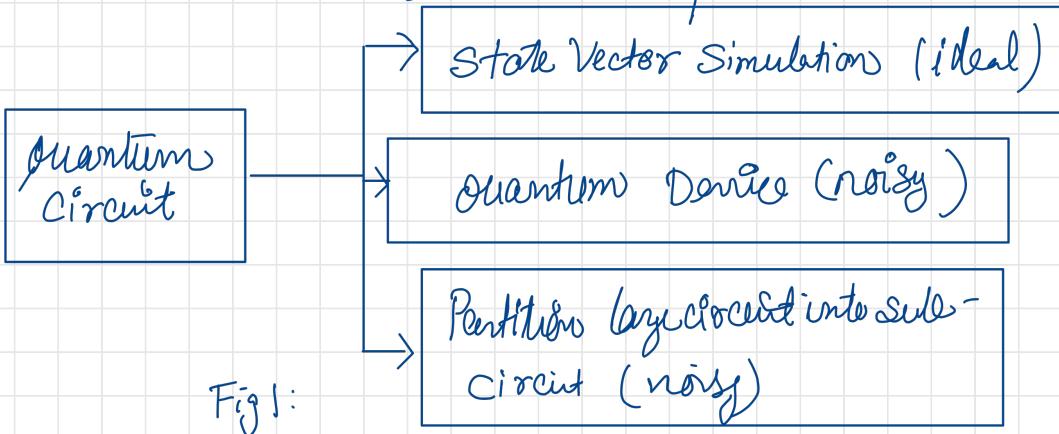
Cut DC: Using small Quantum Computers for  
large quantum circuit  
evaluations.

## Background

Quantum programs are expressed as circuits that consists of a sequence of single- and multi qubit gate operations.



One widely used package is IBM's Qiskit, which allows simulations and cloud access to IBM's quantum hardware.



State vector simulation is typically an idealized noiseless simulation of a quantum circuit. All quantum operations are represented as unitary matrices. ( $2^n \times 2^n$ )

This simulation sequentially multiply each gate's corresponding unitary matrix with the current state vector.

## Circuit Cutting

Unitary Matrix of an arbitrary quantum operation in a quantum circuit can be decomposed into any set of orthonormal matrix bases.

$$A = \frac{\text{Tr}(A_1)I + \text{Tr}(AX)X + \text{Tr}(AY)Y + \text{Tr}(AZ)Z}{2}$$

(∴ this one requires access to complex amplitudes, which are not available on a quantum computer)

we further decompose the Pauli matrices into their eigenbasis and organize the terms-

$$A = \frac{A_1 + A_2 + A_3 + A_4}{2}$$

$$A_1 = [\text{Tr}(A_1) + \text{Tr}(AZ)] |0\rangle\langle 0|$$

$$A_2 = [\text{Tr}(A_1) - \text{Tr}(AZ)] |1\rangle\langle 1|$$

$$A_3 = \text{Tr}(AX) [2|+X+|-|0\rangle\langle 0| - |1\rangle\langle 1|]$$

$$A_4 = \text{Tr}(AY) [2|+iX+i|-|0\rangle\langle 0| - |1\rangle\langle 1|]$$

$\text{Tr}(A_i) \sim$  Trace operator means measuring the qubit in that basis

$|+X+| \sim$  Density operator means to initialize the qubit in one of the eigen states

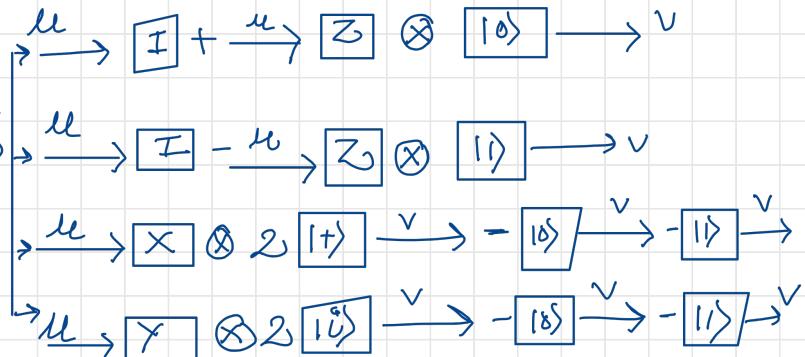


Fig2: Procedure to cut one qubit wire- The wire between vertices  $u$  and  $v$  can be cut by summing over four pairs of measurement circuits appended to  $v$ . Measurement in  $I$  and  $Z$  have some physical meaning.

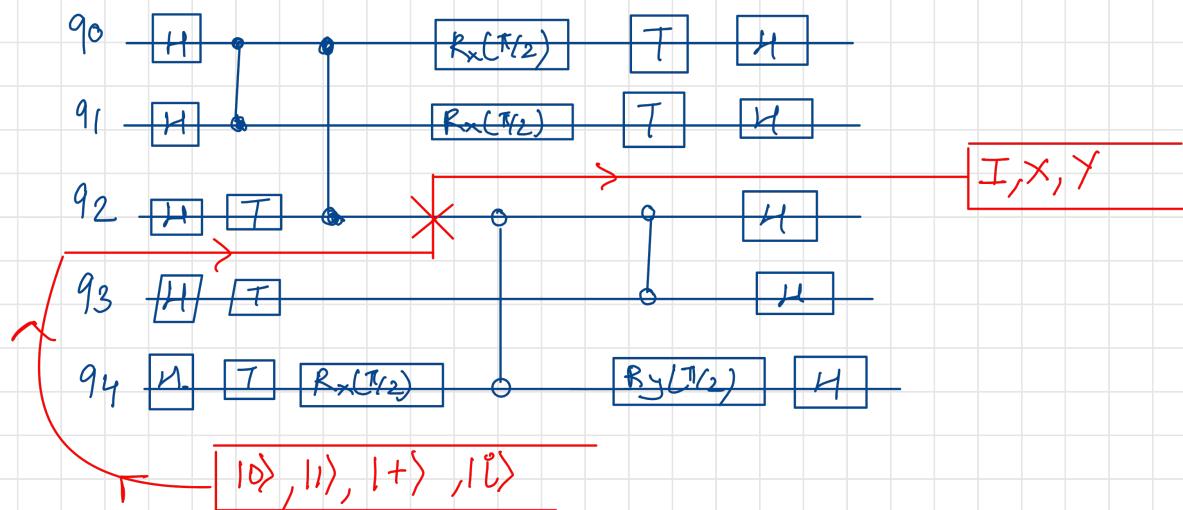
3 upstream :

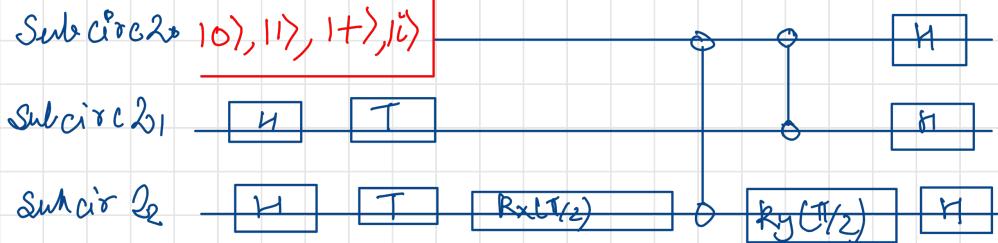
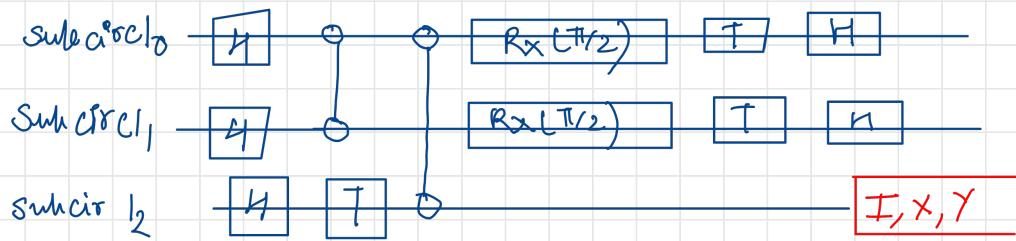


4 downstream

$|0\rangle, |1\rangle, |+\rangle, |i\rangle$

### Circuit Cutting : Example





A 5 qubit circuit is cut into two smaller subcircuits of three qubits each.

(see later)  
How to select the cut location?

Also see that subcir 1<sub>2</sub> does not appear in the final output

$\therefore$  Each shot from subcir1 needs to be multiplied by a  $\pm 1$  contingent on the measurement outcomes of subcir 2. Specifically,

$$\overline{xx0}, \overline{xx1} \rightarrow +\overline{xx} \quad M_2 = I$$

$$\overline{xx0} \rightarrow +\overline{xx}$$

$$\overline{xx1} \rightarrow -\overline{xx} \text{ otherwise}$$

where  $\overline{xx}$  is the measurement outcome of the qubits 1 and 2.

Let's see this with an example:

Let the state  $|01010\rangle$   
 sub1 sub2

Sub circuit 1

$|01\rangle$  third combolotally

$$\rho_{1,1} = \rho(|010\rangle|I\rangle) + \rho(|011\rangle|I\rangle) + \rho(|010\rangle|Z\rangle) - \rho(|011\rangle|Z\rangle)$$

$$\rho_{1,2} = \rho(|010\rangle|I\rangle) + \rho(|011\rangle|I\rangle) - \rho(|010\rangle|Z\rangle) + \rho(|011\rangle|Z\rangle)$$

$$\rho_{1,3} = \rho(|010\rangle|X\rangle) - \rho(|011\rangle|X\rangle)$$

$$\rho_{1,4} = \rho(|010\rangle|Y\rangle) - \rho(|011\rangle|Y\rangle)$$

that's why we have

$$(\rho(|010\rangle|I\rangle) + \cancel{\rho(|011\rangle|I\rangle)})$$

Two possibilities sign will be +ve since  $M_2 = I$

$$|010\rangle, |011\rangle \rightarrow +\bar{01} M_2 = I$$

$$|010\rangle \rightarrow +01$$

$$|011\rangle \rightarrow -01$$

similarly

$$\rho(|010\rangle|Z\rangle) - \cancel{\rho(|011\rangle|Z\rangle)}$$

Two possibilities

$$\rho(|010\rangle|X\rangle) - \cancel{\rho(|011\rangle|X\rangle)}$$

sign will be -ve  
since

$$|010\rangle = +|01\rangle$$

$$|011\rangle = -|01\rangle$$

Now for the subcircuit 2

$$\rho_{2,1} = \rho(|010\rangle|0\rangle) \quad \rho_{2,2} = \rho(|010\rangle|1\rangle)$$

$$\rho_{2,3} = 2\rho(|010\rangle|+\rangle) - \rho(|010\rangle|0\rangle) - \rho(|010\rangle|1\rangle)$$

$$\rho_{2,4} = 2\rho(|010\rangle|i\rangle) - \rho(|010\rangle|0\rangle) - \rho(|010\rangle|1\rangle)$$

Building up to full probabilities

The full probability distribution can be reconstructed by taking the relevant outputs of the two smaller subcircuits performing four pairs of kronecker product and summing them up together.

$$P(|01010\rangle) = \frac{1}{2} \sum_{i=1}^4 P_{1,i} \otimes P_{2,i}$$

do not take too few shots (at most 8192)

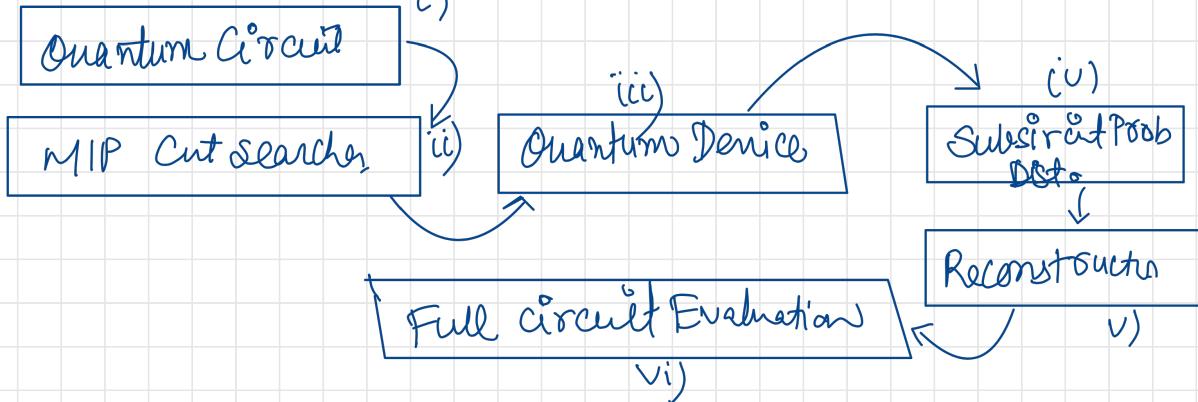
### Challenges

i) Where to place the cut? or How to find the cut location?

For general quantum circuits with  $n$  quantum edges, this task faces  $\mathcal{O}(n!)$  combinatorial search space -

ii) How to scale the classical post-processing?

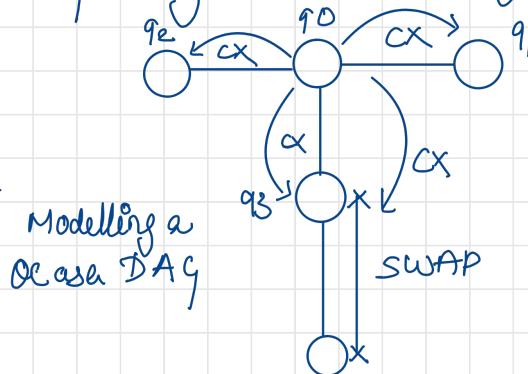
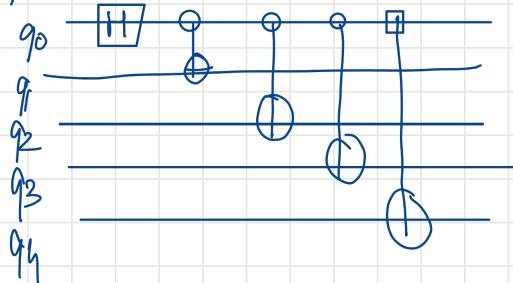
We make use of MIP cut searchers using Gurobi Solver.



# Mixed Integer Programming Cut Searcher.

Assumptions:

- i) Input quantum circuit is fully connected i.e all qubits are connected via multiqubit gate either directly or indirectly



Model parameters

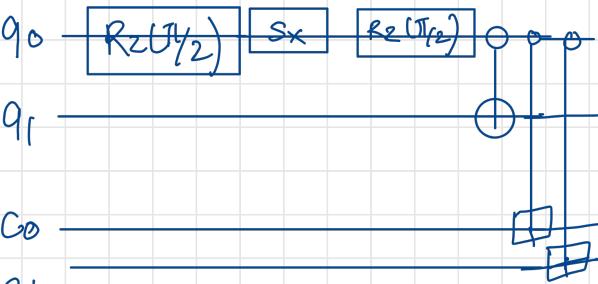
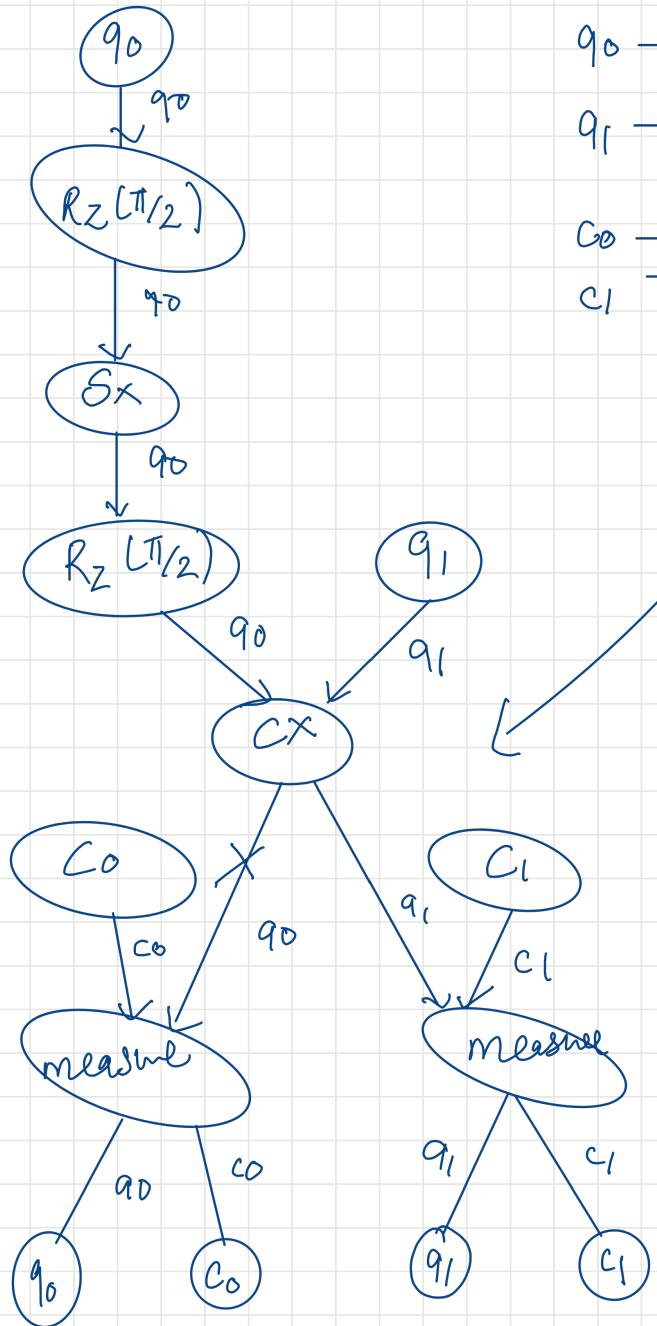
- a) input quantum circuit
- b) maximum number of qubits allowed per subcircuit  $D$   
 $\approx$  size of the quantum device available to the user
- c) maximum numbers of subcircuits allowed  $n_c$

The single qubit gates are ignored during the cut finding process, since they do not affect the connectivity of the circuit.

The multiqubit quantum gates are modelled as vertices  $V = \{v_1, \dots, v_n\}$  and the qubit wires as edges  $E = \{(e_a, e_b) : e_a \in V, e_b \in V\}$

Choosing which edge to cut in order to split into subcircuits  $C = \{C_1, \dots, C_{n_c}\}$  can be thought as clustering the vertices

To visualize a quantum circuit as a graph. Qiskit does that internally using 'qutipworks'. An Example

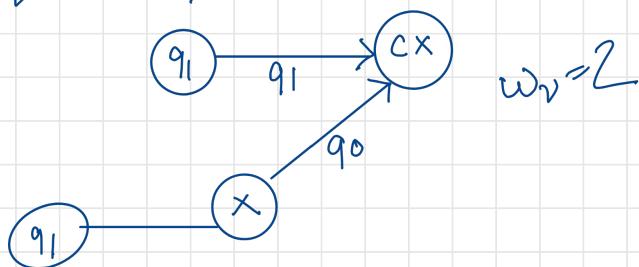


Quantum  
Circuit as  
A Direct  
Acyclic  
Graph

Vertices are the quantum gates and edges are the gates among them

The MIP cutter assigns a parameter ' $w_v$ ' to each vertex ' $v$ ' that indicates the number of original input qubits directly connected to  $v$ .

$$w_v \in \{0, 1, 2\}$$



Since any gate involving more than 2 qubits in can be decomposed into native set.

## Constrained Graph Clustering Algorithms

$$y_{v,c} = \begin{cases} 1 & , \text{ if vertex } v \text{ is in subcircuit } c \\ 0 & , \text{ otherwise} \end{cases} \quad \forall v \in V, \forall c \in C$$

$$x_{e,c} = \begin{cases} 1 & , \text{ if edge } e \text{ is cut by subcircuit } c \\ 0 & , \text{ otherwise} \end{cases} \quad \forall e \in E, \forall c \in C$$

The number of qubits required to run a subcircuit is the sum of two parts, namely the number of original input qubits and the number of initialization qubit induced by cutting.

weight factors for the vertices.

$$\alpha_c = \sum w_v \times y_{v,c} \quad \forall c \in C$$

original input qubits in each subcircuit

# Mixed Integer Programming Cut Searcher.

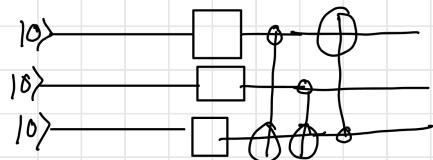
i) Model Parameters

a) input quantum circuit

b) maximum number of qubits allowed per subcircuit ( $D$ )

c) maximum number of subcircuits allowed ( $n_c$ )

d) maximum number of cuts allowed.



quantum gates are modeled as vertices  $V = \{v_1, v_2, \dots, v_n\}$

and the qubit wires are modeled as edges  $E = \{(e_a, e_b) : e_a \in V, e_b \in V\}$

the set of subcircuits  $C = \{c_1, c_2, \dots, c_n\}$

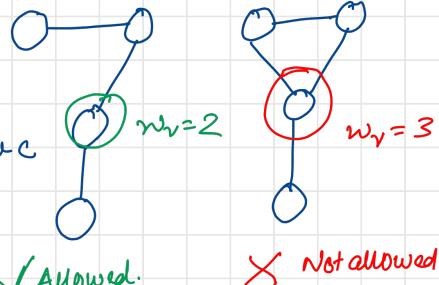
We also make use of a parameter  $w$  (weight) associated with each vertex that indicates the number of original inputs qubits directly connected to  $v$ .  $w_v \in \{0, 1, 2\}$

(can be made to accommodate multi-qubit gates)

ii) Variables

$$y_{v,c} = \begin{cases} 1 & \text{if vertex } v \text{ is in subc} \\ 0 & \text{otherwise} \end{cases}$$

*gate*  
+  $v \in V$       ✓ Allowed.  
+  $c \in C$



$$x_{e,c} = \begin{cases} 1 & \text{if edge } e \text{ is cut by subcircuit } c \\ 0 & \text{otherwise} \end{cases}$$

+  $e \in E$       ✓ Allowed.  
+  $c \in C$

Q)

The number of qubits required to run a subcircuit is the sum of two parts, i) the number of original input qubits ii) the number of initialization qubits induced after cutting.

$$x_c \equiv \sum_{v \in V} w_v \times y_{v,c} \quad \forall c \in C$$

this is basically the sum of qubits directly connected to the vertex  $v$ .

- b) A subcircuit requires initialization qubits when a downstream vertex  $e_b$  is in the subcircuit for some edge  $(e_a, e_b)$  that is cut. The number of initialization qubits  $p_c$  is

$$p_c = \sum_{e: (e_a, e_b) \in E} x_{e,c} \times y_{e_b,c} \quad \forall c \in C$$

- c) A subcircuit requires measurement qubits when an upstream vertex  $e_a$  is in the subcircuit for some edge  $(e_a, e_b)$  that is cut.

$$o_c \equiv \sum x_{e,c} \times y_{e,a,c}, \quad \forall c \in C$$

Consequently, the number of qubits in a subcircuit that contributes to the final measurement of the original uncut circuit is

$$f_c \equiv x_c + p_c - o_c, \quad \forall c \in C$$

### iii) Constraints

- a) Every vertex be assigned to exactly one subcircuit

$$\sum_{c \in C} y_{v,c} = 1 \quad \forall v \in V$$

- b) the qubits ( $d_c$ ) in a subcircuits ( $c$ ) be no larger than the input device size  $D$ .

$$d_c \equiv x_c + p_c \leq D \quad \forall c \in C$$

### c) Edge constraint

$$x_{e,c} = y_{e_a,c} \oplus y_{e_b,c} \quad \forall e = (e_a, e_b) \in E, c \in C$$

This formulation force vertices with smaller indices to be in subcircuits with smaller indices.

#### iv) Objective function

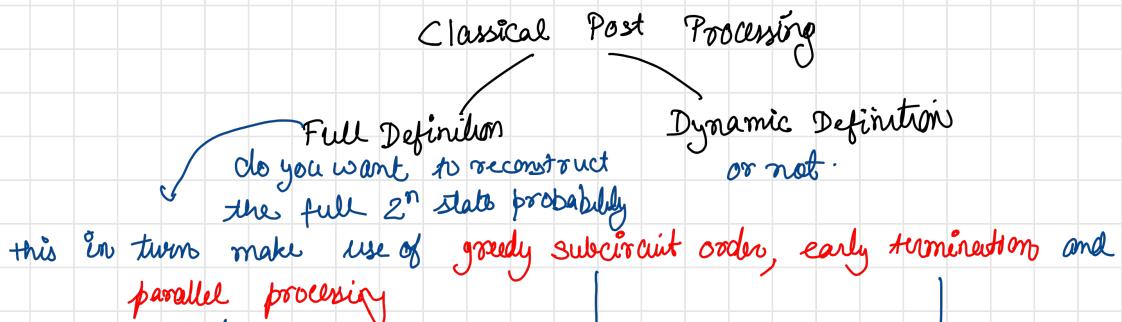
We seek to minimize the classical post-processing overhead required to reconstruct a circuit from its subcircuit. Hence the objective function is the number of floating-point multiplications involved.

number of cuts made is

$$K = \frac{1}{2} \sum_{c \in C} \sum_{e \in E} x_{e,c}$$

The objective function hence becomes.

$$L = 4^K \sum_{c=2}^{n_c} \sum_{i=1}^c 2^{f_i} \quad \text{minimize this}$$



vector arithmetic have no data dependency and hence can be easily executed in parallel.

if one vector of the subcircuit goes 0, the all goes 0, hence good to terminate early.