

JLCPBCB Impedance Calculator

The JLCPBCB Impedance Calculator computes track width values and recommended stack-ups from user-input values of board layer, thickness, weight, target impedance, trace spacing (for edge-coupled pairs), and impedance trace to copper gap (coplanar waveguides). [Click here](#) to guide of our impedance calculator.

* The first stack-up calculated below is highly recommended as it has lowest cost and quickest turn-around.

Layers PCB Thickness Inner Copper Weight Outer Copper Weight Unit

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50	Coplanar Single Ended	L1	/	L2	/	0.2
90	Coplanar Differential Pair	L1	/	L2	0.2	0.2
90	Coplanar Differential Pair	L3	L2	L4	0.2	0.2

[JLCH04161HN1-1078\(通用/成品板厚1.52mm±10%\)](#) [JLC04161H-3313\(Finished thickness1.56mm±10%\)](#) [JLC04161H-3313A\(Spec\)](#)

Impedance (Ω)	Type	Signal Layer	Top Ref	Bottom Ref	Trace Width	Trace Spacing	In
50	Coplanar Single Ended	L1	/	L2	0.1161	/	0.
90	Coplanar Differential Pair	L1	/	L2	0.1257	0.2000	0.
90	Coplanar Differential Pair	L3	L2	L4	0.1105	0.2000	0.
Layer	Material			Thickness (mil)		Thickness (mm)	
L1	Outer Copper Weight 1oz			1.38		0.0350	
Prepreg	1078RC69%3.39mil			3.09		0.0784	
L2	Inner Copper Weight			0.60		0.0152	
Core	1.3mm H/H0Z 含铜			49.80		1.2650	
L3	Inner Copper Weight			0.60		0.0152	
Prepreg	1078RC69%3.39mil			3.09		0.0784	
L4	Outer Copper Weight 1oz			1.38		0.0350	

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