

EE669: VLSI Technology

Dopant Diffusion in Silicon

Anil Kottantharayil,
Professor,
Department of EE, IIT Bombay

1

Diffusion examples

- Deposition of a glass containing high concentration of the dopant on the Si surface and subsequent high temperature “drive-in”
 - Phosphosilicate glass deposition on Si wafers from solid sources, by reaction with $\text{POCl}_3 + \text{O}_2$, or by reaction with $\text{H}_3\text{PO}_4 + \text{O}_2$
 - The glass has to be etched away subsequent to the drive-in process
- Implantation (deposition) of dopants into the semiconductor and subsequent high temperature annealing to remove implant damage, dopant activation, and some times intentional diffusion
- Deposition of doped silicon and diffusion during the deposition (epitaxial silicon) or during a subsequent anneal step (poly-Si emitter process for Bipolar Junction Transistor fabrication)
- Diffusion of dopants already present in silicon during any high temperature process (e.g.: oxide growth, epitaxial growth of low doped Si on heavily doped Si)

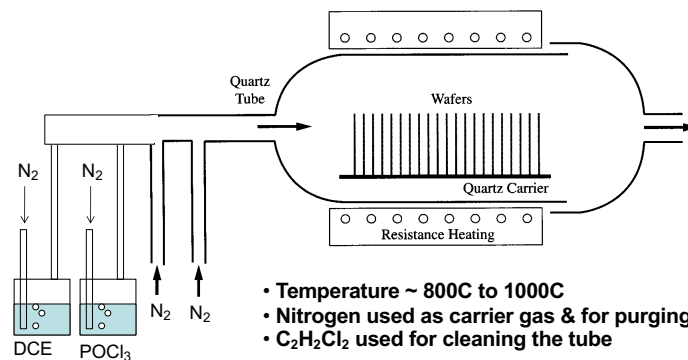
2024 Monsoon

EE669: Dopant Diffusion in Silicon; Anil Kottantharayil

2

2

Furnace diffusion of phosphorous using POCl_3



J. D. Plummer, M. D. Deal, P. G. Griffin, Silicon VLSI Technology, Pearson Education, 2001

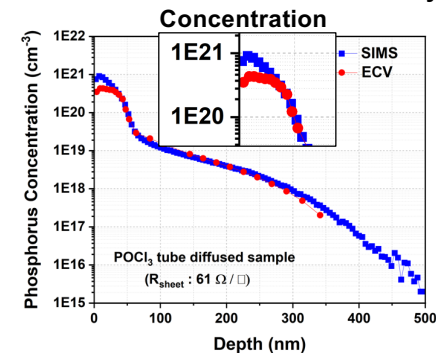
2022 Monsoon

EE669: Thermal Oxidation of Silicon; Anil Kottantharayil

3

3

Chemical Concentration and Electrically Active Concentration



SIMS: Secondary Ion Mass Spectroscopy;
ECV: Electrochemical Capacitance Voltage

Saima C., Ph. D. student, NCPRE and EE, IIT Bombay

2024 Monsoon

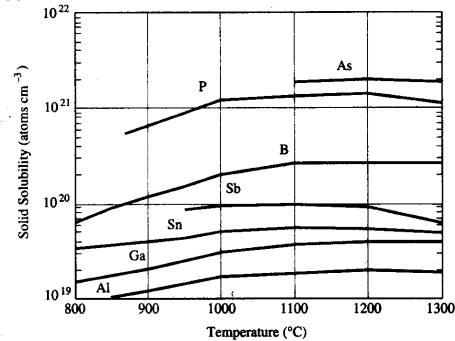
EE669: Dopant Diffusion in Silicon; Anil Kottantharayil

4

4

Solid solubility

The maximum concentration of a dopant that can be dissolved in a semiconductor under equilibrium conditions without formation of separate phases.



J. D. Plummer, M. D. Deal, P. G. Griffin, Silicon VLSI Technology, Pearson Education, 2001

2024 Monsoon

EE669: Dopant Diffusion in Silicon; Anil Kottantharayil

5

5

Diffusion theory: quick review

• Fick's first law of diffusion: $F = -D \frac{\partial C}{\partial x}$

• Diffusion coefficient: $D = D_0 e^{-E_a/k_B T}$

• Fick's second law of diffusion: $\frac{\partial C}{\partial t} = \frac{\partial}{\partial x} \left(D \frac{\partial C}{\partial x} \right)$

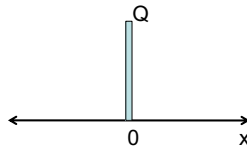
2024 Monsoon

EE669: Dopant Diffusion in Silicon; Anil Kottantharayil

6

6

Solution for fixed dose condition



At $t = 0$

$C \rightarrow 0$ for $x > 0$ & $x < 0$

$C \rightarrow \text{infinity}$ at $x = 0$

$$\int_{-\infty}^{\infty} C(x,t) dx = Q \quad ; \text{ at all times}$$

$$C(x,t) = \frac{Q}{2\sqrt{\pi Dt}} \exp\left(-\frac{x^2}{4Dt}\right) = C(0,t) \exp\left(-\frac{x^2}{4Dt}\right)$$

Exercise: Prove that the equation given is a solution to the Fick's second law of diffusion.

$$C(x,t) = C(0,t) \exp\left(-\frac{x^2}{L_D^2}\right); \quad L_D = 2\sqrt{Dt}$$

J. D. Plummer, M. D. Deal, P. G. Griffin, Silicon VLSI Technology, Pearson Education, 2001

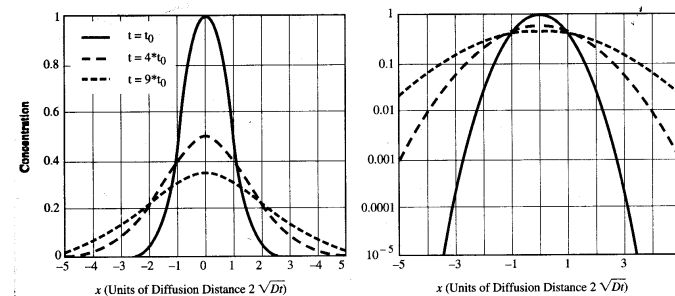
2024 Monsoon

EE669: Dopant Diffusion in Silicon; Anil Kottantharayil

7

7

Solution for fixed dose condition (2)



J. D. Plummer, M. D. Deal, P. G. Griffin, Silicon VLSI Technology, Pearson Education, 2001

2024 Monsoon

EE669: Dopant Diffusion in Silicon; Anil Kottantharayil

8

8

Solution for fixed dose at the surface of the semiconductor (3)



$$C(x,t) = \frac{Q}{\sqrt{\pi Dt}} \exp\left(-\frac{x^2}{4Dt}\right) = C(0,t) \exp\left(-\frac{x^2}{4Dt}\right)$$

For example, phosphorous diffusion using POCl_3 .

J. D. Plummer, M. D. Deal, P. G. Griffin, Silicon VLSI Technology, Pearson Education, 2001

2024 Monsoon

EE669: Dopant Diffusion in Silicon; Anil Kottantharayil

9

Exercise

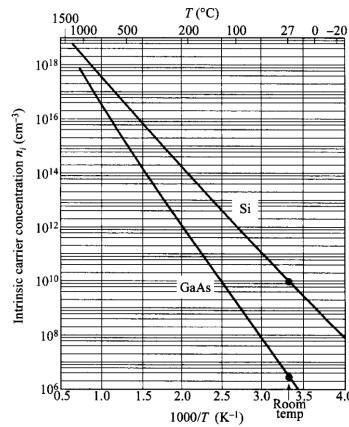
Derive the solution for the diffusion equation for diffusion from an infinite source.

2024 Monsoon

EE669: Dopant Diffusion in Silicon; Anil Kottantharayil

10

Intrinsic silicon



S. M. Sze, Physics of Semiconductor Devices

Exercise: Determine the temperature at which the intrinsic carrier concentration is equal to the doping concentration, for a silicon wafer doped with 10^{17} cm^{-3} of boron.

2024 Monsoon

EE669: Dopant Diffusion in Silicon; Anil Kottantharayil

11

Intrinsic diffusivities in Si

The dopant concentrations are low such that at the process temperature, the semiconductor can be considered intrinsic.

$$D = D_0 \exp\left(-\frac{E_A}{kT}\right)$$

	Si	B	In	As	Sb	P	Units
D_0	560	1.0	1.2	9.17	4.58	4.7	$\text{cm}^2 \text{ s}^{-1}$
E_A	4.76	3.5	3.5	3.99	3.88	3.68	eV

J. D. Plummer, M. D. Deal, P. G. Griffin, Silicon VLSI Technology, Pearson Education, 2001

2024 Monsoon

EE669: Dopant Diffusion in Silicon; Anil Kottantharayil

12

The concept of thermal budget

- The factor “Dt” is seen to appear together in the solutions we have worked out for diffusion.
- Suppose that the “Dt” is the same for two different diffusion processes conducted at two different temperatures, the two processes have same “thermal budget”.
- The thermal budget of various processes (also other than diffusion) are important as any dopants already present in the semiconductor can diffuse during other high temperature processes.

Exercise:

- (1) For a fixed dose diffusion of Boron from a delta profile positioned at the center (along the thickness) of a silicon wafer, the following parameters are given: $Q = 10^{15} \text{ cm}^{-2}$, $T = 1000^\circ\text{C}$, $t = 30 \text{ min}$. Plot the diffusion profile at the end of the diffusion. You may assume intrinsic diffusion.
- (2) In the above problem, supposing that the diffusion was carried out at 900°C with the same thermal budget, what was the duration of diffusion.

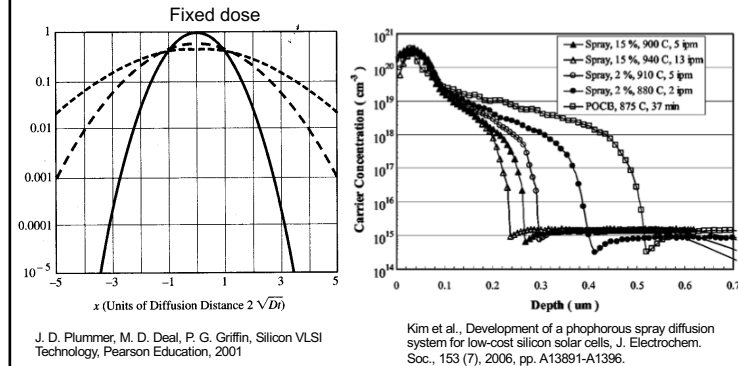
2024 Monsoon

EE669: Dopant Diffusion in Silicon; Anil Kottantharayil

13

13

Deviation from the simple theory



2024 Monsoon

EE669: Dopant Diffusion in Silicon; Anil Kottantharayil

14

14