

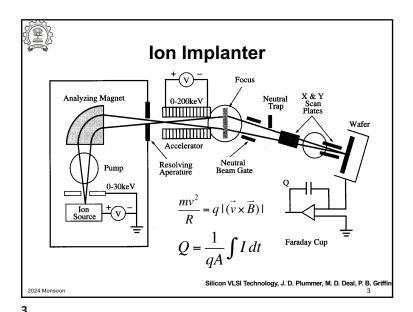
EE669: VLSI Technology

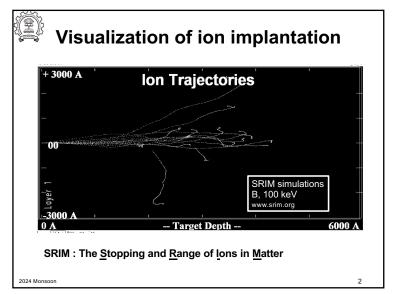
Ion implantation

Anil Kottantharayil Department of EE, IIT Bombay

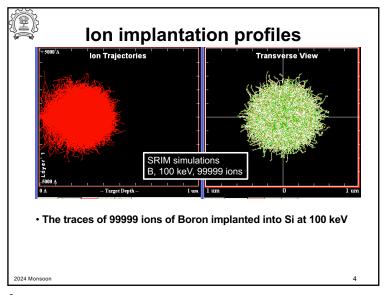
"Design of **ion-implanted** MOSFETs with very small physical dimensions", Dennard et al., IEEE Journal of Solid-State Circuits, October 1974.

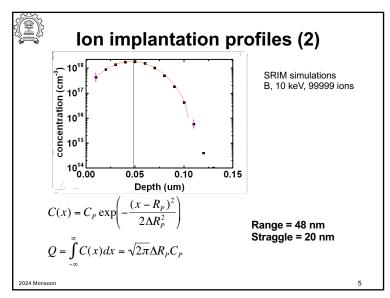
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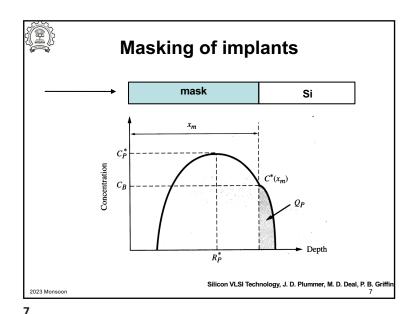


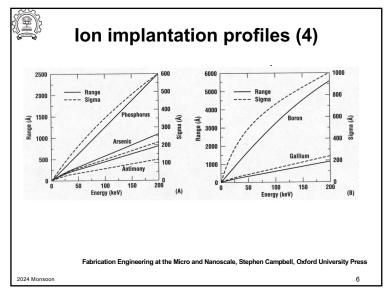


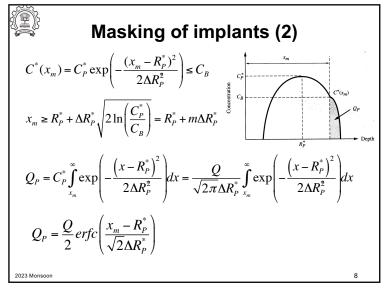
2













Masking of implants (3)

$$E = \int VJdt = V \int Jdt = qVQ$$

- The ions implanted are decelerated in the wafer resulting in dissipation of the kinetic energy of the ions
 - heating of the wafer
 - · thermal mass of the wafer + chuck assembly
- The mask should withstand such temperature
- Photo resist used as mask undergoes chemical and physical changes ~ 120 C

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