TABLE 11-7: PORTD FUNCTIONS

Pin Name	Function	TRIS Setting	I/O	I/O Type	Description
RD0/AD0/PSP0	RD0	0	O	DIG	LATD<0> data output.
		1	I	ST	PORTD<0> data input.
	AD0(1)	x	O	DIG	External memory interface, address/data bit 0 output. Takes priority over PSP and port data.
		x	I	TTL	External memory interface, data bit 0 input.
	PSP0	x	O	DIG	PSP read data output (LATD<0>). Takes priority over port data.
		x	I	TTL	PSP write data input.
RD1/AD1/PSP1	RD1	0	O	DIG	LATD<1> data output.
		1	I	ST	PORTD<1> data input.
	AD1(1)	x	O	DIG	External memory interface, address/data bit 1 output. Takes priority over PSP and port data.
		x	I	TTL	External memory interface, data bit 1 input.
	PSP1	x	O	DIG	PSP read data output (LATD<1>). Takes priority over port data.
		x	I	TTL	PSP write data input.
RD2/AD2/PSP2	RD2	0	O	DIG	LATD<2> data output.
		1	I	ST	PORTD<2> data input.
	AD2(1)	x	O	DIG	External memory interface, address/data bit 2 output. Takes priority over PSP and port data.
		x	I	TTL	External memory interface, data bit 2 input.
	PSP2	x	O	DIG	PSP read data output (LATD<2>). Takes priority over port data.
		x	I	TTL	PSP write data input.
RD3/AD3/PSP3	RD3	0	O	DIG	LATD<3> data output.
		1	I	ST	PORTD<3> data input.
	AD3(1)	x	O	DIG	External memory interface, address/data bit 3 output. Takes priority over PSP and port data.
		x	I	TTL	External memory interface, data bit 3 input.
	PSP3	x	O	DIG	PSP read data output (LATD<3>). Takes priority over port data.
		x	I	TTL	PSP write data input.
RD4/AD4/ PSP4/SDO2	RD4	0	O	DIG	LATD<4> data output.
		1	I	ST	PORTD<4> data input.
	AD4(1)	x	O	DIG	External memory interface, address/data bit 4 output. Takes priority over PSP, MSSP and port data.
		x	I	TTL	External memory interface, data bit 4 input.
	PSP4	x	O	DIG	PSP read data output (LATD<4>). Takes priority over port and PSP data.
		x	I	TTL	PSP write data input.
	SDO2	0	O	DIG	SPI data output (MSSP2 module). Takes priority over PSP and port data.

Legend: PWR = Power Supply, O = Output, I = Input, ANA = Analog Signal, DIG = Digital Output, ST = Schmitt Buffer Input, TTL = TTL Buffer Input, x = Don't care (TRIS bit does not affect port direction or is overridden for this option).

Note 1: Implemented on 80-pin devices only.