

COL 216Assignment-2Stage 3

Combined.vhd - file which contains all circuit components. eg. ALU, RF, fsm etc.  
All these components are combined together in one file.

Datapath.vhd - file which contains all glue logics and all other logical units and it combines all components of Combined.vhd. And a ckt for multi-cycle processor is formed.

for checking files on eda playground.

Put Combined.vhd in Design.vhd  
And Datapath.vhd in testbench.vhd  
and set Top entity  $\Rightarrow$  Controller.

