GND		GND	3V3	X	VCC
HSPI CS UORTS		PB12(CS2)	PC3(MOSI2)	X	HSPI MOSI UBCTS
U1TXD		NC	PC2(MISO2)	X	HSPI MISO
SPI_CS2		PD7	PB13(SCK2)	X	HSPI CLK
		PD10	NC	X	
		NC	PG8	$\times$	CHIP_EN CH_PD
UORXD		PD8(TX3)	NC	X	ADC
SPI_CS1 U0TXD	X	PD9(TX3)	PG7	X	EXT_RSTB