INCLUDE <P18F4321.INC>

ORG 0X50

MOVLW 0X40; START OF STORING 7SEG CODE, 40H REPRESENTS 0

MOVWF 0X30; STORES 40H IN ADDRESS 0X30

MOVLW 0X79; MOVE 79H TO WREG, THIS REPRESENTS 1

MOVWF 0X31; STORES 79H IN ADDRESS 0X31

MOVLW 0X24; MOVE 24H TO WREG, THIS REPRESENTS 2

MOVWF 0X32; STORE 24H IN ADDRESS 0X32

MOVLW 0X30; MOVE 30H TO WREG, THIS REPRESENTS 3

MOVWF 0X33; STORE 30H IN ADDRESS 0X33

MOVLW 0X19; MOVE 19H TO WREG, THIS REPRESENTS 4

MOVWF 0X34; STORE 19H IN ADDRESS 0X34

MOVLW 0X12; MOVE 12H TO WREG, THIS REPRESENTS 5

MOVWF 0X35; STORE 12H IN ADDRESS 0X35

MOVLW 0X02; MOVE 2H TO WREG, THIS REPRESENTS 6

MOVWF 0X36; STORE 2H IN ADDRESS 0X36

MOVLW 0X78; MOVE 78H TO WREG, THIS REPRESENTS 7

MOVWF 0X37; STORE 78H IN ADDRESS 0X37

MOVLW 0X00; MOVE 0H TO WREG, THIS REPRESENTS 8

MOVWF 0X38; STORE 0H IN ADDRESS 0X38

MOVLW 0X10; MOVE 10H TO WREG, THIS REPRESENTS 9

MOVWF 0X39; STORE 10H IN ADDRES 0X39

MOVLW 0X0F; CONFIGURE PORT A AS INPUT PORT

MOVWF ADCON1

CLRF TRISB; CONFIGURE PORT B AS AN OUTPUT PORT

LFSR 0, 0X30; FSR0 TO ADDRESS 0X30

LOOP MOVF PORTA,W; INPUT PORT A SWITCH INTO WREG

ANDLW 0X0F; MASK LOWER 4 BITS

MOVFF PLUSW0, PORTB; OUTPUT BCD DATA TO PORT B

BRA LOOP

SLEEP

END