

	0	1	2	3	4	5	6	7
0	BRK #imm	ORA (zp,X)			TSB zp	ORA zp	ASL zp	RMB0 zp
1	BPL rel	ORA (zp),Y	ORA (zp)		TRB zp	ORA zp,X	ASL zp,X	RMB1 zp
2	JSR abs	AND (zp,X)			BIT zp	AND zp	ROL zp	RMB2 zp
3	BMI rel	AND (zp),Y	AND (zp)		BIT zp,X	AND zp,X	ROL zp,X	RMB3 zp
4	RTI	EOR (zp,X)				EOR zp	LSR zp	RMB4 zp
5	BVC rel	EOR (zp),Y	EOR (zp)			EOR zp,X	LSR zp,X	RMB5 zp
6	RTS	ADC (zp,X)			STZ zp	ADC zp	ROR zp	RMB6 zp
7	BVS rel	ADC (zp),Y	ADC (zp)		STZ zp,X	ADC zp,X	ROR zp,X	RMB7 zp
8	BRA rel	STA (zp,X)			STY zp	STA zp	STX zp	SMB0 zp
9	BCC rel	STA (zp),Y	STA (zp)		STY zp,X	STA zp,X	STX zp,Y	SMB1 zp
A	LDY #imm	LDA (zp,X)	LDX #imm		LDY zp	LDA zp	LDX zp	SMB2 zp
B	BCS rel	LDA (zp),Y	LDA (zp)		LDY zp,X	LDA zp,X	LDX zp,Y	SMB3 zp
C	CPY #imm	CMP (zp,X)			CPY zp	CMP zp	DEC zp	SMB4 zp
D	BNE rel	CMP (zp),Y	CMP (zp)			CMP zp,X	DEC zp,X	SMB5 zp
E	CPX #imm	SBC (zp,X)			CPX zp	SBC zp	INC zp	SMB6 zp
F	BEQ rel	SBC (zp),Y	SBC (zp)			SBC zp,X	INC zp,X	SMB7 zp

	8	9	A	B	C	D	E	F
0	PHP	ORA #imm	ASL A		TSB abs	ORA abs	ASL abs	BBR0 zp,rel
1	CLC	ORA abs,Y	INC A		TRB abs	ORA abs,X	ASL abs,X	BBR1 zp,rel
2	PLP	AND #imm	ROL A		BIT abs	AND abs	ROL abs	BBR2 zp,rel
3	SEC	AND abs,Y	DEC A		BIT abs,X	AND abs,X	ROL abs,X	BBR3 zp,rel
4	PHA	EOR #imm	LSR A		JMP abs	EOR abs	LSR abs	BBR4 zp,rel
5	CLI	EOR abs,Y	PHY			EOR abs,X	LSR abs,X	BBR5 zp,rel
6	PLA	ADC #imm	ROR A		JMP (abs)	ADC abs	ROR abs	BBR6 zp,rel
7	SEI	ADC abs,Y	PLY		JMP (abs,X)	ADC abs,X	ROR abs,X	BBR7 zp,rel
8	DEY	BIT #imm	TXA		STY abs	STA abs	STX abs	BBS0 zp,rel
9	TYA	STA abs,Y	TXS		STZ abs	STA abs,X	STZ abs,X	BBS1 zp,rel
A	TAY	LDA #imm	TAX		LDY abs	LDA abs	LDX abs	BBS2 zp,rel
B	CLV	LDA abs,Y	TSX		LDY abs,X	LDA abs,X	LDX abs,Y	BBS3 zp,rel
C	INY	CMP #imm	DEX	WAI	CPY abs	CMP abs	DEC abs	BBS4 zp,rel
D	CLD	CMP abs,Y	PHX	STP		CMP abs,X	DEC abs,X	BBS5 zp,rel
E	INX	SBC #imm	NOP		CPX abs	SBC abs	INC abs	BBS6 zp,rel
F	SED	SBC abs,Y	PLX			SBC abs,X	INC abs,X	BBS7 zp,rel