DV1655/6 - Assignment 3

Intermediate representation, code generation, and interpretation

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1 Introduction

In this assignment you are going to:

- 1. Traverse the AST and generate the Intermediate Representation IR (described in Section 2),
- 2. Option 1
 - Traverse the IR and generate the target bytecode code (described in Section 3.1), and
 - Interpret the target bytecode code (described in Section 3.2).
- 3. Option 2
 - Generate C/C++ code by traversing the IR (described in Section 4.1), and
 - Generate Assembly target code by traversing the IR (described in Section 4.2).

Accordingly, this assignment is split into three parts, (1) Intermediate Representation, (2) Byte-code or C/C++ Code Generation, and (3) Interpretation or Assembly code generation.

The goal of the first part is to traverse the AST to construct the intermediate representation (IR), which is a data structure that contains basic blocks and three-address code (TAC) instructions.

Depending on which option you choose, the goal of the second part of this assignment is to traverse the IR and for each of the three-address code instructions in the basic building blocks generate MiniJava byte-code or C/C++ code.

Depending on which option you choose, the goal of the third part is to write a stack-based machine interpreter, which can read and interpret MiniJava byte-code instructions, or generate assembly code.

The examination of this project is done through demonstrations during the lab sessions. It is expected that you submit the source code in Canvas. The source code should be compressed in a zip/tar file.

The solution should be implemented using C or C++, and it should be compiled and executed correctly on a Unix-based operating system.

1.1 Laboratory groups

Collaborative work in pairs of two students for this assignment is suggested. Please note that groups larger than two students are not permitted. While individual submissions are accepted, I strongly recommend working in pairs, as the project is designed for groups of two students.

Collaboration and discussions between laboratory groups are encouraged. However, it is essential to maintain the distinction between collaborative learning and plagiarism, as outlined in section 1.3 below.

1.2 Lecture support

In Canvas (the course management page), you should be able to find the Intermediate Representation lecture, which is related to the first part of the assignment. In that lecture we introduce the general concepts and theory for intermediate representation. We briefly describe the three address instructions and basic blocks intermediate representation and provide examples of how we can traverse the AST and construct the IR.

The code generation and interpretation lecture corresponds to the second and third part of this assignment (for option 1). In that lecture, we describe translation process from a control flow graph (in terms of basic blocks and three-address instructions) into byte-code. Furthermore, we describe the stack machine-based execution of the byte-code instruction (i.e. interpretation).

For students that choose option 2 for the second and third parts of the assignment, they are encouraged to have a look at the assembly code generation lecture for a brief overview of the x86-64 architecture.

1.3 Plagiarism

We emphasize the importance of academic integrity. Any work that is not your own must be appropriately referenced. Failure to do so will be considered plagiarism and reported to the university disciplinary board.

2 Part 1: Intermediate Representation (IR)

The first step of this assignment is to traverse the abstract syntax tree and generate the corresponding low-level intermediate representation.

At this point, it is presumed that the provided source code is lexically, syntactically, and semantically correct. This may be ascertained by verifying that the input test classes that you use are compiled by Java.

Design/Implement data structures (class hierarchy) similar to what we proposed in the Intermediate Representation lecture to store the three address instructions and the basic blocks (See slide: 11).

Write a tree traversal for generating three address instructions for expressions, and basic blocks for statements. Note that we distinguish two types of language constructs for which we need to perform some actions when generating the IR, expressions and statements. As mentioned in the lecture, expressions do not generate new basic blocks in the control flow graph, instead they simply add three address instructions to the current block, whereas statements do generate new blocks most of the times.

Write a function that prints the basic blocks into a dot format, similarly to what we have seen in the slides, and similarly to what we have done when generating the graphviz output for the AST. A simple example of what the output of the dot file should look like is provided in the example below.

Table 1: An example of a dot file (on the left) and the generated output (on the right) for a simple CFG. It corresponds to the translation of the while statements example from slide 20 of the Intermediate Representation lecture.

```
digraph {
                                                         block 0
    graph [splines=ortho]
          [shape=box];
    block_0 [label="block_0\n...\n"];
                                                         true
    block_1 [ shape=box label="block_1\n_\x_1\]
                                                                    false
                                                         block 1
       ];
                                                          x < 1
    block_1 -> block_2 [xlabel="true"];
    block_1 -> block_3 [xlabel="false"];
    block_2 [ shape=box label="block_2\na_\:=\b\n
                                                              true
                                                         block_2
                                                                     block_3
    block_3 [ shape=box label="block_3\n"];
                                                          a := b
```

2.0.1 Recommended approach

The recommended approach for constructing the intermediate representation is as follows:

- Create the objects/classes to store the information for the three-address code and basic blocks
- Write a method that prints the CFG in a dot file.
- Start translating expressions. The result should be one basic block with a list of three address code instructions.
- Continue with the translation of statements. The result should be multiple basic blocks linked together, each basic block may have multiple three address code instructions.

3 Part 2: Code generation

In this assignment, we presume that:

• Programs contain only *int* and *boolean* variable types, that is, we disregard *arrays* and *Identifier* types (i.e., class objects).

• Programs contain only local variables for a method, i.e., there are no public variables for a class.

Such assumptions apply only to step 2 and 3. You still need to be able to generate the correct IR for all MiniJava programs.

3.1 Option 1: Java Byte-Code

The target code that we use in this course is a simplified version of the Java byte-code. Note that, the generated target code is similar to the Java bytecode but is not executable by a real Java Virtual Machine (JVM). The aim is to simulate how byte-code is generated by the Java compiler and then how such code is interpreted by JVM. For students that are interested to see what are the differences between our version of the byte-code and the actual Java byte-code, Section A in the Appendix, shows an example and summarizes the main differences.

Assuming that you have a list of entry points in your IR, where each entry point corresponds to the first block of a method, to generate byte-code you need to do the following:

- Loop over the list of entry points, and traverse the CFG starting from the entry point
- Visit all TAC instructions of the current block, and generate byte-code instructions (see Table 2 for the full list of instructions needed)
- Visit the trueExit block and the falseExit block.
- Keep track of the visited blocks to avoid generating target code for already visited blocks

The main byte-code instructions that will be needed for this assignment include (1) instructions to load and store values into the stack (iload, iconst, istore); (2) instructions to perform arithmetic and logic operations (iadd, isub, imul, idiv, ilt, igt, ieq, iand,ior, inot); (3) jump instructions (goto, iffalse goto); (4) method calls (invokevirtual); and (5) return, print, and exit instructions. The full set of instructions and their description is listed in Table 2.

The output of the code generation phase is a file that contains either:

- A readable java byte-code, similar to the examples in the appendix (Table 3)
- A serialized file that contains the necessary information to reconstruct the object files that represent the byte-code instructions. Note that you still need a method for pretty printing the byte-code.

Note that the generated class files does not necessarily need to be compatible with the examples that I have provided. It should not necessarily be the same with the format that other groups will use either. It is important though that the format of the class file is readable and executable by the interpreter (see Part 3).

3.1.1 Recommended approach

For this part of the assignment, I suggest the following approach:

- You may need to design data structures for storing the byte-code instructions. Add a function that can write such instruction to a file or simply serialize an object to a file.
- Start with the simplest example (E.java), which has two simple print statements, each with a specific expression, an arithmetic expression and a logical expression.
- Continue adding more features, such as different statements, including assignment statements, ifelse statements and while statements (D1.java and D2.java).

	Table 2: The list of bytecode instructions used for MiniJava
Instruction	Description
iload n	Push integer value stored in local variable n.
iconst v	Push integer value v.
istore n	Pop value v and store it in local variable n.
iadd	Pop value v1 and v2. Push v2 + v1.
isub	Pop value v1 and v2. Push v2 - v1.
imul	Pop value v1 and v2. Push v2 * v1.
idiv	Pop value v1 and v2. Push v2 / v1.
ilt	Pop value v1 and v2. Push 1 if $v2 < v1$, else push 0.
igt	Pop value v1 and v2. Push 1 if $v2 > v1$, else push 0.
ieq	Pop value v1 and v2. Push 1 if $v2 == v1$, else push 0.
iand	Pop value v1 and v2. Push 0 if v1 * v2 == 0, else push 1.
ior	Pop value v1 and v2. Push 0 if v1 + v2 == 0, else push 1.
inot	Pop value v. Push 1 if $v == 0$, else push 0.
goto i	Jump to instruction labeled i unconditionally.
iffalse goto i	Pop value v from the data stack. If $v == 0$ jump to instruction labeled i, else continue with the following instruction.
invokevirtual m	Push current activation to the activation stack and switch to the method having qualified name m.
ireturn	Pop the activation from the activation stack and continue.
print	Pop the value from the data stack and print it.
stop	Execution completed.

Table 2: The list of bytecode instructions used for MiniJava

- Add the functionality to generate code for recursive calls (D3.java).
- Add the functionality to generate code for nested if-else and while statements (C1.java and C2.java).
- Add functionality for handling multiple methods and classes (B.java).
- Add functionality for generating code for multi-level nested method calls (A.java).

3.2 Option 2: C/C++ Code generation

In this step, it is expected that you traverse the Control Flow Graph and generate valid C/C++ code. The generated code should be ready to compile and execute by a regular C/C++ compiler. When the generated code is compiled and executed the results must match the Java interpreter on the source program.

Assuming that you have a list of entry points in your IR, where each entry point corresponds to the first block of a method, to generate byte-code you need to do the following:

- Loop over the list of entry points, and traverse the CFG starting from the entry point
- Visit all TAC instructions of the current block, and generate the corresponding C/C++ instructions.
- Visit the trueExit block and the falseExit block.
- Keep track of the visited blocks to avoid generating target code for already visited blocks

4 Part 3: Byte-code interpretation or Assembly code generation

For the last part of the assignment, depending on which option you chose, you have to either write a byte-code interpreter or generate Assembly code.

4.1 Option 1: Stack-machine based interpretation

The interpreter is a **separate** program, which reads the output generated from step 2, and executes the instructions inside the program.

Depending on what the output file from step 2 is, you may need to:

- Read the file line by line (i.e., parse), reconstruct the byte-code instructions and interpret them, or
- Read the file, deserialize it, and interpret the instructions.

Table 2 provides a comprehensive description of the bytecode instructions, which may be utilized to gain a thorough understanding of the proper interpretation for each of instruction.

It is highly advisable to listen to the lecture about code generation and interpretation, as it offers valuable insights into the underlying mechanics of the stack-machine based execution.

Note that the stack-machine based interpreter uses two stacks, the data stack that is used to keep track of the data values, and the activation stack that keeps track of the previously called but not completed methods. This means that a recursive call results with multiple items in the activation stack.

4.1.1 Recommended approach

The suggested approach for the stack-machine based interpreter mirrors the techniques utilized for code generation, which basically mean that you start from the simplest examples and progressively advance towards the more complex language constructs.

- You may need to write a function that reads a file and constructs the byte-code instructions from it, or simply describing an object.
- Start with interpreting the simplest language constructs, i.e. expressions, including arithmetic and logical expressions (E.java).
- Continue adding more features to the interpreter, such as interpreting different statements, including assignment statements, if-else statements and while statements (D1.java and D2.java).
- Add the functionality to interpret instructions related to recursive calls (D3.java).
- Add the functionality to interpret instructions related to nested if-else and while statements (C1.java and C2.java).
- Add functionality for interpret byte-code blocks that correspond to multiple methods and classes (B.java).
- Add functionality for interpreting instructions for multi-level nested method calls (A.java).

4.2 Option 2: Assembly code generation

Copy the code that generates the C/C++ code and start replacing the functionality and generate assembly code (utilize inline assembly __volatile__ _asm__(...)). It is recommended that you follow some incremental approach, where you start with simpler language constructs (such as basic arithmetic and logical operations) and you will progressively tackle more complex constructs (such as recursive calls).

The corresponding functionality that is expected to be implemented in assembly includes:

- Arithmetic and Logical Operations (E. java):
 - Begin with translating basic arithmetic (+, -, *, /) and logical (&&, ||, !) operations.
- Loop Constructs (D1.java and D2.java):

- Implement loop constructs.
- Conditional Branching (D3. java):
 - Translate if-else statements.
- Nested branching and loop constructs (C1.java and C2.java):
 - conditional branches inside loop constructs and the other way around.
- Function Calls (B.java):
 - Handle function definitions, calls, and parameter passing
- Recursive Function Calls (A.java):
 - Handle recursive function calls

5 Testing

For the first part of the assignment, that is constructing the intermediate representation, you can use the test classes from the $test_files/valid$ folder provided as part of the assignment. Constructing the IR for any given MiniJava program is part of the minimum requirements to pass this assignment. For the second and third part of this assignment, a set of classes with limited functionality is provided in the $test_files/assignment3_valid$ folder. You are encouraged to write other test classes to test various functionalities of your compiler.

6 Examination

During the demonstration:

- Your compiler should be easily compiled using the Makefile.
- You should be able to explain specific parts of the solution. For example, part sof the code that are responsible for traversing the AST and generating the IR. I may ask to explain how the IR is built for a particular feature of the MiniJava. You may also expect questions related to how the code generator and the interpreter works.
- You are expected to show the IR visually for each of the test classes in the test_files/valid folder.
- You are expected to show the generated byte-code, and eventually explain parts of it.
- You are expected to run each of the test programs and show that the result of the interpretation of the generated code is the same as the one of the java compiler.
 - Compile each test program with java, and observe the result
 - Compile each test program with your compiler, and observe the result

7 Grading Scheme

The minimum requirements for this assignment include:

- \bullet The IR should be generated for all of the MiniJava programs (i.e. all test classes from the $test_files/valid$ folder) and pretty printed in .dot file.
- Option 1
 - The code generator should work for the E. java example.
 - The interpreter should be able to interpret the generated code for the E.java example.

- ullet Option 2
 - The generated C/C++ code works for all test cases
 - The assembly code works for E.java

Higher grades can be achieved by implementing the code generator and interpreter for the rest of the MiniJava features available in the other java class examples.

A MiniJava byte-code vs Java byte-code

An example that highlights the differences between our version of byte-code and the java byte-code is shown below, in Table 3. Here we use a simple example, that calculates the sum of all number from 0 to N (in this case N = 100). Note that here we present two alternatives for our byte-code.

In the first one we use labels, such as Sum.main or 0, 1, 2, ...; Lettered labels correspond to the qualified names of methods, whereas numbered labels inside a method correspond to the instruction number.

In most cases the labeled numbers will not be used. The only ones that have meaning are the ones that are used inside a goto instruction (in this case in *Test.Sum*, *label 3* and *16*).

Alternatively, you may use the second representation, where we only add labels for method names and blocks that are used in goto instructions (in this case, in *Test.Sum*, *block_2* and *block_3*).

Another difference between representation 1 and 2, is that in the first one we use indices to access local variables (example, #0 corresponds to num; and #1 to sum), whereas in the second representation, we use the variable names (example num, sum). Note that variable names are unique inside a scope. You could also combine using numbered labels for instructions and variable names. All variants area cceptable.

Sum.main Sum.main	Test example	Test example Our byte-code (1) Our byte-code (2) Java Byte-code	Our byte-code (2)	Java Byte-code	
Sum.main					
		Sum.main	Sum.main:	class	c class
Sum		iconst	iconst 100	//constructor	//constructor
Sum	g □		nvokevirtual		
Stop	¥	.Sum	Sum	c static	int
Test.Sum	System.out.println		print	main();	arg0);
Test.Sum Test.Sum Test.Sum Lest.Sum Lest.Sum	(new Test().Sum		stop	ФО	iconst_
Test.Sum Test.Sum; [16] 2 iconst_0 istore num istore num istore num 6 dup 4 if_icmplt 2 istore #1 istore sum 7 invokespecial 7 iconst_0 1 iconst 0 7 invokespecial 7 iconst_0 1 iconst_0 7 invokespecial 7 iconst_0 1 iconst_0 1 iconst_0 1 iconst_0 1 iconst_0 1 iconst_1 1 iconst_0 1 iconst_1 1 iconst_	(100));				istore_
Test 1	~	Φ	Test.Sum:	. [16	iconst_0
Test { I iconst 0	٨.	istore		new Test	iload_1
ublic int Sum(int 2 istore #1 istore sum into sum; a iconst 0 istore sum into sum; a iconst 0 istore forms 1 istore 2 Tinovkespecial rest 0 7 iconst 0 1 iconst 1 7 iconst 1 1 iconst 1 8 goto 12 1 iconst 1 1 iconst 2 1 iconst 3 1 iconst 3 1 iconst 3 1 iconst 1 1 iconst 1 1 iconst 1 2 iconst 1 2 iconst 2 3 iconst 2	class Test {				if_icmplt
Int sum; S iconst 0 block_2: Test() [19] S goto 12 Int sum; S iload #0 iconst 0 10 bipush 100 11 iconst_1 S iload #1 iload mum					iconst_
int sum; 4 iload #0 iconst 0 10 bipush 100 11 iconst_1 sum = 0; silt iload num 12 invokevirtual 12 ifeq 26 while (0 < num) {	} (mnu	iconst	$block_2:$		
sum = 0; 5 ilt iload num 12 invokevirtual 12 ifeq 26 while (0 < num) {	int sum;			bipush	
while (0 < num) { 6 if_false 16 ilt Test.Sum(int): 15 iload_2 sum = sum + num; 7 iload #1 iffalse goto int [22] 16 iload_1 num = num - 1; 8 iload #0 block_3 15 invokevirtual 17 iad sum = num - 1; 10 istore #1 iload num .println(int): 19 iload_1 return sum; 11 iload #0 istore sum la istore sum la return 22 istore_1 12 iconst 1 istore num la istore num la istore num la istore num 22 istore_1 16 iload #1 istore num block_2: 27 ireturn 17 ireturn block_3: licaturn			ъq		ifeq 2
sum = sum + num; 7 iload #1 iffalse goto int [22] 16 iload_1 [5] num = num - 1; 8 iload #0 block_3 15 invokevirtual 17 iadd stoad iload sum java.io.PrintStream 18 istore_2 return 10 istore #1 iload num .println(int): 19 iload_1 [5] 12 iconst 1 istore sum 18 return 20 iconst_1 [5] 13 isub istore num 22 istore_1 [5] 15 goto 3 istob 26 iload_2 [5] 16 iload #1 goto block_2 [5] 27 ireturn 17 ireturn block_3: iload sum	(mnu >	if_false	ilt		iload_
S iload #0 block_3 15 invokevirtual 17 iadd	mns =			int [22]	iload_1
10 istore #1 10 istore #1 10 istore #2 10 istore #1 10 istore #1 10 istore #1 10 istore #1 10 istore sum 18 return 19 iload 1 12 iconst 1 13 isub 14 istore #0 15 istore num 15 goto 3 16 iload #1 17 ireturn 17 ireturn 18 iload sum 19 iload sum 19 iload sum 19 iload sum 19 iload sum 10 i	- wnu = wnu	iload	.ock_	15 invokevirtual	7
10 istore #1 iload num	~			java.io.PrintStream	istore_2
11 iload #0 iadd void [28] 20 iconst_1 12 iconst 1 iload num 3 21 isub 14 istore #0 iconst 1 23 goto 2 15 goto 3 istore num 27 ireturn 17 ireturn block_3: 27 ireturn iload sum ireturn 3	return	istore	ಧ	tln(int)	9 iload_1
2 iconst 1 istore sum 18 return 21 isub 3 isub iconst 1 23 goto 2 4 istore #0 isub 26 iload_2 5 goto 3 istore num 27 ireturn 6 iload #1 goto block_2 27 ireturn 7 ireturn block_3: iload sum ireturn	۲-	iload	iadd	void	O iconst_
3 isub iload num } 3 isub 22 istore_1 4 istore #0 isub 26 iload_2 6 iload #1 goto block_2 7 ireturn block_3: iload sum ireturn ireturn	~	iconst			1 isub
4 istore #0 isub 5 goto 3 isub 6 iload #1 istore num 7 ireturn block_3:		isub	iload num	~	2 istore_1
<pre>5 goto 3</pre>		istore			3 goto
6 iload #1 istore num 7 ireturn goto block_2 block_3:		goto 3	isub		6 iload_
ireturn goto block_ block_3: iload sum ireturn		6 iload	store		7 i
block_3: iload sum ireturn					::
iload sum ireturn			block_3:		~~
			iload sum ireturn		
			1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		