

# 1. Description

# 1.1. Project

Project Name	mlx90614_driver_library_project
Board Name	custom
Generated with:	STM32CubeMX 6.11.1
Date	06/24/2024

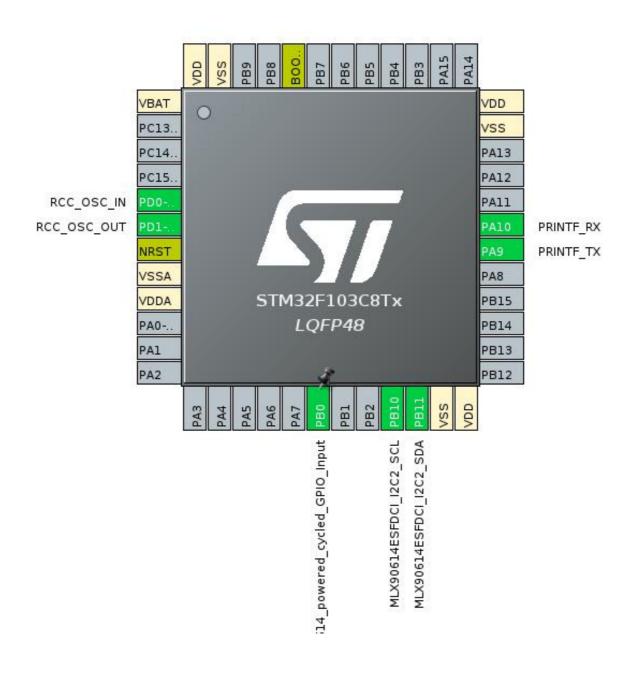
### 1.2. MCU

MCU Series	STM32F1
MCU Line	STM32F103
MCU name	STM32F103C8Tx
MCU Package	LQFP48
MCU Pin number	48

# 1.3. Core(s) information

Core(s)	Arm Cortex-M3

# 2. Pinout Configuration

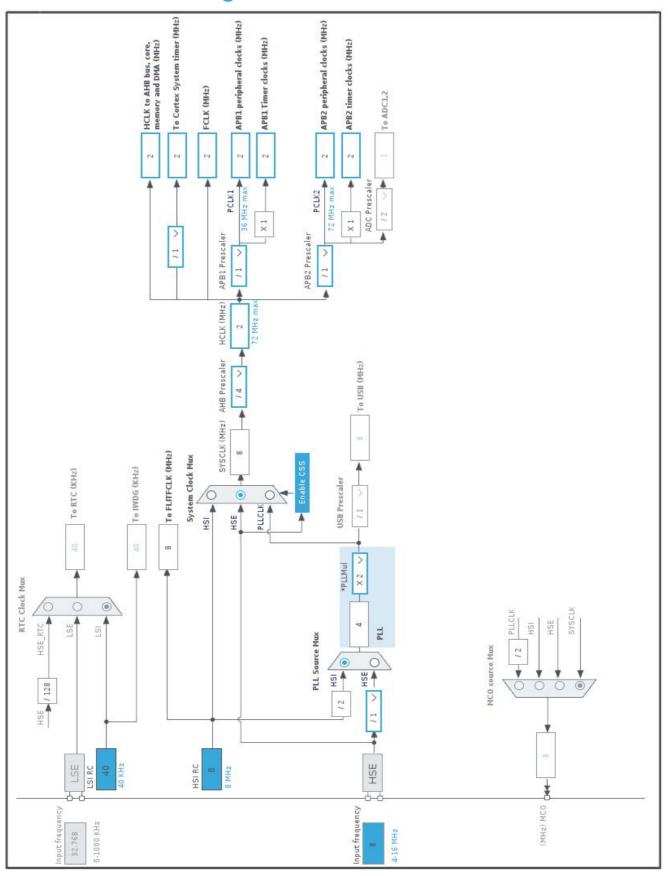


# 3. Pins Configuration

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
5	PD0-OSC_IN	I/O	RCC_OSC_IN	
6	PD1-OSC_OUT	I/O	RCC_OSC_OUT	
7	NRST	Reset		
8	VSSA	Power		
9	VDDA	Power		
18	PB0 *	I/O	GPIO_Input	is_MLX90614_powered_cycl ed_GPIO_Input
21	PB10	I/O	I2C2_SCL	MLX90614ESFDCI_I2C2_S CL
22	PB11	I/O	I2C2_SDA	MLX90614ESFDCI_I2C2_S DA
23	VSS	Power		
24	VDD	Power		
30	PA9	I/O	USART1_TX	PRINTF_TX
31	PA10	I/O	USART1_RX	PRINTF_RX
35	VSS	Power		
36	VDD	Power		
44	BOOT0	Boot		
47	VSS	Power		
48	VDD	Power		

<sup>\*</sup> The pin is affected with an I/O function

# 4. Clock Tree Configuration



# 5. Software Project

### 5.1. Project Settings

Name	Value	
Project Name	mlx90614_driver_library_project	
Project Folder	/home/dannyca/Desktop/Mortrack/stm32_projects/mlx90614_driver_library_proje	
Toolchain / IDE	STM32CubeIDE	
Firmware Package Name and Version	STM32Cube FW_F1 V1.8.5	
Application Structure	Advanced	
Generate Under Root	Yes	
Do not generate the main()	No	
Minimum Heap Size 0x200		
Minimum Stack Size 0x400		

## 5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	
Enable Full Assert	No

## 5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO
3	MX_USART1_UART_Init	USART1
4	MX_I2C2_Init	I2C2

# 2. Peripherals and Middlewares Configuration

### 2.1. I2C2 I2C: I2C

### 2.1.1. Parameter Settings:

#### **Master Features:**

I2C Speed Mode Standard Mode

I2C Clock Speed (Hz) 100000

**Slave Features:** 

Clock No Stretch Mode Disabled

Primary Address Length selection 7-bit

Dual Address Acknowledged Disabled

Primary slave address 0

General Call address detection Disabled

#### 2.2. RCC

### High Speed Clock (HSE): Crystal/Ceramic Resonator

### 2.2.1. Parameter Settings:

#### **System Parameters:**

VDD voltage (V) 3.3
Prefetch Buffer Enabled

Flash Latency(WS) 0 WS (1 CPU cycle)

**RCC Parameters:** 

HSI Calibration Value 16
HSE Startup Timout Value (ms) 100
LSE Startup Timout Value (ms) 5000

#### 2.3. SYS

**Debug: No Debug** 

Timebase Source: SysTick

2.4. USART1

**Mode: Asynchronous** 

### 2.4.1. Parameter Settings:

#### **Basic Parameters:**

Baud Rate 115200

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

**Advanced Parameters:** 

Data Direction Receive and Transmit

Over Sampling 16 Samples

<sup>\*</sup> User modified value

# 3. System Configuration

# 3.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
I2C2	PB10	I2C2_SCL	Alternate Function Open Drain	n/a	High *	MLX90614ESFDCI_I2C2_ SCL
	PB11	I2C2_SDA	Alternate Function Open Drain	n/a	High *	MLX90614ESFDCI_I2C2_ SDA
RCC	PD0- OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PD1- OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
USART1	PA9	USART1_TX	Alternate Function Push Pull	n/a	High *	PRINTF_TX
	PA10	USART1_RX	Input mode	No pull-up and no pull-down	n/a	PRINTF_RX
GPIO	PB0	GPIO_Input	Input mode	Pull-down *	n/a	is_MLX90614_powered_cy cled_GPIO_Input

## 3.2. DMA configuration

nothing configured in DMA service

## 3.3. NVIC configuration

# 3.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority	
Non maskable interrupt	true	0	0	
Hard fault interrupt	true	0	0	
Memory management fault	true	0	0	
Prefetch fault, memory access fault	true	0	0	
Undefined instruction or illegal state	true	0	0	
System service call via SWI instruction	true	0	0	
Debug monitor	true	0	0	
Pendable request for system service	true	0	0	
System tick timer	true	15	0	
PVD interrupt through EXTI line 16	unused			
Flash global interrupt		unused		
RCC global interrupt	unused			
I2C2 event interrupt	unused			
I2C2 error interrupt	unused			
USART1 global interrupt	unused			

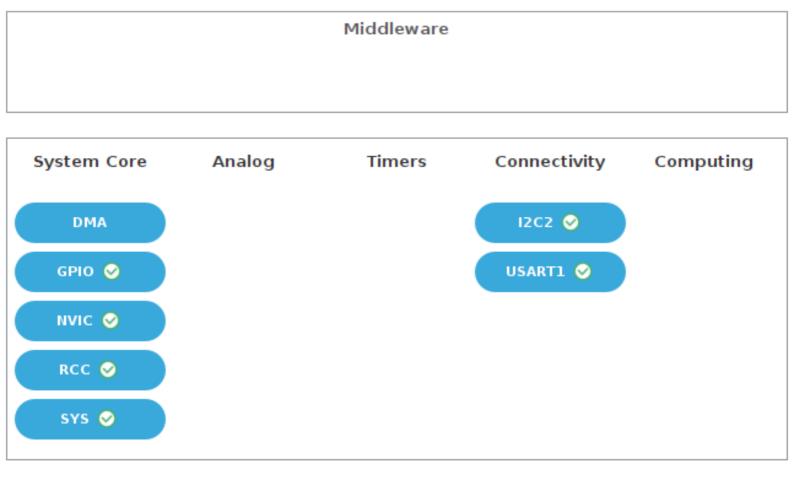
### 3.3.2. NVIC Code generation

Enabled interrupt Table	Select for init	Generate IRQ	Call HAL handler
	sequence ordering	handler	
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Prefetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	true	false
Debug monitor	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true

### \* User modified value

# 4. System Views

- 4.1. Category view
- 4.1.1. Current



# 5. Docs & Resources

Type Link

BSDL files https://www.st.com/resource/en/bsdl\_model/stm32f1\_bsdl.zip

IBIS models https://www.st.com/resource/en/ibis\_model/stm32ibis.zip

System View https://www.st.com/resource/en/svd/stm32f1\_svd.zip

Description

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