

رَجیستر	پایه کنترل کننده	تابع کنترل
AC	LD	$(D0+D1+D2)T5+pB11+(B9+B7+B6)r$
	INC	rB5
	CLR	rB11
	OE	$D3T4 + pB10$
AR	LD	$D7'IT3 + (T2 + T0)R'$
	INC	D5T4
	CLR	RT0
	OE	$D4T4 + D5T5$
DR	LD	$(D0 + D1 + D2 + D6)T4$
	INC	D6T5
	OE	$D2T5 + D6T6$
E	J	$rB8 + D1T5Cout + rB7AC0 + rB6AC15$
	K	$rB8 + D1T5Cout' + rB7AC0' + rB6AC15'$
	CLR	rB10
FGI	CLR	pB11
FGO	CLR	pB11
IEN	J	pIR(7)
	K	pIR(6)+RT2'
INR	OE	pB11
IR	LD	R'T1
	OE	RT2
OUTR	LD	pB10
Memory	RD	$R'T1+D7'IT3+(D0+D1+D6)T4$
	WR	$RT1+D6T6+(D3+T5)T4$
	CS	$R'T1+D7'IT3+(D0+D1+D2+D6)T4$
PC	LD	$D4T4 + D5T5$
	INC	$R'T1 + RT2 + (DR=0)D6T6 + AC15rB4 + AC15rB3 + (AC=0)rB2 + E'rB1 + (FGI)pB9 + (FGO)pB8$
	CLR	RT1
	OE	$T0 + D5T4$
SC	CLR	$RT2 + r + p + (D0+D1+D2+D5)T5 + (D3 + D4)T4 + D6T6$
Designed By Mostafa Fazli		