# Generierung des Eingangssingals für Barrier Bucket RF Systeme and der GSI



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Projektseminar Beschleunigertechnik



#### **Outline**

- 1 Einführung
  - Problemstellung
  - Aufbau
- 2 Gerätekommunikation
- 3 Code
  - Design
  - Vorgehensweise
  - Evaluierung
- 4 Ausblick
- 5 Quellen

# **Problemstellung**

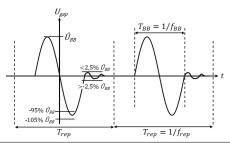
- Barrier-Bucket System
- Ziel

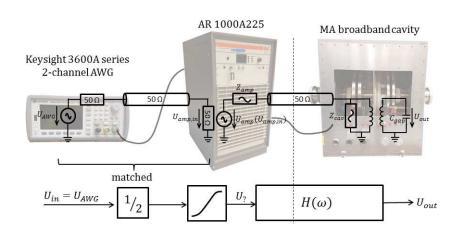
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- Barrier-Bucket System :
  - Longitudinale Manipulation der Bunches
- Ziel

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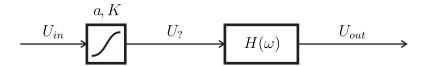
- Barrier-Bucket System :
  - Longitudinale Manipulation der Bunches
- Ziel :
  - Gap Spannung in Form einer Ein-Sinus Periode
  - Qualität das Signals



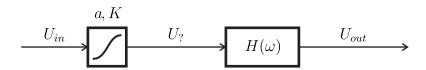


Hammerstein Modell

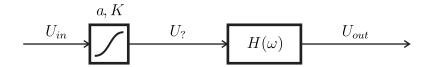
Zielsetzung



- Hammerstein Modell:
  - System ist linear bis  $\hat{U}_{BB} \approx 550 V$
  - Ergänzung um eine nichtlineare Vorverzerrung
  - Potenzreihenansatz  $U_{?}(t) = \sum_{n=1}^{N} a_n [U_{in}(t)]^n$
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  - Parameter an der Kennlinie zubestimmen



#### **Dokumentation und Gerätekommunikation**

Dokumentation

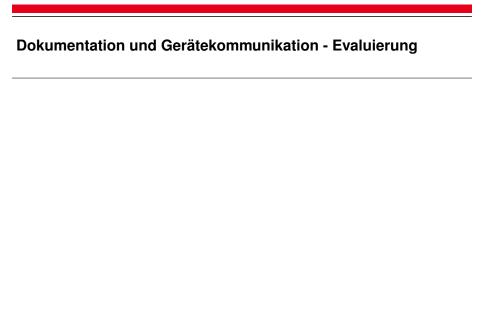
Gerätekommunikation

#### **Dokumentation und Gerätekommunikation**

- Dokumentation :
  - Handhabung der Geräte, Vorgehensweise bei Tests
  - Bedienung des Programms
  - Ausführliches Kommentieren der Code-Funktionalität
- Gerätekommunikation

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- Dokumentation :
  - Handhabung der Geräte, Vorgehensweise bei Tests
  - Bedienung des Programms
  - Ausführliches Kommentieren der Code-Funktionalität
- Gerätekommunikation :
  - Treiber und Programmer-Manuals zur Nutzung des Programms von anderen Geräten aus
  - Laufzeitoptimierung durch Abfrage von Gerätezuständen mittels VISA
  - Verbesserung der Auflösung des Signals durch Anpassung der Darstellung des Oszilloskops mittels VISA



# Dokumentation und Gerätekommunikation - Evaluierung

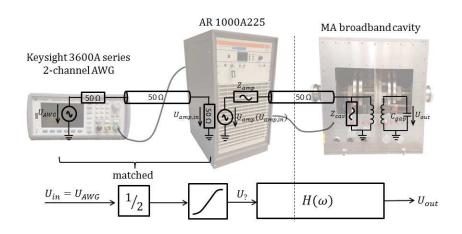
- Unvollständige Dokumentation:
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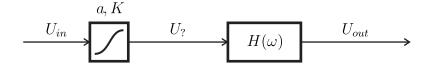
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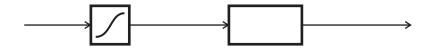
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  - VISA-Protokoll und PyVisa Package Installation
  - Kommunikation mit AWG von anderem Laptop aus über USB

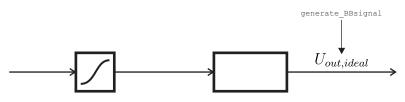
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  - Kommunikation mit Oszilloskop von anderem Laptop
  - Laufzeit: Status-Abfrage der Geräte mit BUSY? oder \*WAI
  - Anpassung der Auflösung des DSO

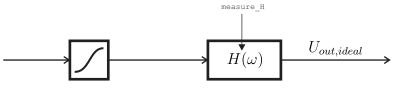




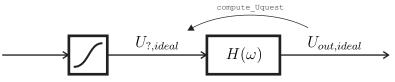




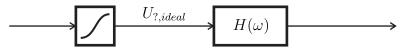
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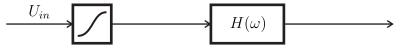
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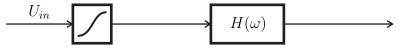
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U_{?,ideal} \longrightarrow H(\omega) \longrightarrow
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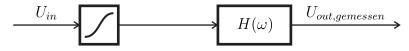
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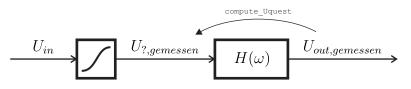
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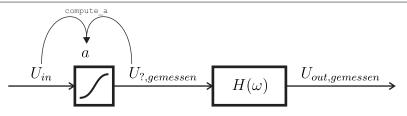
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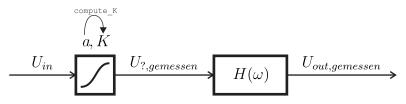
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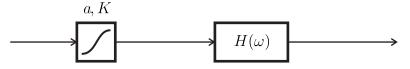
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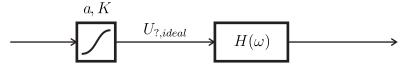
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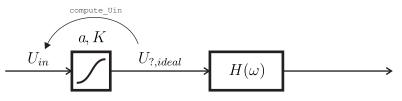
K = compute_K ( a )
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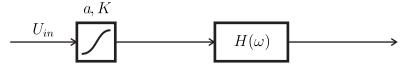
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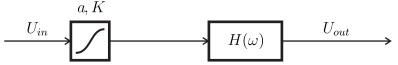
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Uout = measure_Uout ( Uin )
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#### Code: die Bausteine

```
generate_BBsignal
measure_H
compute_Uquest
compute_Uin
measure_Uout
compute_a
compute_K
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generate_BBsignal : musste implementiert werden
measure_H
compute_Uquest
compute_Uin
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measure_H : bereits gegeben in Python
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compute_a
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 ${\tt generate\_BBsignal: muss te implementiert werden}$ 

 ${\tt measure\_H}$  : bereits gegeben in Python

compute\_Uquest : zum Teil gegeben in Matlab und Python

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measure\_Uout : writeAWG.py und readDSO.py waren gegeben

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Refactoring / Anpassung der Matlab-Funktionen an unser Design

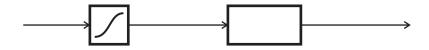
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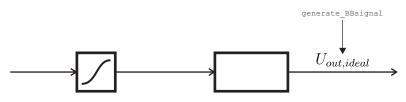
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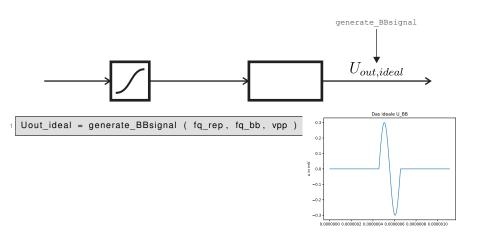
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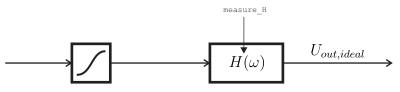
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  - Nur zum Testen von measure\_Uout sind Geräte notwendig



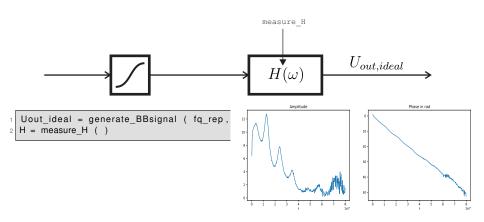


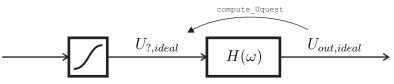
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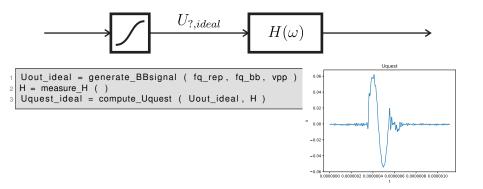


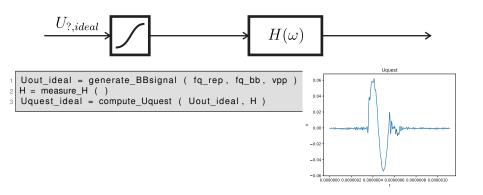
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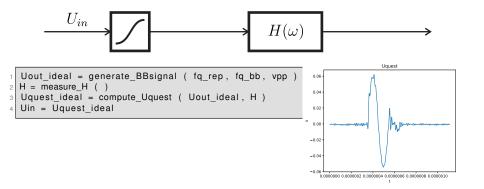


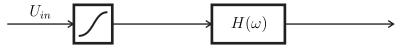


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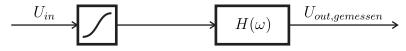




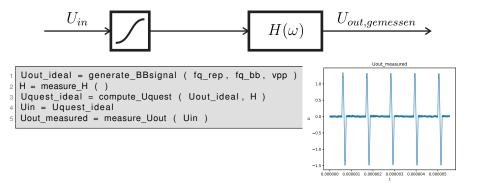


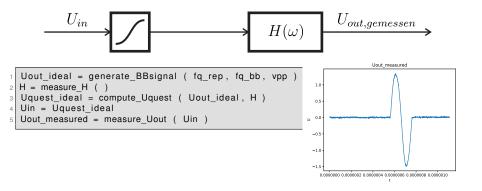


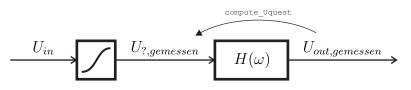
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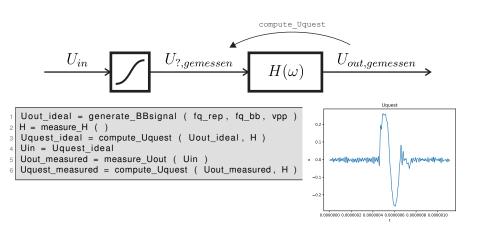
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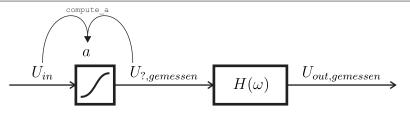




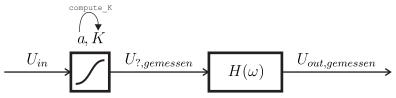


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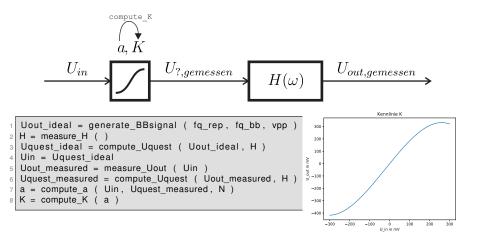


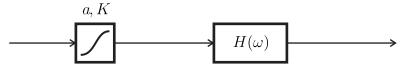


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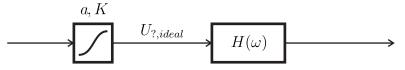


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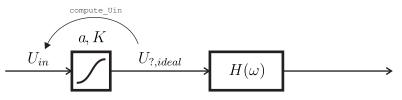




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Uquest_ideal = compute_Uquest ( Uout_ideal , H )

Uin = Uquest_ideal

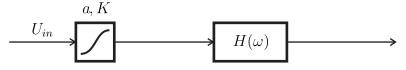
Uout_measured = measure_Uout ( Uin )

Uquest_measured = compute_Uquest ( Uout_measured , H )

a = compute_a ( Uin , Uquest_measured , N )

K = compute_K ( a )

Uin = compute_Uin ( Uquest_ideal , K )
```



```
Uout_ideal = generate_BBsignal ( fq_rep , fq_bb , vpp )

H = measure_H ( )

Uquest_ideal = compute_Uquest ( Uout_ideal , H )

Uin = Uquest_ideal

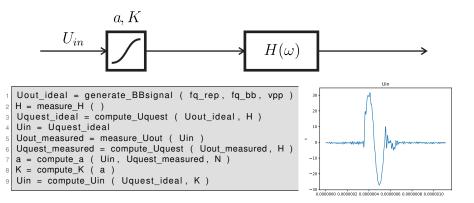
Uout_measured = measure_Uout ( Uin )

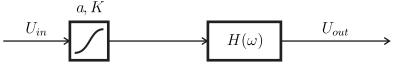
Uquest_measured = compute_Uquest ( Uout_measured , H )

a = compute_a ( Uin , Uquest_measured , N )

K = compute_K ( a )

Uin = compute_Uin ( Uquest_ideal , K )
```





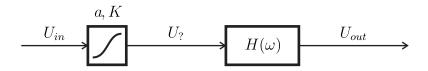
```
Uout_ideal = generate_BBsignal ( fq_rep, fq_bb, vpp )
H = measure_H ( )
Uquest_ideal = compute_Uquest ( Uout_ideal, H )
Uin = Uquest_ideal
Uout_measured = measure_Uout ( Uin )
Uquest_measured = compute_Uquest ( Uout_measured, H )
a = compute_a ( Uin, Uquest_measured, N )
K = compute_K ( a )
Uin = compute_Uin ( Uquest_ideal, K )
Uout = measure_Uout ( Uin )
```

#### **Ausblick**

Iterative Optimierung der linearen Übertragungsfunktion mittels Auswertung der erwarteten und gemessenen Ausgangssignale U<sub>out</sub>:

$$\underline{\underline{H}}^{\mathsf{neu}}\left(\omega\right) = \underline{\underline{H}}^{\mathsf{alt}}\left(\omega\right) \cdot \frac{\underline{\underline{U}}_{out,\mathsf{ideal}}\left(\omega\right)}{\underline{\underline{U}}_{out,\mathsf{mess}}\left(\omega\right)} \cdot \sigma_{H}$$

mit  $\sigma_H$  als Schrittweite der jeweiligen Iteration

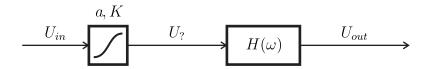


#### **Ausblick**

 Optimierung der nichtlinearen Kennlinie mittels Vergleich der Differenz der erwarteten und gemessenen Spannungssignale Uquest und der Faktoren a der polynomialen Kennlinie:

$$\Delta U_? = U_{?,\text{mess}} - U_{?,\text{berechnet}} = \sum_{n} \tilde{a}_n U_{in}^n$$
  $a_n^{\text{neu}} = a_n^{\text{alt}} + \sigma_a \cdot \tilde{a}_n$ 

mit  $\sigma_a$  als Schrittweite der jeweiligen Iteration



# Offene Fragen

- Reihenfolge der Optimierung: Parallele Iteration ⇔ alternierende Iteration von H und K
- Einfluss von K auf das Spektrum von U<sub>?</sub> und damit auf Optimierung von H durch Oberschwingungen bei Potenzierung des Eingangssignals
- Bewertung der Qualität des Ausgangssignals nach einem Iterationsschritt

#### Quellen

- Denys Bast, Armin Galetzka, "Projektseminar Beschleunigertechnik", 2017
- Jens Harzheim et al., "Input Signal Generation For Barrier Bucket RF Systems At GSI",
- Kerstin Gross et al., "Test Setup For Automated Barrier Bucket Signal Generation", 2017