

The block contains three circuit diagrams for USB power supply:

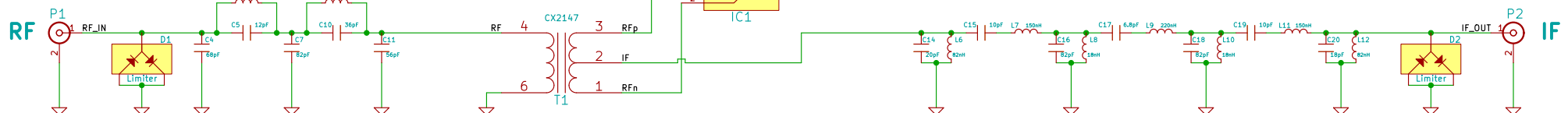
- USB-microB:** A schematic showing a USB-microB connector (U2) connected to a USB-A-Plug (U1). The plug's pins are labeled: 3 (DPA\_P), 2 (DPA\_N), 1 (VBUS\_CONN), 4 (GND), 5 (SHIELD), and 6 (DPA\_ID). The VBUS line passes through a PTC Fuse (F1) to the VBUS pin of the microB connector. The shield is connected to GND. The microB connector's pins are labeled: U2 (DPA\_P), U1 (DPA\_N), U2 (DPA\_ID), and U1 (DPA\_ID).
- USB-A-Plug:** A schematic showing a USB-A-Plug (U2) connected to a USB-A-Port (U1). The plug's pins are labeled: 3 (DPA\_P), 2 (DPA\_N), 1 (VBUS\_CONN), 4 (GND), 5 (SHIELD), and 6 (DPA\_ID). The VBUS line passes through a PTC Fuse (F1) to the VBUS pin of the microB connector. The shield is connected to GND. The microB connector's pins are labeled: U2 (DPA\_P), U1 (DPA\_N), U2 (DPA\_ID), and U1 (DPA\_ID).
- USB-A-Port:** A schematic showing a USB-A-Port (U2) connected to a USB-A-Plug (U1). The plug's pins are labeled: 3 (DPA\_P), 2 (DPA\_N), 1 (VBUS\_CONN), 4 (GND), 5 (SHIELD), and 6 (DPA\_ID). The VBUS line passes through a PTC Fuse (F1) to the VBUS pin of the microB connector. The shield is connected to GND. The microB connector's pins are labeled: U2 (DPA\_P), U1 (DPA\_N), U2 (DPA\_ID), and U1 (DPA\_ID).

A 74LVC1G125 buffer must be powered at 2.5V for it to handle a 100MHz+ signal.

- <0.3dB ripple below 46MHz
- <1.5dB ripple below 67MHz
- 3dB cutoff at 69MHz
- >40dB attenuation at >88MHz

L0 Level = +13dB

- Minimum -1dB outside 102MHz to 168MHz
- Minimum -3dB outside 100MHz to 171MHz
- Minimum -30dB outside 85MHz to 198MHz



**Opendous Inc.**

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