Mostafa Abbasmollaei

mostafa.abbasmollaei@polymtl.ca ✉

mostafa.abbasy@gmail.com ✉

LinkedIn Profile 𝗂𝗇

Homepage 🌐

Education

2022 – Present **Ph.D. / Computer Engineering, <u>Polytechnique Montreal</u>, Montreal, Canada**
Thesis: P4 applications on heterogeneous CPU/FPGA platform
GPA (so far): 4/4
Supervisor: <u>Prof. Tarek Ould-Bachir</u>
Co-supervisor: <u>Prof. Yvon Savaria</u>

2015 – 2018 **Master / Computer Engineering, <u>University of Tehran</u>, Tehran, Iran**
Thesis: Decreasing the Effect of Process Variation on Detection of Hardware Trojan in Nanotechnology
GPA: 18.36/20
Supervisor: <u>Prof. Siamak Mohammadi</u>

2011 – 2015 **Bachelor / Computer Engineering, <u>Shahid Beheshti University</u>, Tehran, Iran**
Thesis: Modeling of PID Controller with Finite Accuracy Computations
GPA: 17.12/20
Supervisor: <u>Prof. Hamidreza Mahdiani</u>

2007 – 2011 **High School Diploma in Physics and Mathematics Discipline, Qom, Iran**
National Organization for the Development of Exceptional Talents (NODET)
GPA: 19.4/20

Academic Experience

2015 – Present **Researcher, Dependable Systems Design Laboratory**
Electrical and Computer Engineering Department, University of Tehran

2013 – 2015 **Teacher Assistant**
Advanced Computer Programming, Algorithm Design, Theory of Machines and Languages, and Digital Systems and Circuits courses
at Computer Science and Engineering Department, Shahid Beheshti University

Work Experience

2018 – 2021 **Embedded System Developer / SAMIM Company, Tehran, Iran**
Embedded Linux, C++ Programming, Testing and Profiling Software

Skills

- VHDL and verilog
- Digital System Design
- ModelSim
- Linux OS
- C++ Programming
- Qt Framework
- Socket Programming
- Git and GitLab
- HSpice
- MATLAB
- Postgresql
- GIS Programming

Publications

- F. Hajizadeh, M. Binesh Marvasti, SA. Asghari, **M. Abbasmollaei**, AM. Rahmani, **Configurable DSI partitioned approximate multiplier**, *Future Generation Computer Systems*, Volume 115, 2021, Pages 100-114, ISSN 0167-739X, https://doi.org/10.1016/j.future.2020.09.008

- **M. Abbasmollaei**, F. Hajizadeh, S. Mohammadi, M. Binesh Marvasti, SA. Asghari, B. Samieiyan, **A power constrained approximate multiplier with a high level of configurability constraints,** *Microprocessors and Microsystems*, Volume 90, 2022, ISSN 0141-9331, https://doi.org/10.1016/j.micpro.2022.104519

- B. Samieiyan, P. MohammadiNasab, **M. Abbasmollaei**, F. Hajizadeh, M. Kangavari, **Solving dimension reduction problems for classification using Promoted Crow Search Algorithm (PCSA)**, *Computing*, 2022, https://doi.org/10.1007/s00607-021-01037-2

- B. Samieiyan, P. MohammadiNasab, **M. Abbasmollaei**, F. Hajizadeh, M. Kangavari, **Novel optimized crow search algorithm for feature selection**, *Expert Systems with Applications*, Volume 204, 2022, ISSN 0957-4174, https://doi.org/10.1016/j.eswa.2022.117486

Honors and Awards

2018	Ranked 3rd among 40 graduate students of Electrical and Computer Engineering, University of Tehran, Tehran, Iran.
2015	Ranked 87th in the Nationwide University Entrance Exam among over 80,000 participants (for Graduate Education).
2015	Ranked 3rd among 90 students of Computer Science and Engineering, Shahid Beheshti University, Tehran, Iran.

Certificates

2014 – 2015	**MCSE course at Tehran Institute of Technology** Windows 8, Windows Server 2012 R2, Administrating Windows Server 2012 R2
2021	**Duolingo English Test / Score: 115**