ECE 362
Mosiah Beal
HW 2

I didn't see any questions in the files provided, so I just filled in the comments left there. My makefile has a custom rule that I liked from my other courses, it makes object files from any c files in the directory. This prevents the makefile from getting cluttered with individual rules for each file.
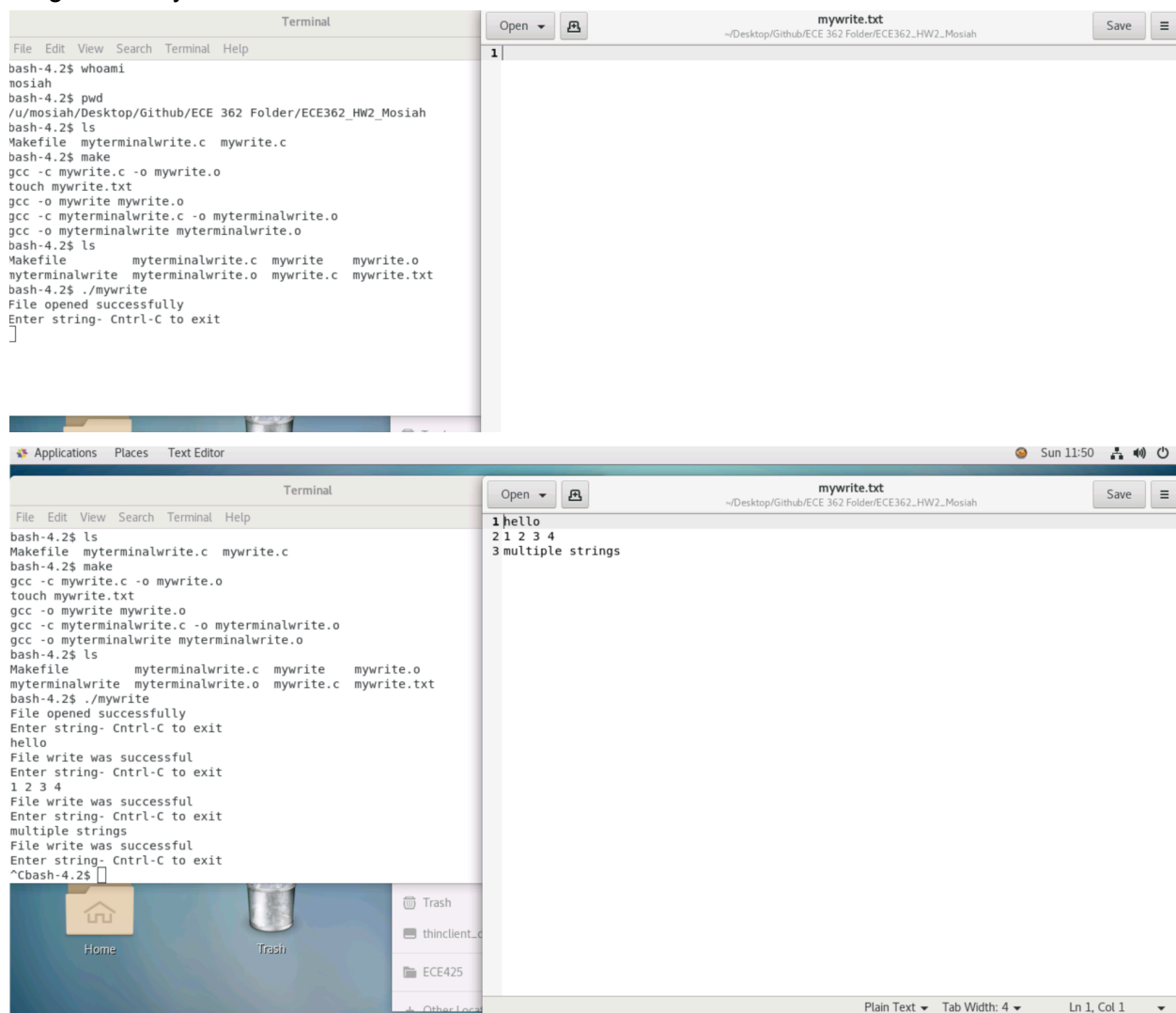
```
# Implicit rule for .c to .o
%.o: %.c
	gcc -c $< -o $@          You,
```
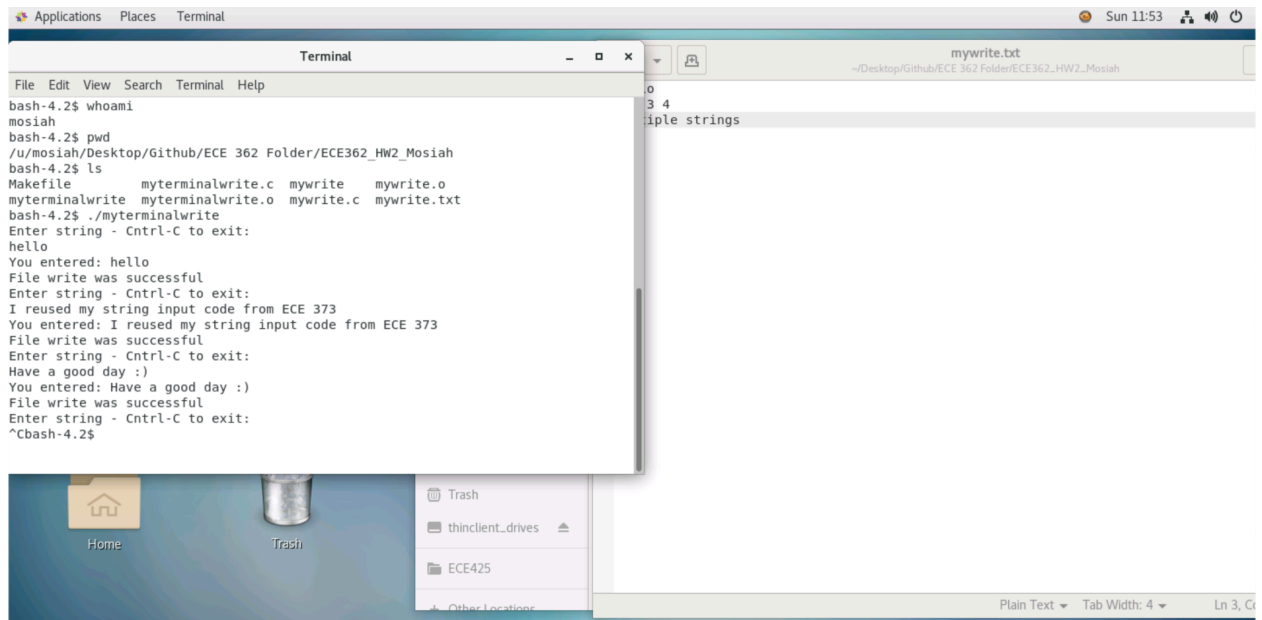
(The "You, " in the bottom right is from a revision blame for my GitHub repo, it isn't in the makefile)

Program 1: mywrite

Program 2: myterminalwrite

```
bash-4.2$ whoami
mosiah
bash-4.2$ pwd
/u/mosiah/Desktop/Github/ECE 362 Folder/ECE362_HW2_Mosiah
bash-4.2$ ls
Makefile          myterminalwrite.c  mywrite      mywrite.o
myterminalwrite   myterminalwrite.o  mywrite.c    mywrite.txt
bash-4.2$ ./myterminalwrite
Enter string - Cntrl-C to exit:
hello
You entered: hello
File write was successful
Enter string - Cntrl-C to exit:
I reused my string input code from ECE 373
You entered: I reused my string input code from ECE 373
File write was successful
Enter string - Cntrl-C to exit:
Have a good day :)
You entered: Have a good day :)
File write was successful
Enter string - Cntrl-C to exit:
^Cbash-4.2$
```