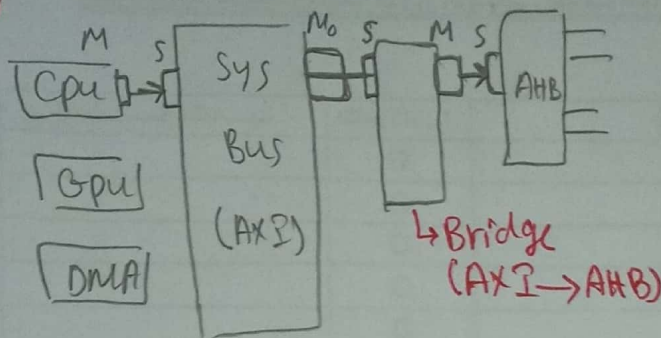


• Memory mapped:

$$\text{Add} = \text{Base} + \text{offset} \Rightarrow \text{TRM} =$$

transaction $\begin{cases} \text{data (1/0)} \\ \text{add (base)/(offset)} \\ \text{size} \end{cases}$

• Bus Bridges



#define GPIO (*(volatile u32*) (0))

GPIO |= (1 << 16);

TRM \Rightarrow General
Datasheet \Rightarrow Family

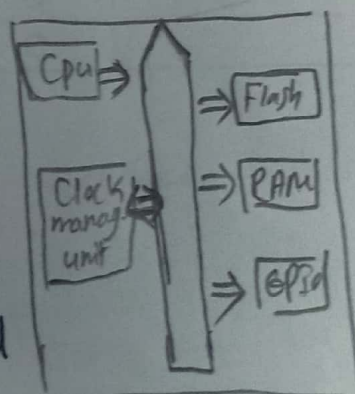
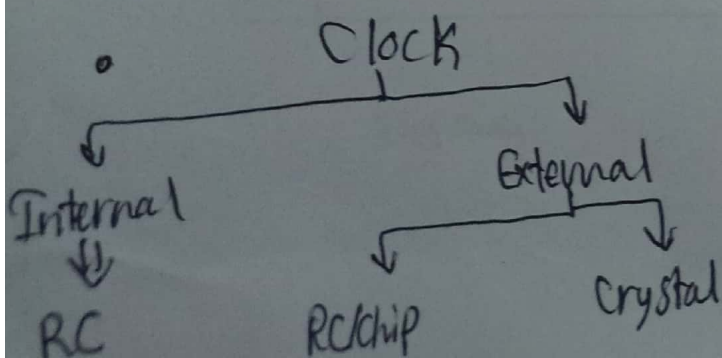
• AMBA: The ARM Advanced MicroController Bus Architecture.

AXI AHB reusability \uparrow , standard

~~AS~~ *

MCU clocks

- CLK \Rightarrow PSA \Rightarrow peripheral
- Clock Tree \Rightarrow TRM
- $f \propto$ power (use PSA to reduce them)
- Most MCs have (power management unit, clock management unit) \Rightarrow LATMEGA3
- By default (CLK \neq GPIO)



General clock topology