

Q1)

Micro-Processor:- Processor didn't have RAM, ROM and i/o Ports and it's a must to add them to operate it.

Micro-Controller:- a chip contains MPC or CPU and RAM, ROM also i/o Ports and other peripherals.

Embedded systems:- A system controlled by a special computer "Micro-Controller".

Mechatronics system:- Integration between mechanical and electronic systems.

n-bit Processor:- Processor works for n-bit of Data with n-bit instructions and if Data larger than (n) it will be broken down to (n).

Q2)

Micro-Processor
General Purpose

Micro-Controller
Specific Purpose "include MPU"

RAM, ROM, i/o doesn't exist.

exist

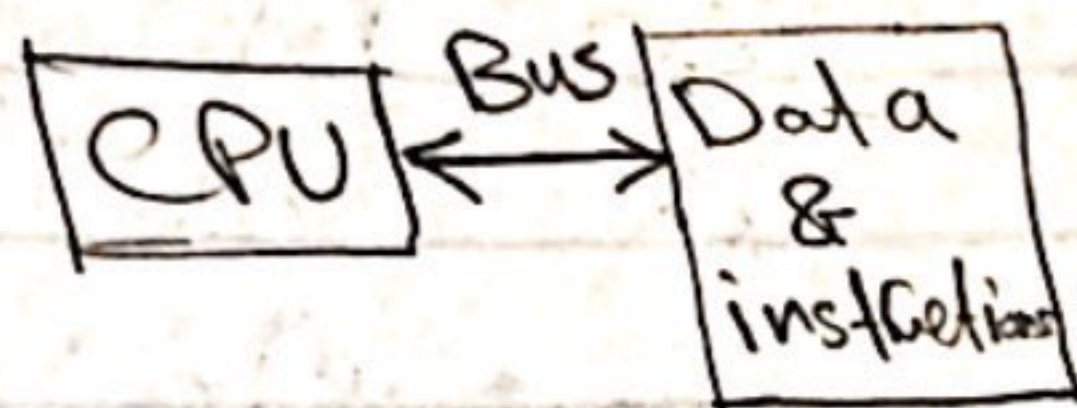
Adv:- Modular and can be customized by designer by choosing size of RAM, ROM and peripherals.

Ideal for systems in which Cost, Space and Power are critical.

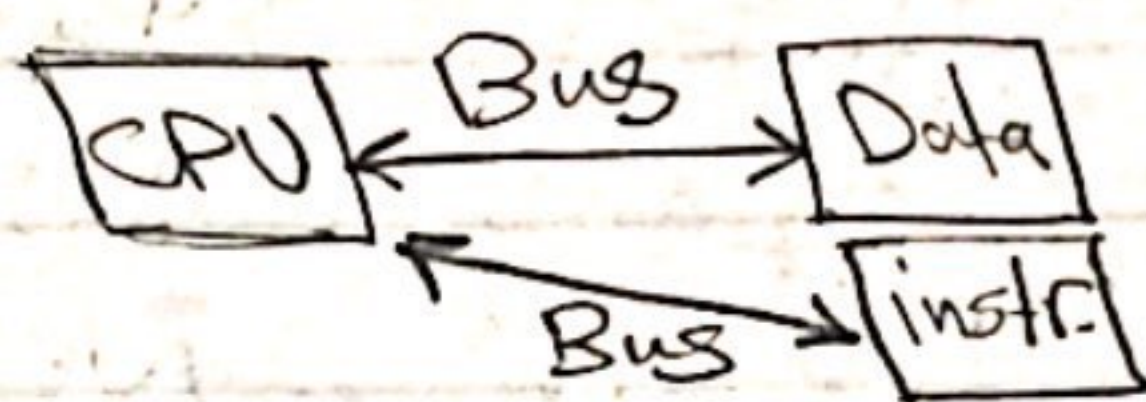
Dis Adv:- More expensive as RAM, ROM and I/O are added externally.

Designer can't add externally same for i/o, RAM, ROM, Timers and so on.

Q3) Von-Neuman Harvard Arch.



- * Single bus for Data and instruction
- * Single memory
- * Used in computers



- * two Buses for Data and instructions.
- * Separate memories.
- * used in MCU's.

Q4) PROM → one time programmed by user
EPROM → Can be programmed more than one time and erased by UV
EEPROM → Programmed multiple times and erased electrically.
Flash → Programmed and Erased Electrically with feature of specific data Erase.
Masked Rom → Programmed once by IC manufacturer.

Q5) SRAM → Based on flipflops "6 transistors" doesn't require refreshing
DRAM → Based on capacitor & 1 trans and it require refreshing to keep data.
NV-RAM → SRAM + Battery } To keep data when there is no voltage input
 SRAM + EEPROM

Q6) CPU doesn't have the capability to write to ROM
But it can be write on it by external device
eg. CDROM Burner.

Q 7)

Type	volatile?	writable	Erase size	Erase Cycles	Cost	Speed
SRAM	yes	yes	Byte	∞	Expensive	Fast
DRAM	yes	yes	Byte	∞	Moderate	Moderate
Masked ROM	No	No,	—	—	Cheap	Fast
PRom	No	once	—	—	Moderate	Fast
		by user				
EPROM	No	yes, by user	Entire chip	limited	Moderate	Fast
EEPROM	No	yes	Byte	limited	Expensive	Fast R
						Slow W
Flash	No	yes	sector	limited	Moderate	Fast R
						Slow W
NVRAM	No	yes	Byte	∞	Expensive	Fast