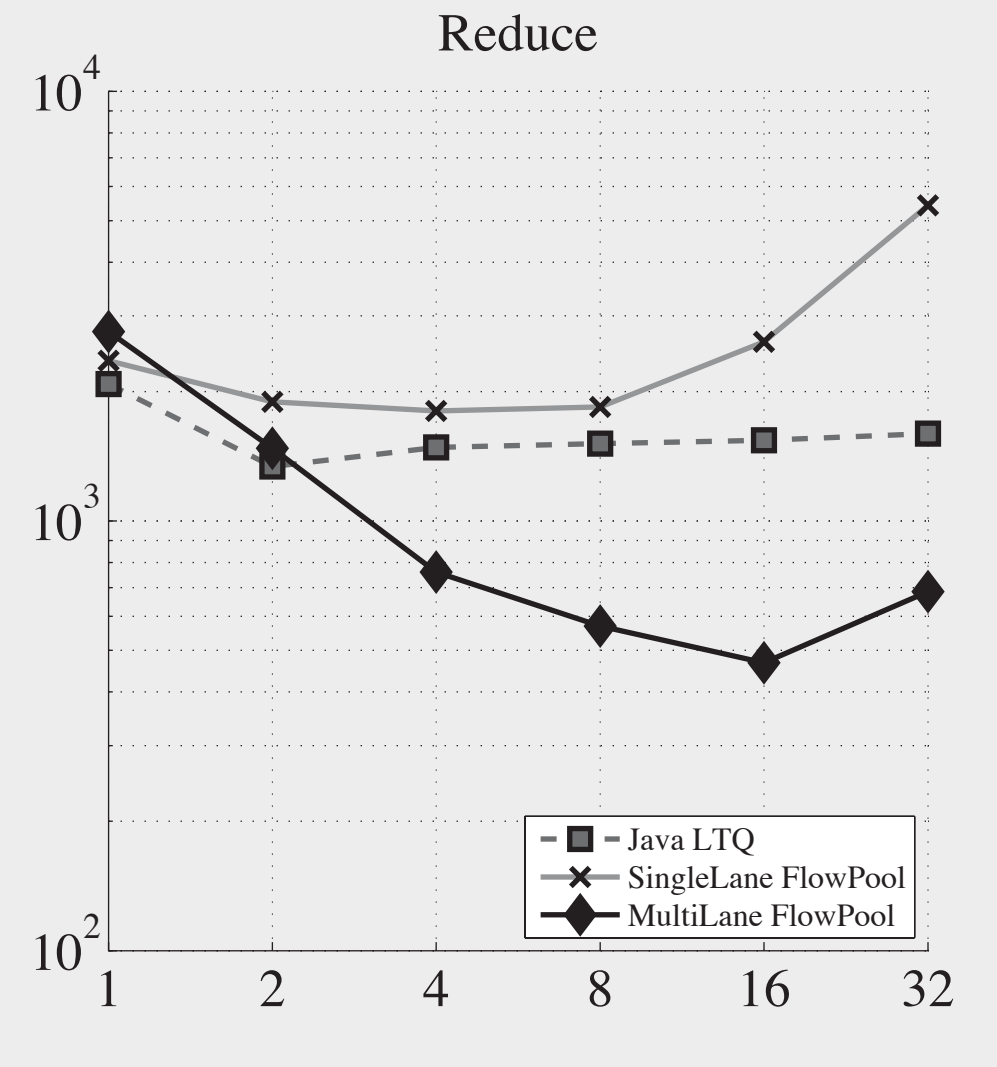
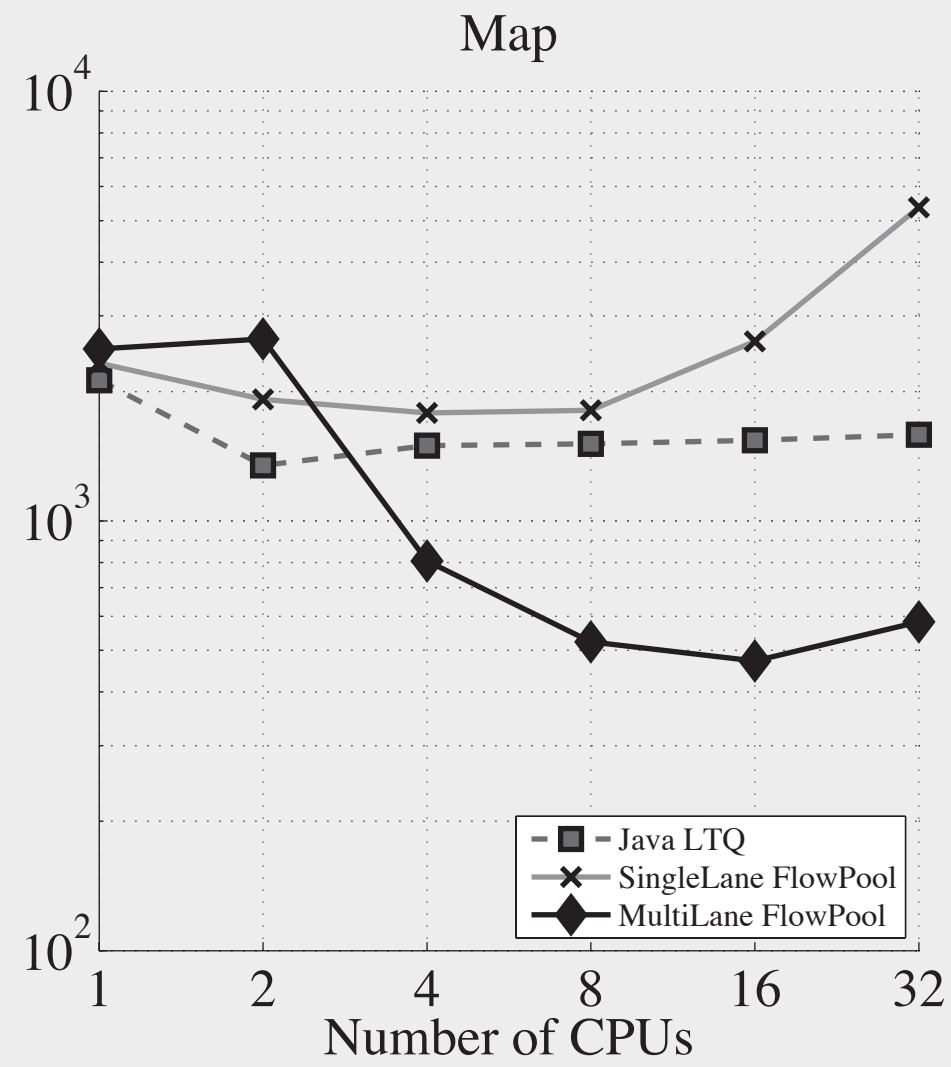
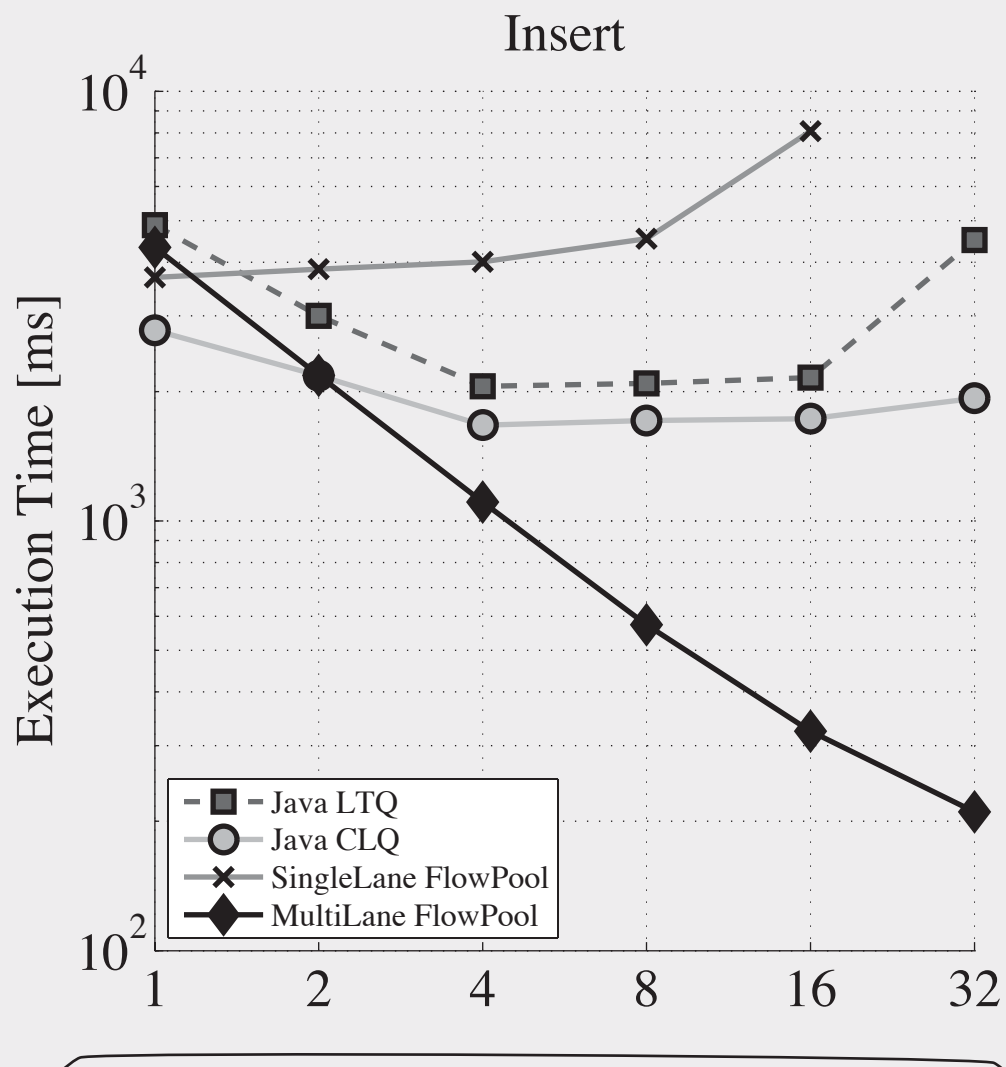
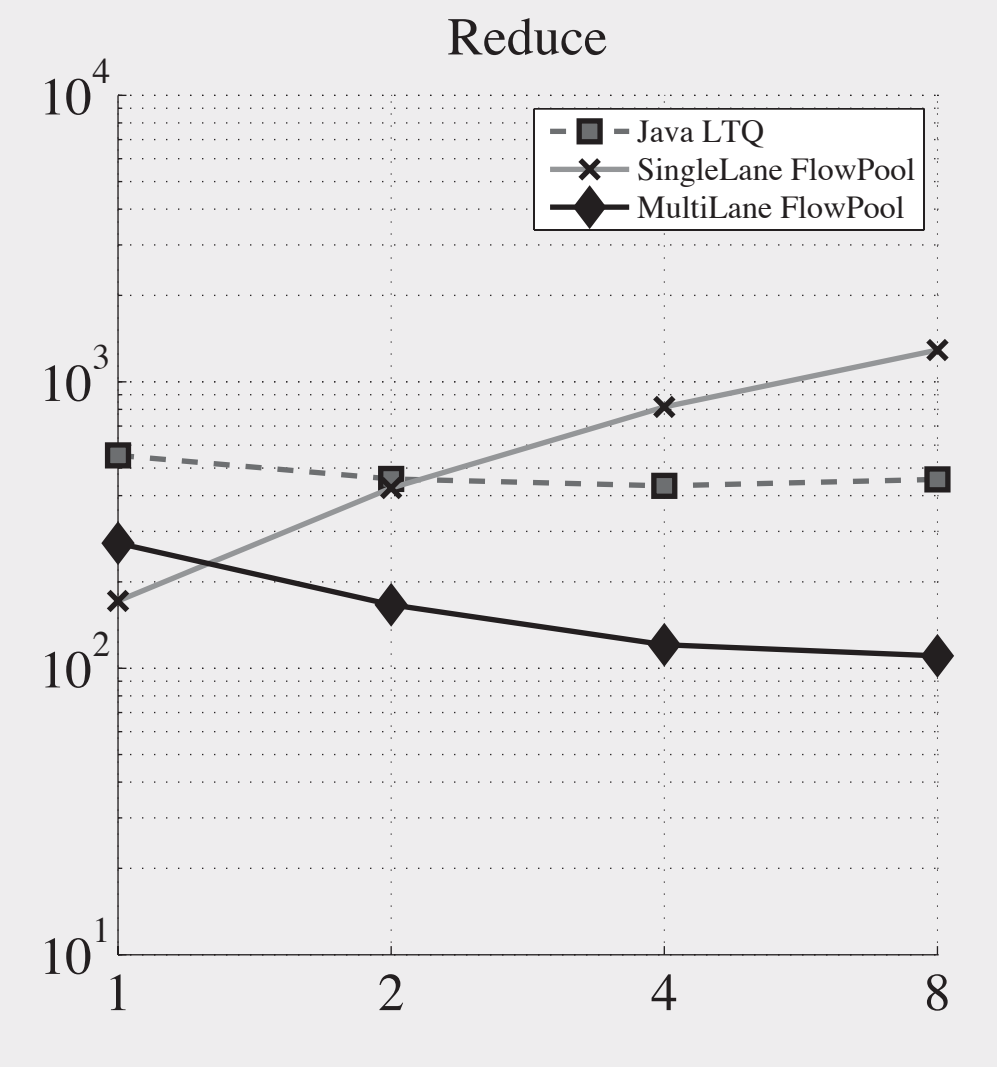
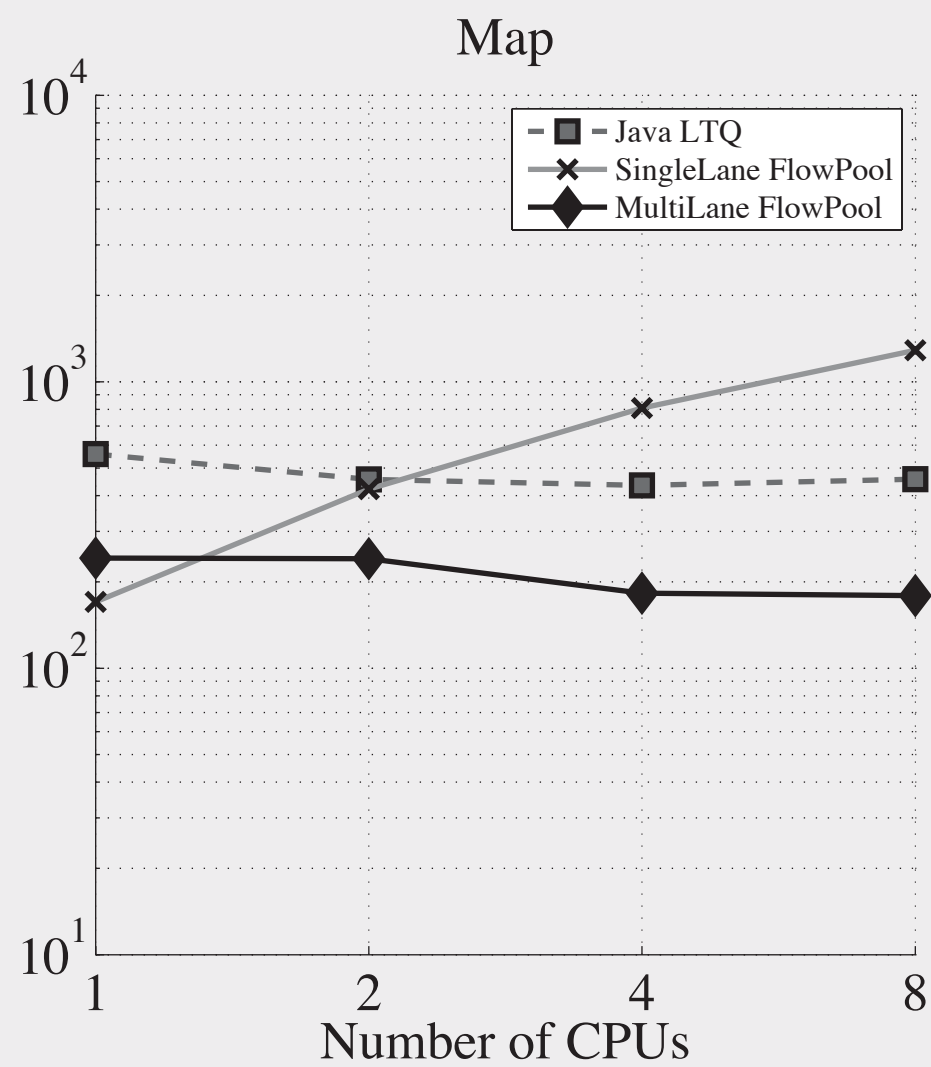
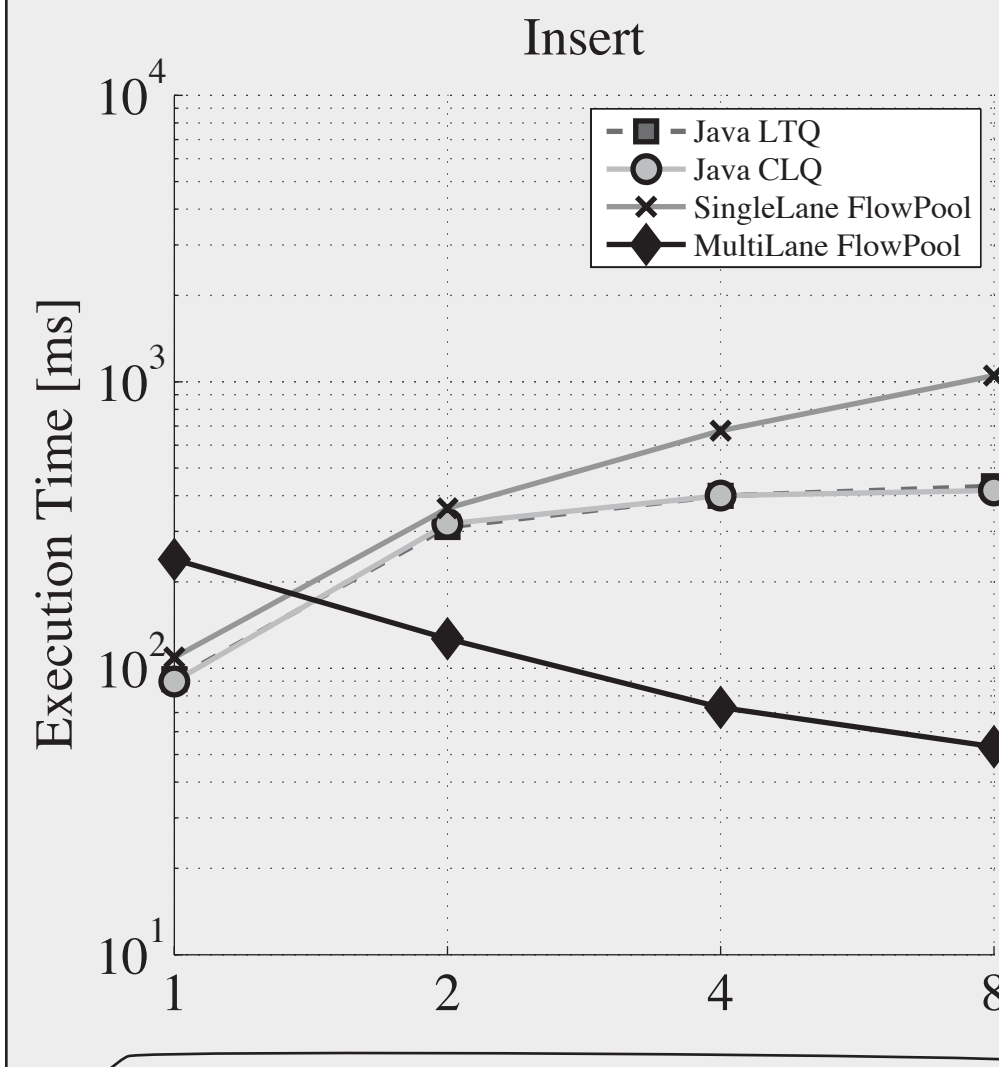


Operations on FlowPools Across Architectures

UltraSPARC T2 Architecture



Intel i7 Architecture



Intel Xeon Architecture

