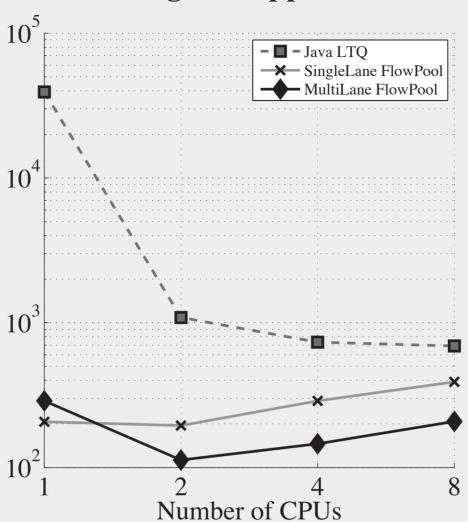
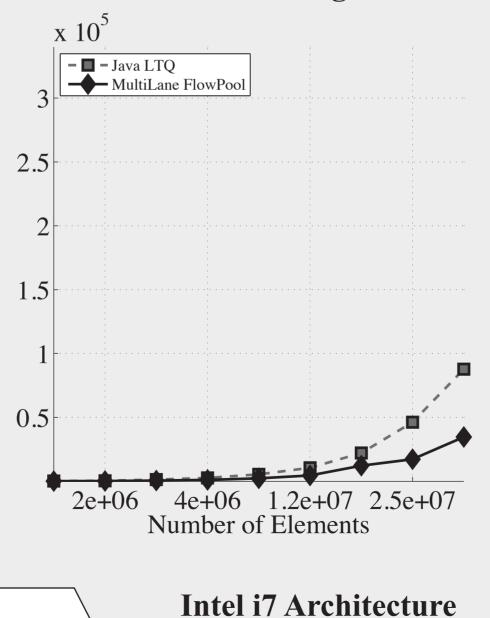


**UltraSPARC T2 Architecture** 

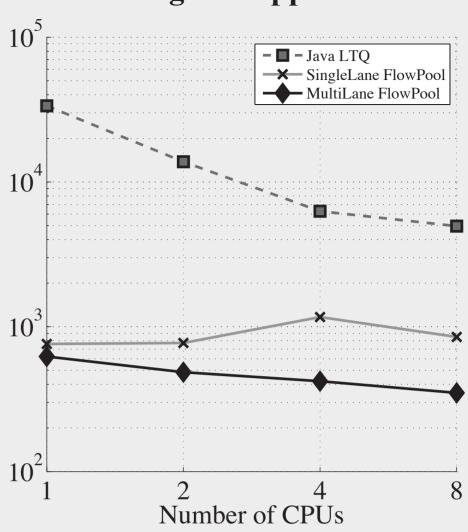




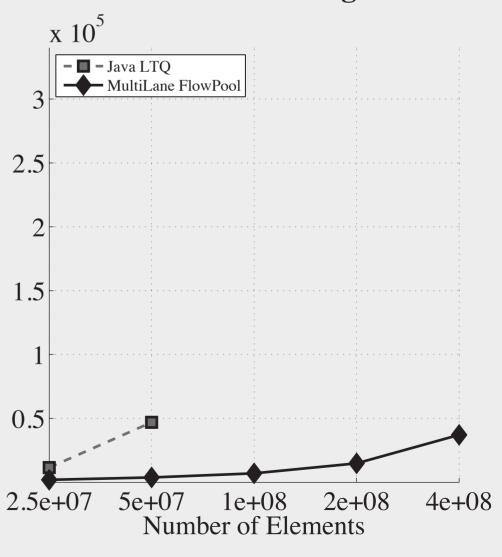
## **Communication/Garbage Collection**







## **Communication/Garbage Collection**



**Intel Xeon Architecture**