

BTNC

Input(1/2) and ~~output~~ are 8 bit
output is 16 bit signed signed

Start
signal

8-bit ?

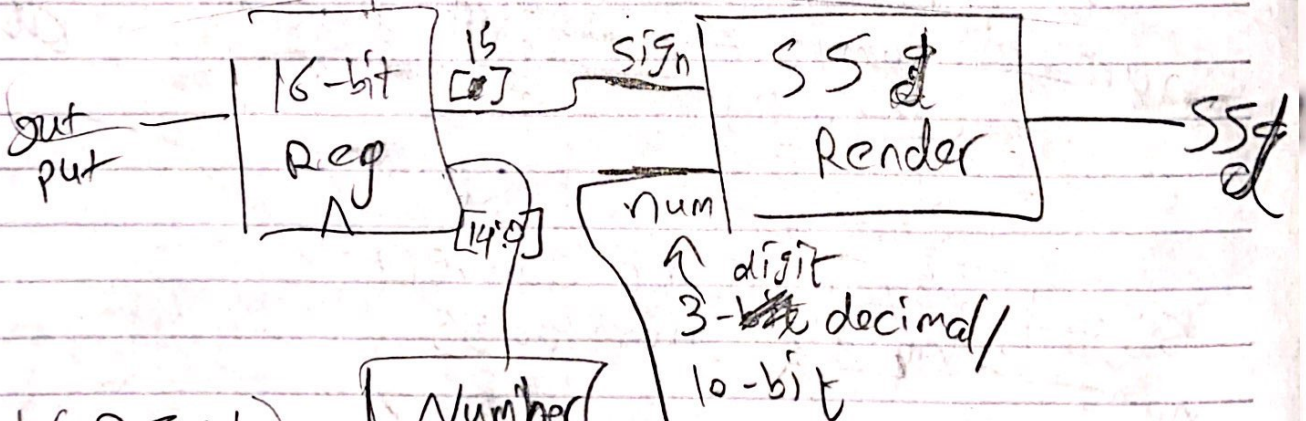
Is done

signed

Sequential
multiplier

Output includes
(Sign, abs(out))

load



SHL (BTNL)

SHR (BTNR)

Number
Slicer

start

In1

In2

SHL

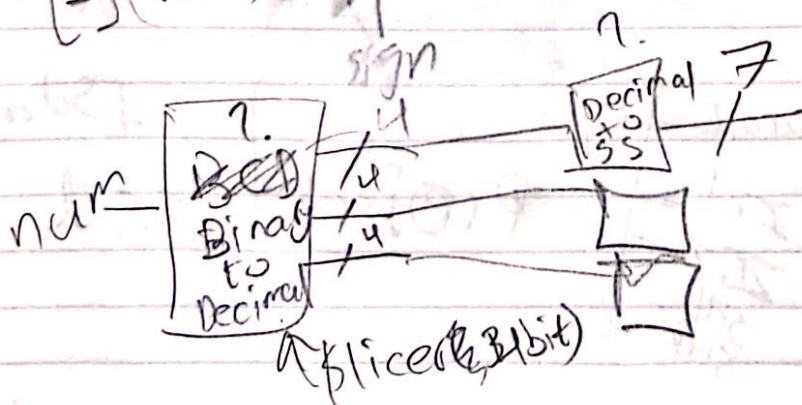
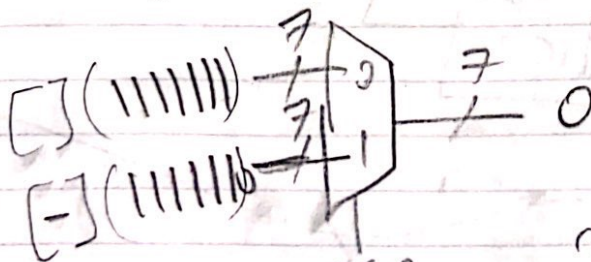
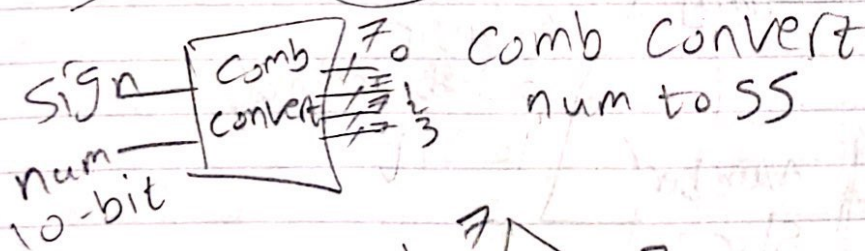
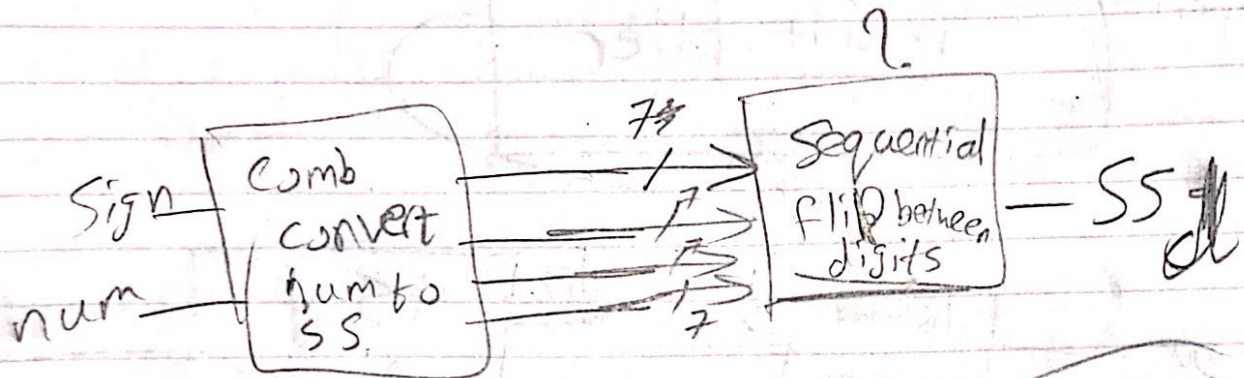
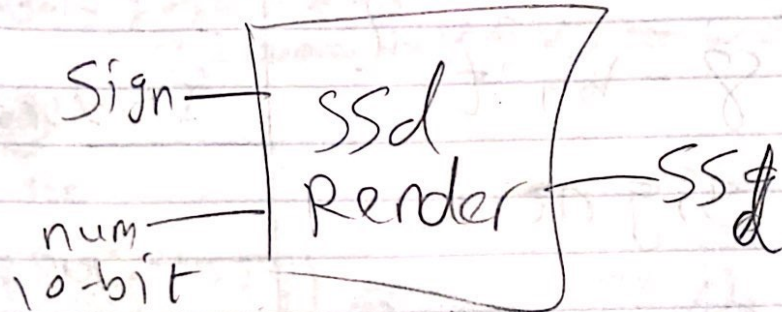
SHR

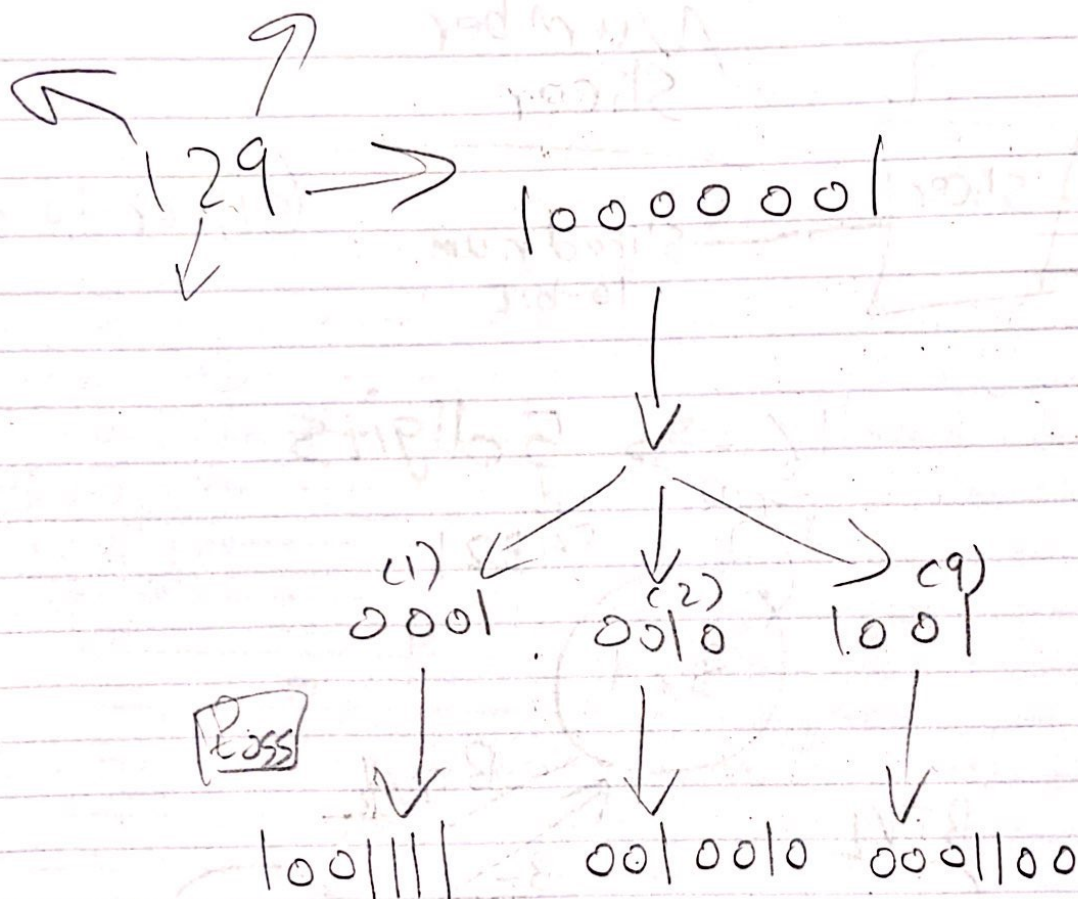
Project

Is done

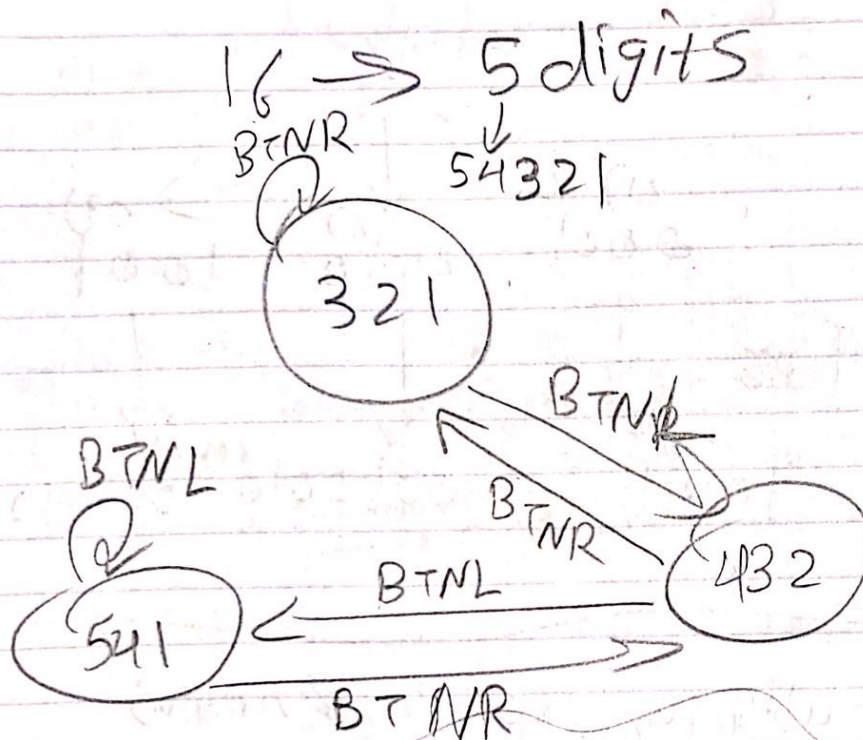
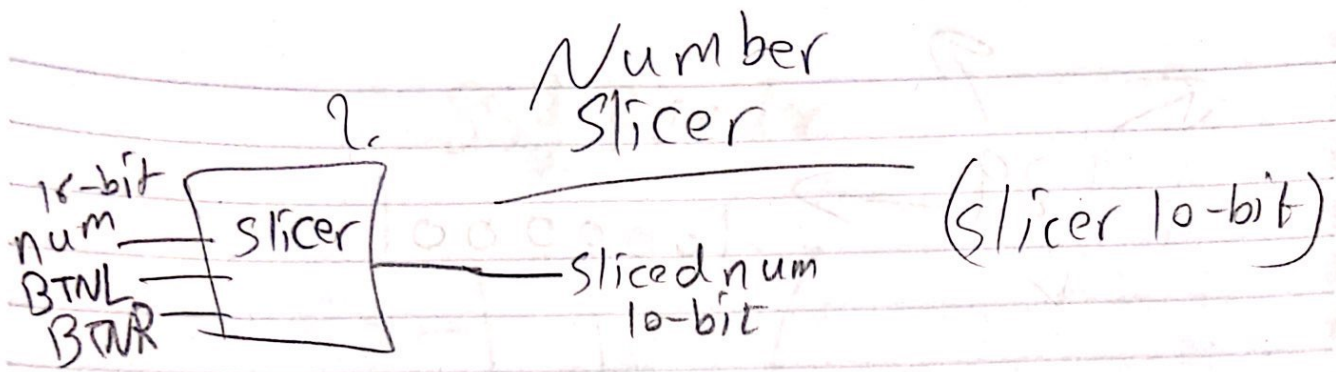
SSd

SSd Render





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- multiplier (mostafa & Andrew)
 - ~~Bin to Dec (16-bit to 32-bit)~~
 - Dec to SS
 - Sequential Flip between digits
 - slicer (youssef)



Logic? 16-bit slicer