

Instruction	i3	i2	i1	i0	/C	/Z	Phase	INCpc	/LOADpc	/LOADa	/LOADflags	/CarryIn	M	S3	S2	S1	S0	/OEram	/WEram	/OEalu	/OEin	/OEoprnd	/LOADout
Fetch	x	x	x	x	x	x	0	1	1	1	1	1	0	0	0	0	0	1	1	0	1	1	1
JC	0	0	0	0	0	x	1	0	0	1	1	1	0	0	0	0	0	1	1	0	1	1	1
JC	0	0	0	0	1	x	1	1	1	1	1	1	0	0	0	0	0	1	1	0	1	1	1
JNC	0	0	0	1	0	x	1	1	1	1	1	1	0	0	0	0	0	1	1	0	1	1	1
JNC	0	0	0	1	1	x	1	0	0	1	1	1	0	0	0	0	0	1	1	0	1	1	1
CMPI	0	0	1	0	x	x	1	0	1	1	0	0	0	0	1	1	0	1	1	1	1	0	1
CMPM	0	0	1	1	x	x	1	1	1	1	0	0	0	0	1	1	0	0	1	1	1	1	1
LIT	0	1	0	0	x	x	1	0	1	0	0	x	1	1	0	1	0	1	1	1	1	0	1
IN	0	1	0	1	x	x	1	0	1	0	0	x	1	1	0	1	0	1	1	1	0	1	1
LD	0	1	1	0	x	x	1	1	1	0	0	x	1	1	0	1	0	0	1	1	1	1	1
ST	0	1	1	1	x	x	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1
JZ	1	0	0	0	x	0	1	0	0	1	1	1	0	0	0	0	0	1	1	0	1	1	1
JZ	1	0	0	0	x	1	1	1	1	1	1	1	0	0	0	0	0	1	1	0	1	1	1
JNZ	1	0	0	1	x	0	1	1	1	1	1	1	0	0	0	0	0	1	1	0	1	1	1
JNZ	1	0	0	1	x	1	1	0	0	1	1	1	0	0	0	0	0	1	1	0	1	1	1
ADDI	1	0	1	0	x	x	1	0	1	0	0	1	0	1	0	0	1	1	1	1	1	0	1
ADDM	1	0	1	1	x	x	1	1	1	0	0	1	0	1	0	0	1	0	1	1	1	1	1
JMP	1	1	0	0	x	x	1	0	0	1	1	1	0	0	0	0	0	1	1	0	1	1	1
OUT	1	1	0	1	x	x	1	0	1	1	1	1	0	0	0	0	0	1	1	0	1	1	0
NORI	1	1	1	0	x	x	1	0	1	0	0	x	1	0	0	0	1	1	1	1	1	0	1
NORM	1	1	1	1	x	x	1	1	1	0	0	x	1	0	0	0	1	0	1	1	1	1	1