



ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ



VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS

J 10 015658

BE First Semester January 2010

Electronics & Communication Engineering

Name of the Student

MOUNESH GURUNATH BADIGER

Father's / Mother's Name

GURUNATH BADIGER

Seat No. : 2KL09EC039

Name of the College

K.L.E. SOCIETYS COLLEGE OF ENGINEERING & TECHNOLOGY, BELGAUM

Sl. No.	Subject Code	Title	Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
			Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	06MAT11	Engineering Maths - I	100	35	61	25	-	20	125	50	81	Pass
2	06CHE12	Engineering Chemistry	100	35	35	25	-	17	125	50	52	Pass
3	06CCP13	Computer Concepts & C Programming	100	35	70	25	-	16	125	50	86	Pass
4	06CED14	Computer Aided Engineering Drawing	100	35	80	25	-	16	125	50	96	Pass
5	06ELN15	Basic Electronics	100	35	65	25	-	19	125	50	84	Pass
6	06CPL16	Computer Programming Lab	50	20	43	25	12	24	75	32	67	Pass
7	06CHEL17	Engg. Chemistry Lab	50	20	46	25	12	17	75	32	63	Pass
8	06CIV18	Environmental Studies	50	18	24	25	-	22	75	30	46	Pass
GRAND TOTAL									775	314	529	

Total Marks Obtained (in words) : FIVE HUNDRED AND TWENTY NINE ONLY

Result of the Semester : FIRST CLASS

DATE : JUN 09.2010

06CIP/CIV18 is not considered for Grand Total and the Class Declaration

A - Absent NE - Not Eligible

Registrar (Evaluation)



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VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS

10 242630

BE Second Semester July 2010

Electronics & Communication Engineering

Name of the Student

: MOUNESH GURUNATH BADIGER

Father's / Mother's Name

: GURUNATH BADIGER

Seat No. : 2KL09EC039

Name of the College

: K.L.E. SOCIETYS COLLEGE OF ENGINEERING & TECHNOLOGY, BELGAUM

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	06MAT21	Engineering Maths - II	100	35	73	25	-	19	125	50	92	Pass
2	06PHY22	Engineering Physics	100	35	68	25	-	15	125	50	83	Pass
3	06CIV23	Elements of Civil Engg. & Engg. Mechanics	100	35	92	25	-	20	125	50	112	Pass
4	06EME24	Elements of Mechanical Engg.	100	35	77	25	-	19	125	50	96	Pass
5	06ELE25	Basic Electrical Engg.	100	35	66	25	-	17	125	50	83	Pass
6	06WSL26	Workshop Practice	50	20	32	25	12	20	75	32	52	Pass
7	06PHYL27	Engg. Physics Lab	50	20	40	25	12	20	75	32	60	Pass
8	06CIP28	Constitution of India & Professional Ethics	50	18	36	25	-	23	75	30	59	Pass
GRAND TOTAL									775	314	578	

Total Marks Obtained (in words) : FIVE HUNDRED AND SEVENTY EIGHT ONLY

Result of the Semester : FIRST CLASS WITH DISTINCTION

DATE : NOV 04,2010

06CIP/CIV28 is not considered for Grand Total and the Class Declaration

A - Absent NE - Not Eligible

Registrar (Evaluation)

Krishnamurthy G.N.



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VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

J 256699



STATEMENT OF MARKS

BE Third Semester January 2011
Electronics & Communication Engineering

Name of the Student : MOUNESH GURUNATH BADIGER

Seat No. : 2KL09EC039

Father's / Mother's Name : GURUNATH BADIGER

Name of the College : K.L.E. SOCIETY'S COLLEGE OF ENGINEERING & TECHNOLOGY, BELGAUM

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	06MAT31	Engineering Mathematics -III	100	35	75	25	-	17	125	50	92	Pass
2	06ES32	Analog Electronic Circuits	100	35	35	25	-	19	125	50	54	Pass
3	06ES33	Logic Design	100	35	37	25	-	17	125	50	54	Pass
4	06ES34	Network Analysis	100	35	35	25	-	16	125	50	51	Pass
5	06IT35	Electronic Instrumentation	100	35	62	25	-	21	125	50	83	Pass
6	06ES36	Field Theory	100	35	68	25	-	19	125	50	87	Pass
7	06ESL37	Analog Electronics Lab	50	20	25	25	12	22	75	32	47	Pass
8	06ESL38	Logic Design Lab	50	20	22	25	12	22	75	32	44	Pass
GRAND TOTAL									900	364	512	

Total Marks Obtained (in words) : FIVE HUNDRED AND TWELVE ONLY

Result of the Semester : SECOND CLASS

DATE : JUN 07, 2011

A - Absent NE - Not Eligible



Krishnareddy G.N.
Registrar (Evaluation)



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VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Fourth Semester July 2011

Electronics & Communication Engineering

J 454284

Name of the Student : MOUNESH GURUNATH BADIGER

Father's / Mother's Name : GURUNATH BADIGER

Seat No. : 2KL09EC039

Name of the College : K.L.E. SOCIETYS COLLEGE OF ENGINEERING & TECHNOLOGY, BELGAUM

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	06MAT41	Engineering Mathematics - IV	100	35	65	25	-	19	125	50	84	Pass
2	06ES42	Microcontrollers	100	35	70	25	-	15	125	50	85	Pass
3	06ES43	Control Systems	100	35	76	25	-	18	125	50	94	Pass
4	06EC44	Signals & Systems	100	35	40	25	-	16	125	50	56	Pass
5	06EC45	Fundamentals of HDL	100	35	61	25	-	19	125	50	80	Pass
6	06EC46	Linear IC's & Applications	100	35	62	25	-	20	125	50	82	Pass
7	06ESL47	Microcontrollers Lab	50	20	34	25	12	22	75	32	56	Pass
8	06ECL48	HDL Lab	50	20	44	25	12	23	75	32	67	Pass
GRAND TOTAL									900	364	604	

Total Marks Obtained (in words) : SIX HUNDRED AND FOUR ONLY

Result of the Semester : FIRST CLASS

DATE : OCT 19, 2011

A - Absent NE - Not Eligible



Mounesh G.N.
Registrar (Evaluation)



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VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

STATEMENT OF MARKS

BE Fifth Semester January 2012

Electronics & Communication Engineering

J 11 773858

Name of the Student : MOUNESH GURUNATH BADIGER

Seat No. : 2KL09EC039

Father's / Mother's Name : GURUNATH BADIGER

Name of the College : K.L.E. SOCIETYS COLLEGE OF ENGINEERING & TECHNOLOGY, BELGAUM

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	06AL51	Management & Entrepreneurship	100	35	41	25	-	20	125	50	61	Pass
2	06EC52	Digital Signal Processing	100	35	46	25	-	16	125	50	62	Pass
3	06EC53	Analog Communication	100	35	54	25	-	18	125	50	72	Pass
4	06EC54	Microwaves and Radar	100	35	40	25	-	22	125	50	62	Pass
5	06EC55	Digital Switching Systems	100	35	60	25	-	19	125	50	79	Pass
6	06EC56	Fundamentals of CMOS VLSI	100	35	37	25	-	23	125	50	60	Pass
7	06ECL57	DSP Lab	50	20	42	25	12	24	75	32	66	Pass
8	06ECL58	Analog Communication Lab+LIC Lab	50	20	27	25	12	24	75	32	51	Pass
GRAND TOTAL									900	364	513	

Total Marks Obtained (in words) : FIVE HUNDRED AND THIRTEEN ONLY

Result of the Semester : SECOND CLASS

DATE : JUN 13, 2012

A - Absent NE - Not Eligible



Krishnamurthy G.N.

Registrar (Evaluation)



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VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

J 12 396084

STATEMENT OF MARKS

BE Sixth Semester July 2012

Electronics & Communication Engineering

Name of the Student

MOUNESH GURUNATH BADIGER

Father's / Mother's Name

GURUNATH BADIGER

Seat No. : 2KL09EC039

Name of the College

K.L.E. SOCIETY'S COLLEGE OF ENGINEERING & TECHNOLOGY, BELGAUM

Sl. No.	Subject		Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
	Code	Title	Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	06EC61	Digital Communication	100	35	54	25	-	19	125	50	73	Pass
2	06EC62	Microprocessors	100	35	47	25	-	17	125	50	64	Pass
3	06EC63	Analog and Mixed Mode VLSI Design	100	35	51	25	-	17	125	50	68	Pass
4	06EC64	Antennas and Propagation	100	35	67	25	-	20	125	50	87	Pass
5	06EC65	Information Theory and Coding	100	35	78	25	-	17	125	50	95	Pass
6	06EC661	Programming in C++	100	35	74	25	-	22	125	50	96	Pass
7	06ECL67	Advanced Communication Lab	50	20	32	25	12	23	75	32	55	Pass
8	06ECL68	Microprocessors Lab.	50	20	45	25	12	21	75	32	66	Pass
GRAND TOTAL										900	364	604

Total Marks Obtained (In words) : SIX HUNDRED AND FOUR ONLY

Result of the Semester : FIRST CLASS

DATE : NOV 04.2012

A - Absent NE - Not Eligible



Registrar (Evaluation)



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VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

J 12 572782

STATEMENT OF MARKS

BE Seventh Semester January 2013

Electronics & Communication Engineering

Name of the Student : MOUNESH GURUNATH BADIGER

Father's / Mother's Name : GURUNATH BADIGER

Seat No. : 2KL09EC039

Name of the College : KLE DR.M.S. SHESHGIRI COLLEGE OF ENGINEERING & TECHNOLOGY, BELGAUM

SL. No.	Subject Code	Title	Examination Marks			Internal Assessment Marks			Total Marks			Subject Result
			Max.	Min.	Obtained	Max.	Min.	Obtained	Max.	Min.	Obtained	
1	06EC71	Computer Communication Networks	100	35	66	25	-	24	125	50	90	Pass
2	06EC72	Optical Fiber Communication	100	35	45	25	-	23	125	50	68	Pass
3	06EC73	Power Electronics	100	35	59	25	-	22	125	50	81	Pass
4	06EC74	DSP Algorithms & Architecture	100	35	59	25	-	19	125	50	78	Pass
5	06EC751	Operating Systems	100	35	48	25	-	19	125	50	67	Pass
6	06EC762	Real Time Systems	100	35	61	25	-	21	125	50	82	Pass
7	06ECL77	VLSI Lab	50	20	48	25	12	22	75	32	70	Pass
8	06ECL78	Power Electronics Lab	50	20	41	25	12	24	75	32	65	Pass
GRAND TOTAL									900	384	601	

Total Marks Obtained (in words) : SIX HUNDRED AND ONE ONLY

Result of the Semester : FIRST CLASS



Registrar (Evaluation)

DATE : APR 20, 2013

A - Absent NE - Not Eligible



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VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

STATEMENT OF MARKS

KARNATAKA, INDIA
BE Eighth Semester (Electronics & Communication Engineering)

Seat No. : 2KL09EC039



F_J
12 034687

Name of the Student

: MOUNESH GURUNATH BADIGER

Father's / Mother's Name

: GURUNATH BADIGER

Name of the College

: KLE DR.M.S. SHESHGIRI COLLEGE OF ENGINEERING & TECHNOLOGY, BELGAUM

Sl. No.	Code	Subject Title	Examination Marks			Internal Assessment Marks			Total Marks			Month & Year of Passing
			Max	Min	Obtained	Max	Min	Obtained	Max	Min	Obtained	
1	06EC81	Wireless Communication	100	35	58	25	-	15	125	50	73	Jul 13
2	06EC82	Embedded System Design	100	35	73	25	-	17	125	50	80	Jul 13
3	06EC834	Biomedical Signal Processing	100	35	71	25	-	21	125	50	92	Jul 13
4	06EC842	Real Time Operating Systems	100	35	80	25	-	16	125	50	86	Jul 13
5	06EC85	Project Work	100	40	91	100	50	95	200	90	186	Jul 13
6	06EC86	Seminar	-	-	-	50	20	45	50	20	45	Jul 13

*First Attempt Total (in words) : FIVE HUNDRED AND EIGHTY TWO ONLY

Result of the Semester : FIRST CLASS WITH DISTINCTION

Results of V to VIII Semesters :

Semesters	V	VI	VII	VIII	V TO VIII
Marks at First Attempt	513	604	601	582	2300
Total Marks (Max.)	900	900	900	750	3450
Result	SC	FC	FC	FCD	FC

First Attempt Total (V to VIII Sem) In words : TWO THOUSAND THREE HUNDRED ONLY

Result of the Degree : FIRST CLASS

Date : AUG 20,2013

Registrar (Evaluation)