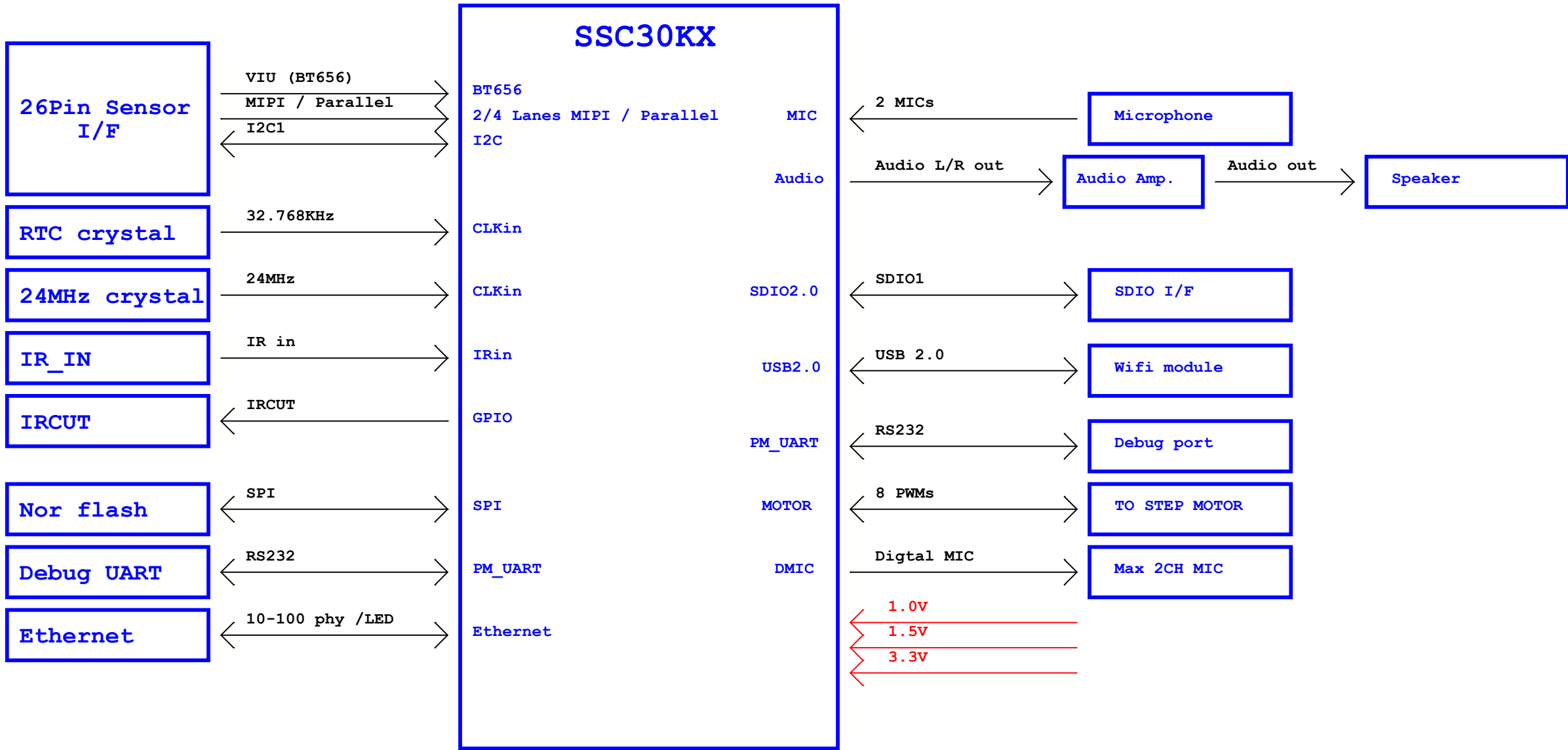
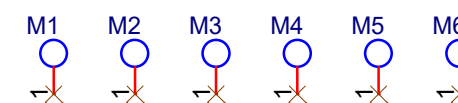
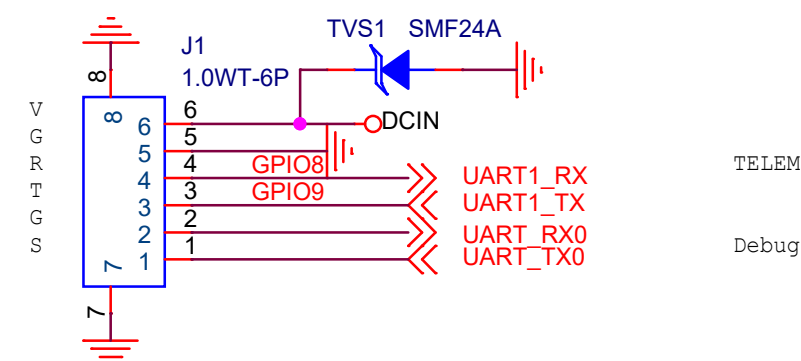
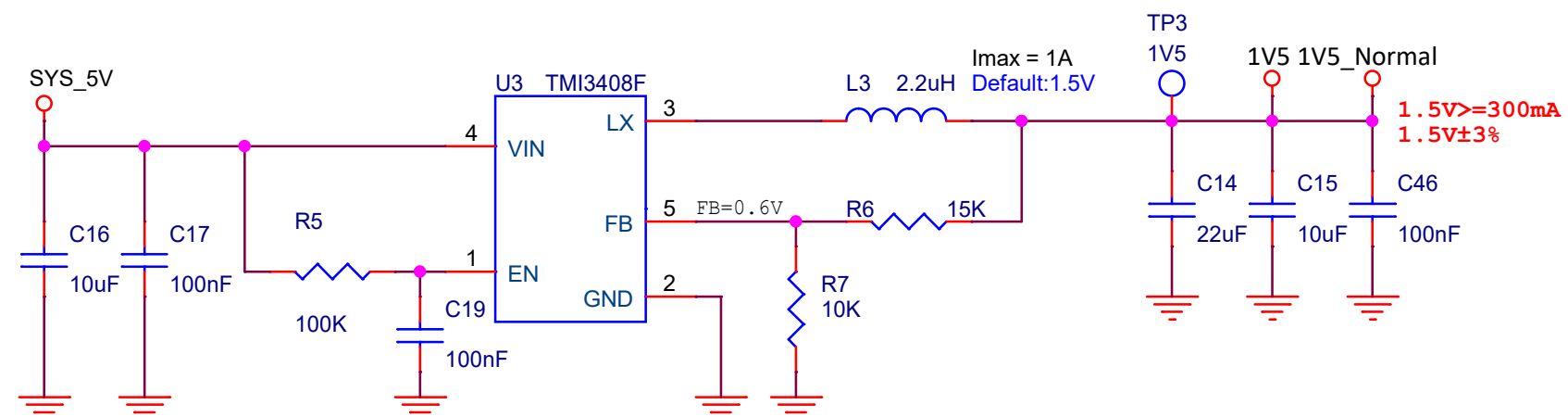
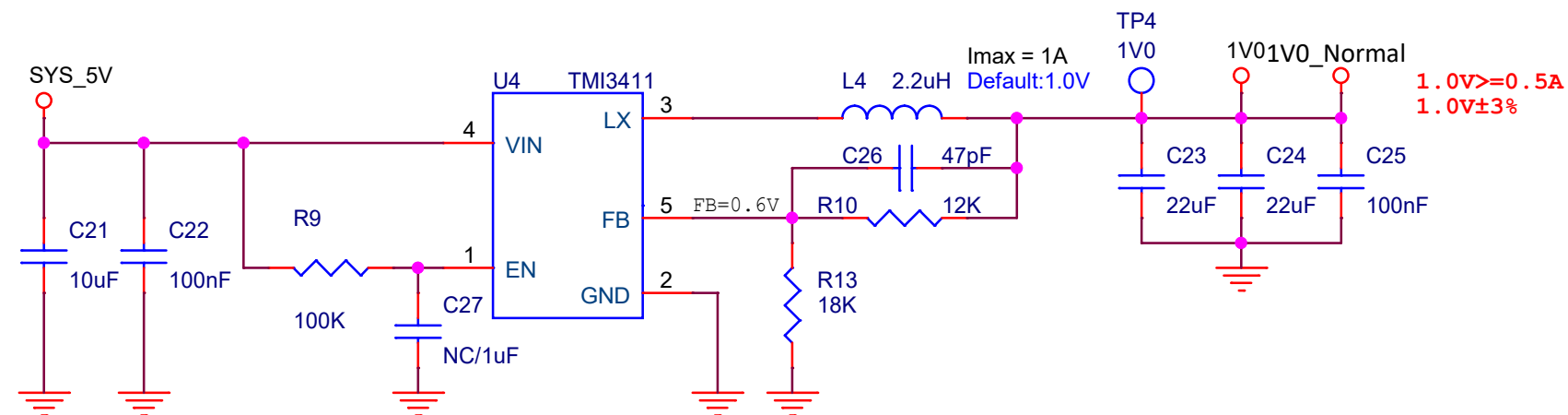
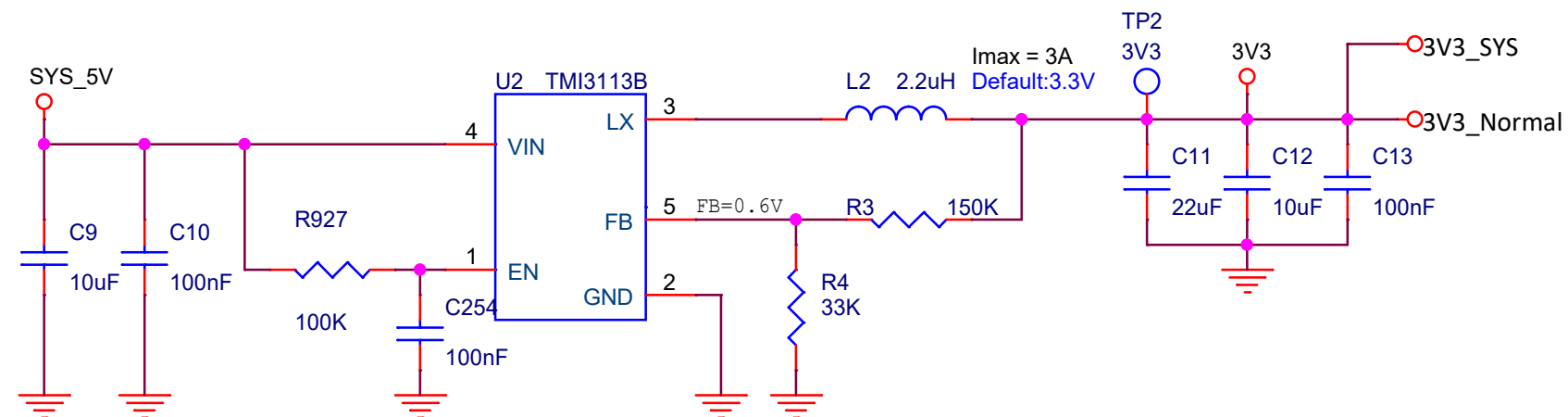
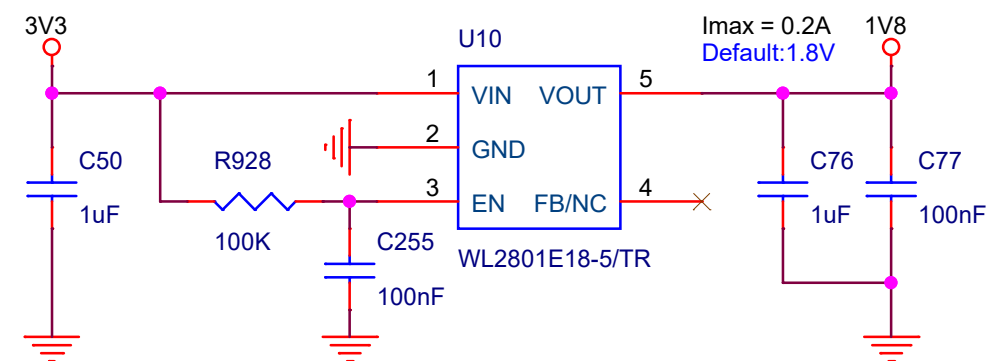
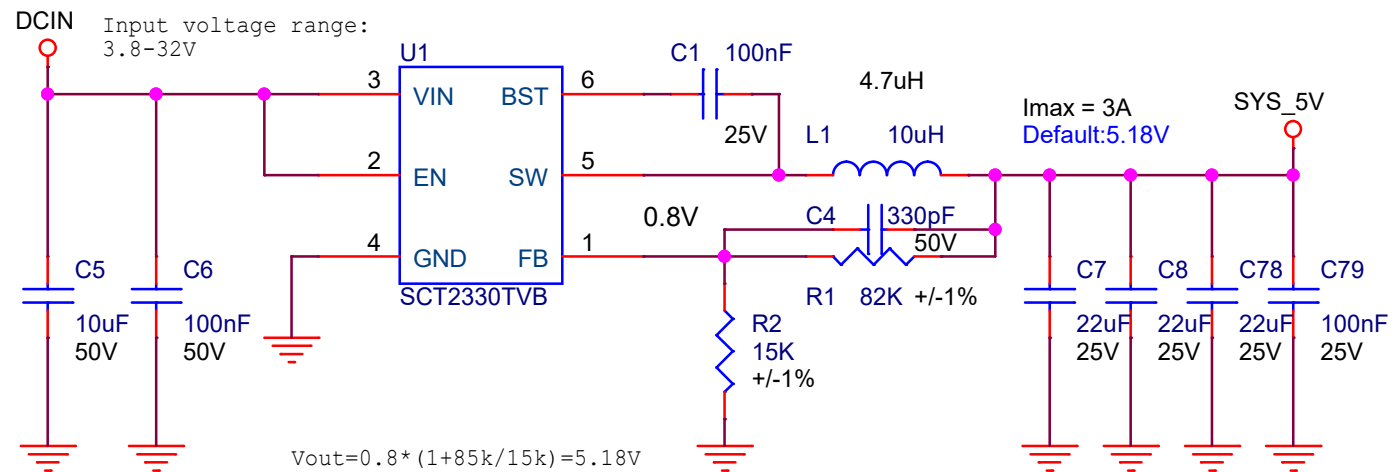


Version	Date	Author	Change Note	Approved
V1.0	2024.03.17	Lemon	1.First edit.	

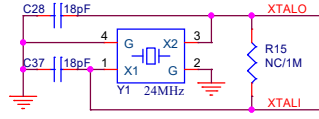




Attention:
Power on sequence, 1.0V no later than 3.3V_SYS.

Copyright: Lemon. All rights reserved.					
Project:	VTX_SSC338				
File:	03. Power				
Date:	Sunday, March 17, 2024		Size:	A4	Rev: 1.0
Designed by:	Lemon	Reviewed by:	Lemon	Sheet:	3 of 10

RTC (32.768KHz)



SSC30KX

QFN 12.3x12.3 128P

QFN128_12_3X12_3H0_9P0_35

MQFN128\0.35-12.3X12.3-B8.5X8.5

For VEN

For DLA

IR_CUT

ETHERNET



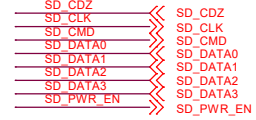
USB Function



SDIO1



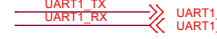
SD0 Card



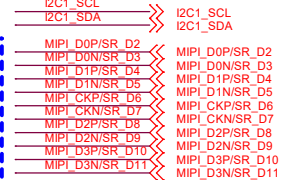
I2CM



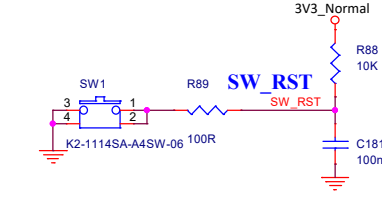
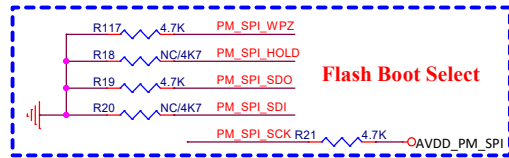
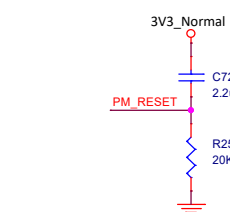
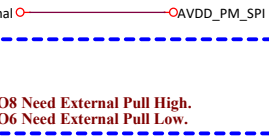
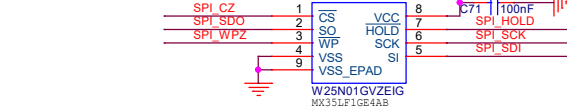
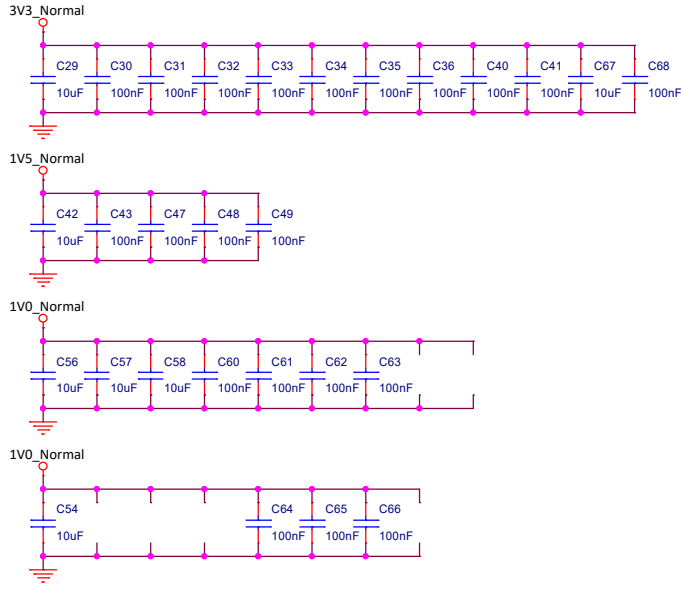
UART1



SENSOR I/F



DVFS



HW STRAPPING

Boot Information (ROM - Boot Mode Selection)						ROM Boot Storage Selection	Upgrade Path 1st option before storage	Upgrade Path 2nd option after storage fail
ROM boot up Path	PM_SPI_HLD	PM_SPI_DO	PM_SPI_WP	PM_SPI_DI	PM_SPI_CLK			
(default)	M[4]	M[3]	M[2]	M[1]	M[0]			
Storage Mode (SD Boot first) (or Skip SD)	1	0	1	1	0	SPI NAND Flash (w/ embedded ECC)	SD (1st)	PC/USB
		0			1	Skip SD Card, run SPI NAND Flash	(none)	PC/USB
		1			0	SPI NOR Flash	SD (1st)	PC/USB
		1			1	Skip SD Card, run SPI NOR Flash	(none)	PC/USB
Non-NOR/NAND Flash storage	0	0			0	None	(none)	(none)
		0			1	None	(none)	(none)
		1			0	eMMC 8 bit	(none)	PC/USB
		1			1	eMMC 4 bit (From SD0 Pin)	(none)	PC/USB
Secure Mode				1		Normal boot		
				0		Secure boot		
Boot Mode			1			SPI NOR boot		
			0			ROM boot.		
IC Default Status	1	1	1	1	0	ROM boot -> SPI NOR Flash		

Note 1 : Our IC only support the SPI-NAND Flash with ECC.

Note 2 : Skip SD Card means Rom Code don't detect SD Card that it can start faster.

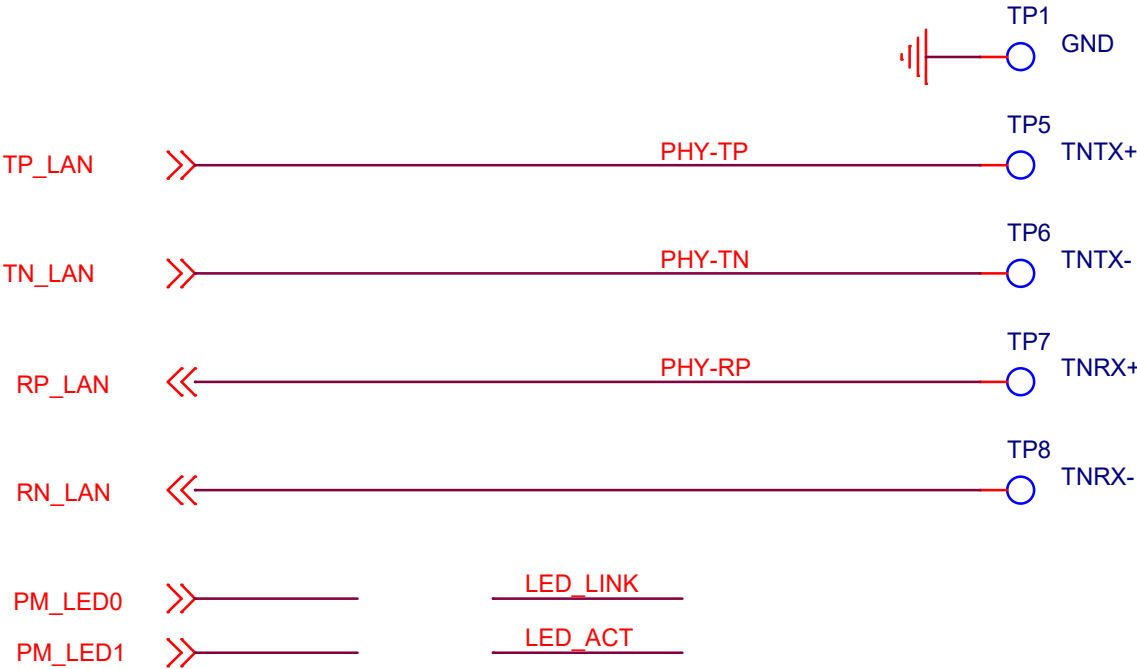
Note 3 : PM_SPI_WP2 must external PD=4K7.



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Project:	VTX_SSC30KX
File:	04_SSC30KX
Date:	Sunday, March 17, 2024
Designed by:	Lemon
Reviewed by:	Lemon
Size:	A2
Rev:	1.0
Sheet:	4 of 10

Ethernet



TNTX+

TNTX-

TNRX+

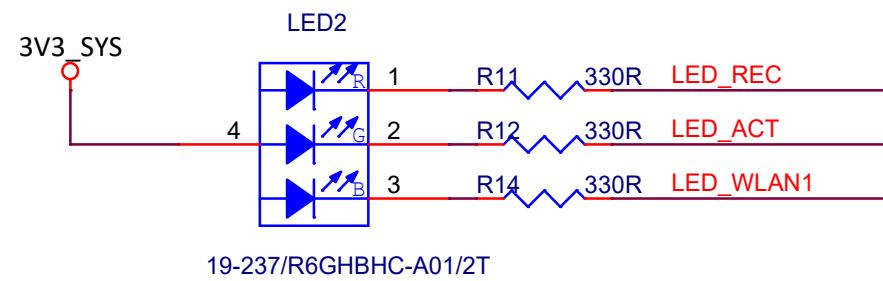
TNRX-




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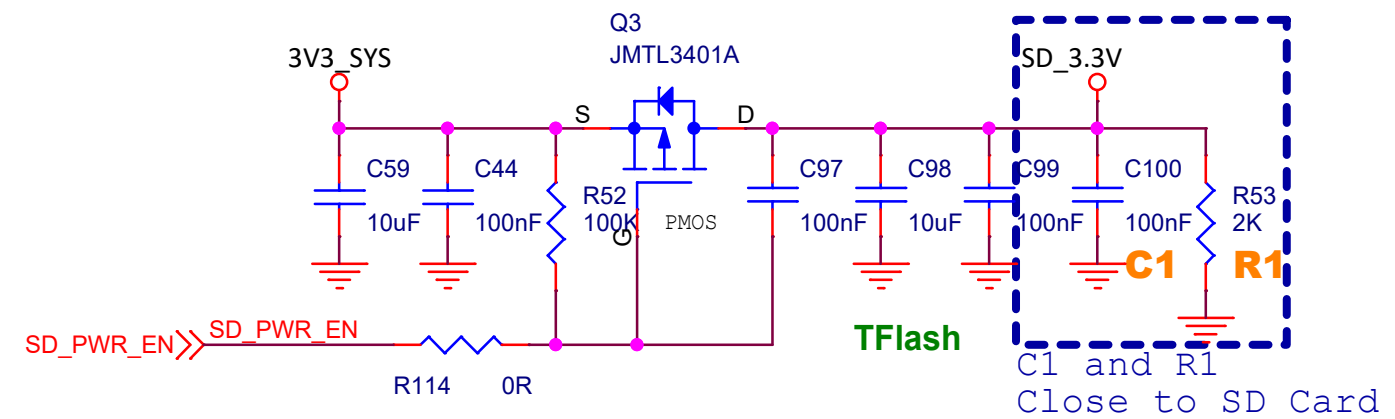
Project:	VTX_SSC338				
File:	05. ETHERNET				
Date:	Sunday, March 17, 2024		Size:	A4	Rev: 1.0
Designed by:	Lemon	Reviewed by:	Lemon	Sheet:	5 of 10

LED

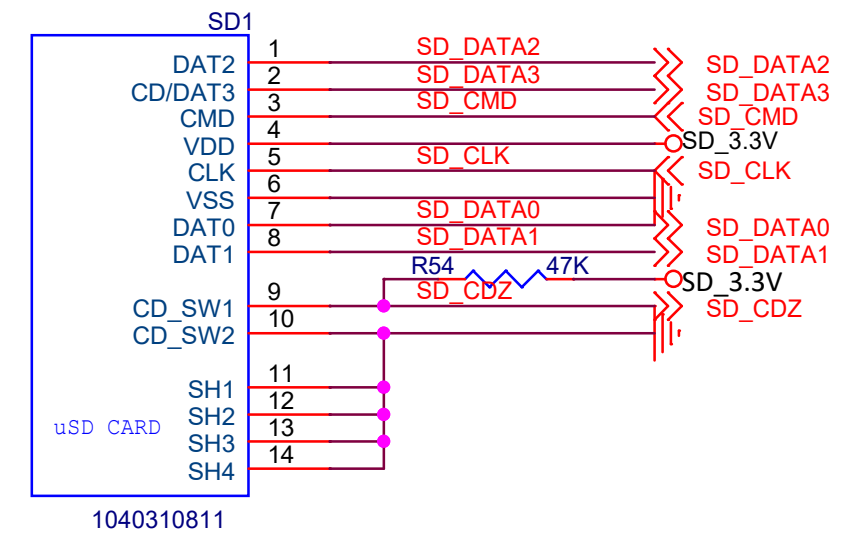
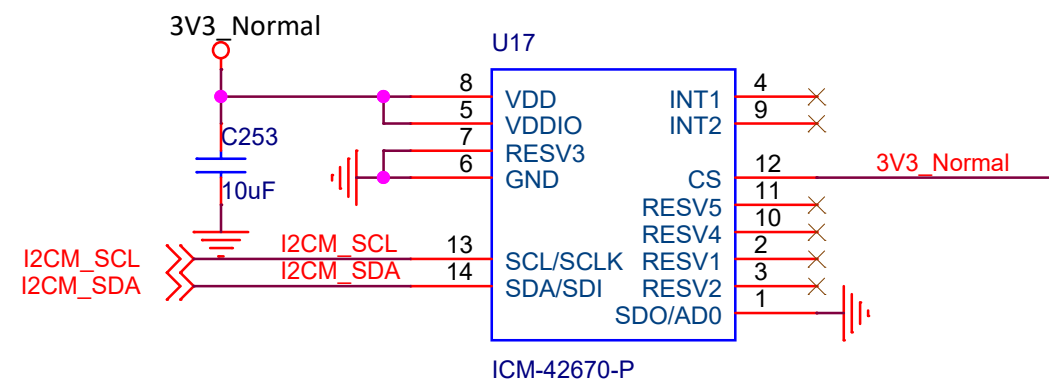


		Copyright:Lemon. All rights reserved.			
Project:	VTX_SSC338				
File:	06. LED				
Date:	Sunday, March 17, 2024		Size:	A4	Rev: 1.0
Designed by:	Lemon	Reviewed by:	Lemon	Sheet:	6 of 10

SD Card



G-Sensor



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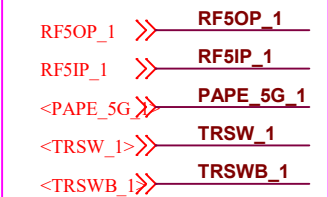
Project:	VTX_SSC338
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File:	07. G-Sensor&SD CARD
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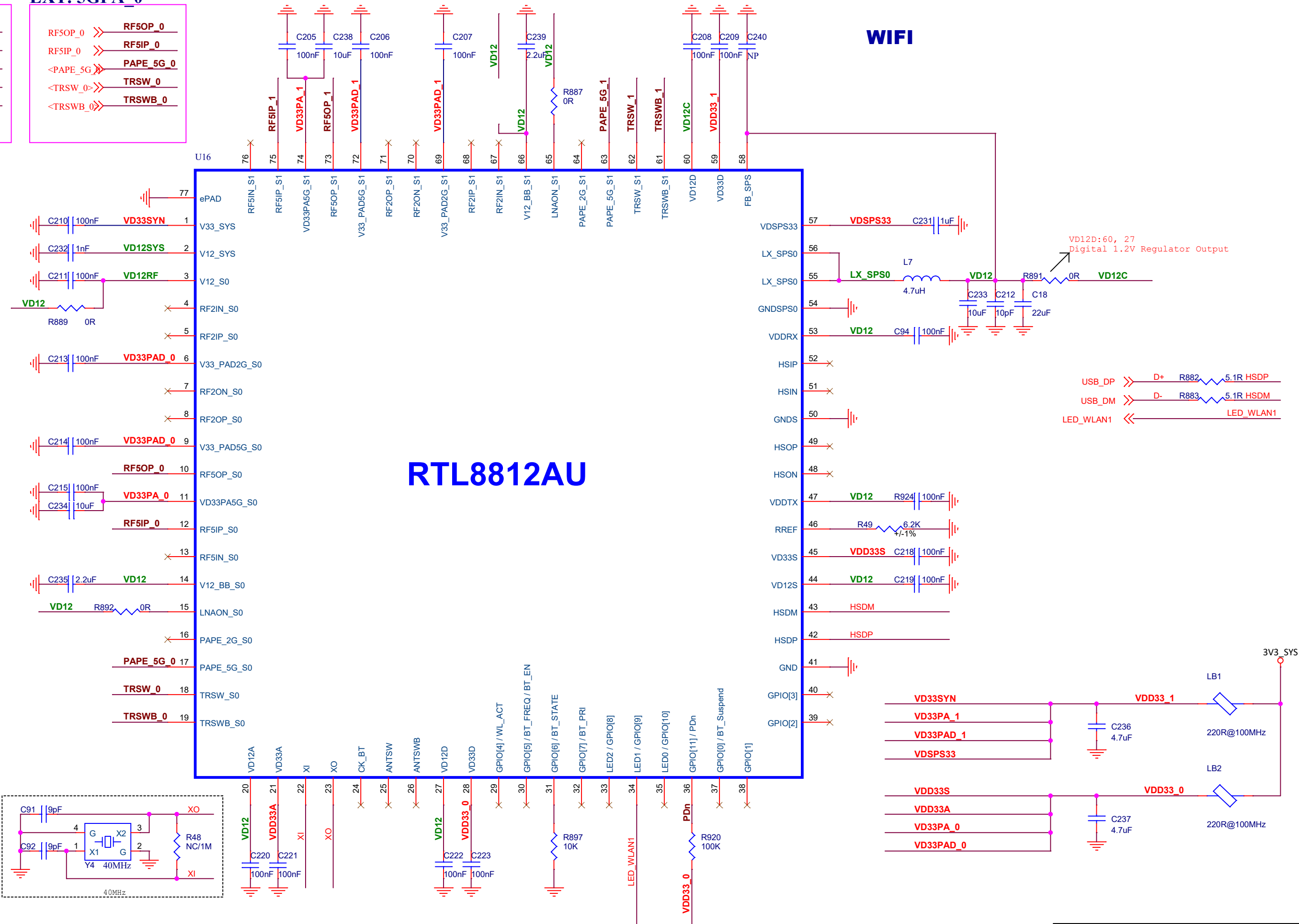
Date:	Sunday, March 17, 2024	Size:	A4	Rev:	1.0
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Designed by:	Lemon	Reviewed by:	Lemon	Sheet:	7 of 10
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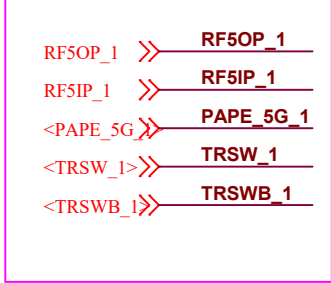
EXT. 5GPA_1



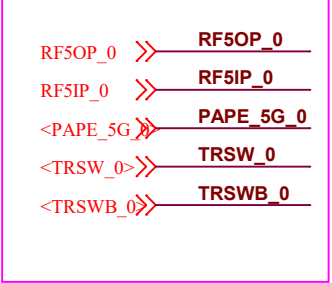
EXT. 5GPA_0



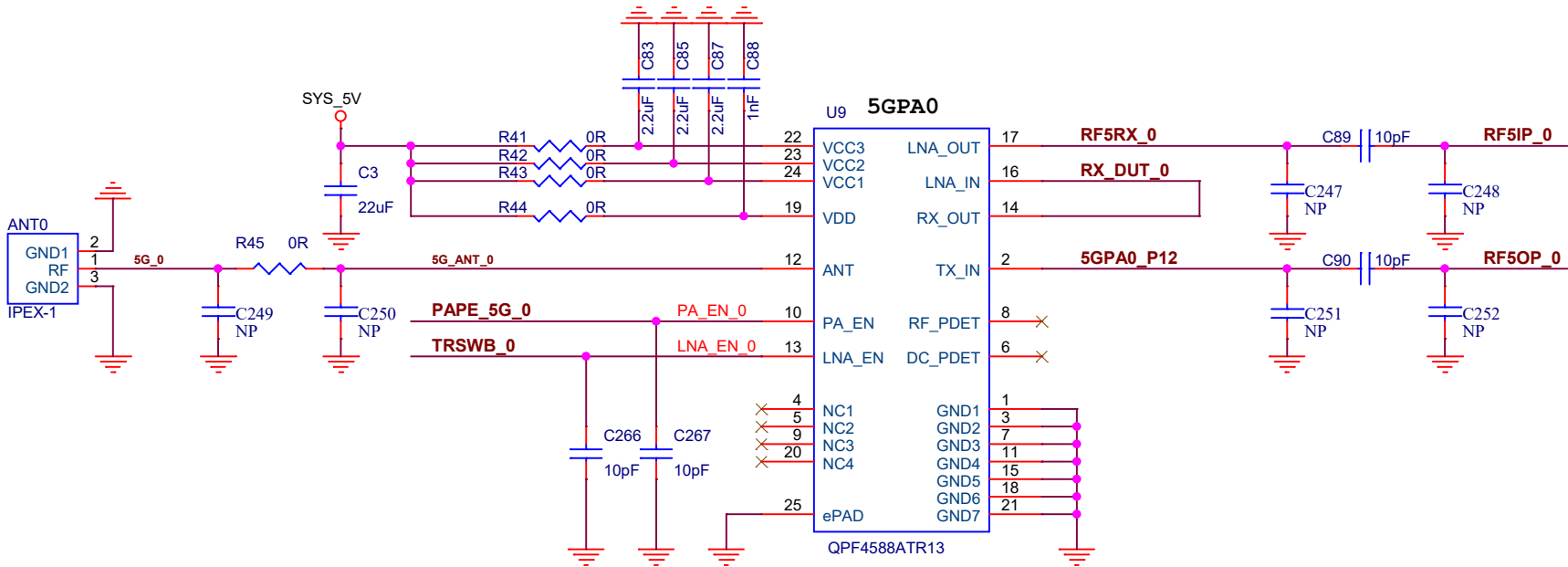
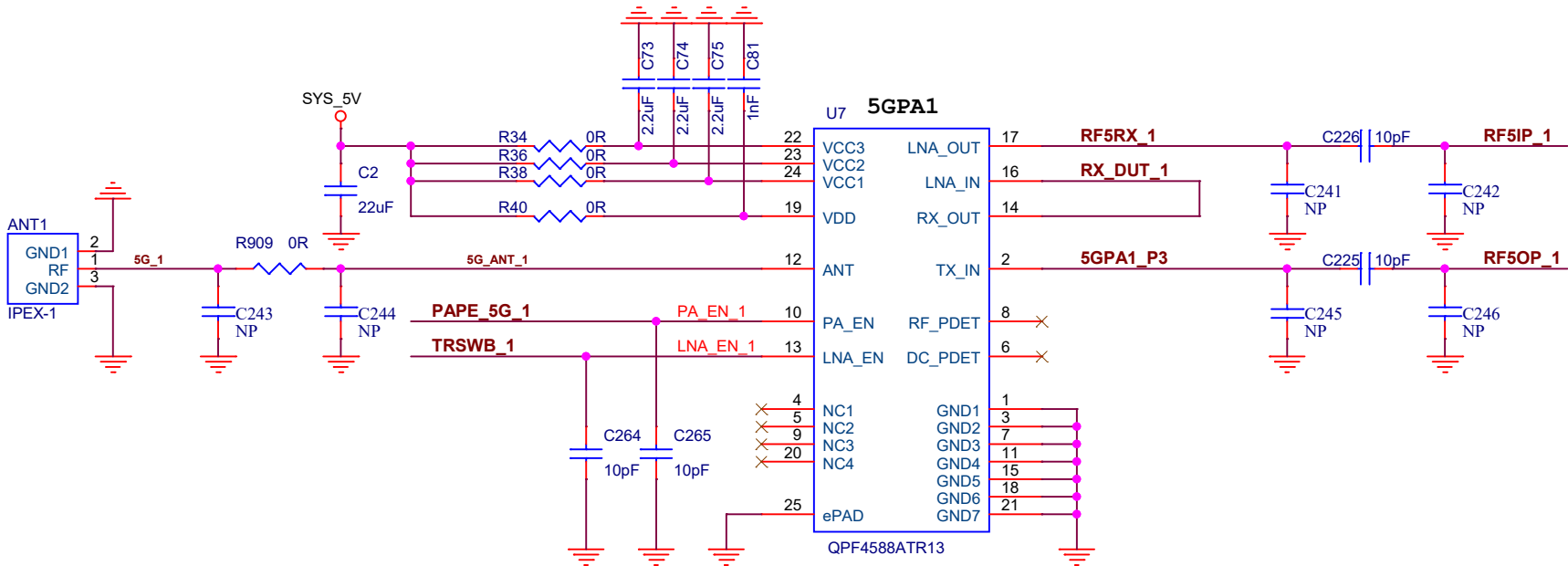
EXT. 5GPA_1



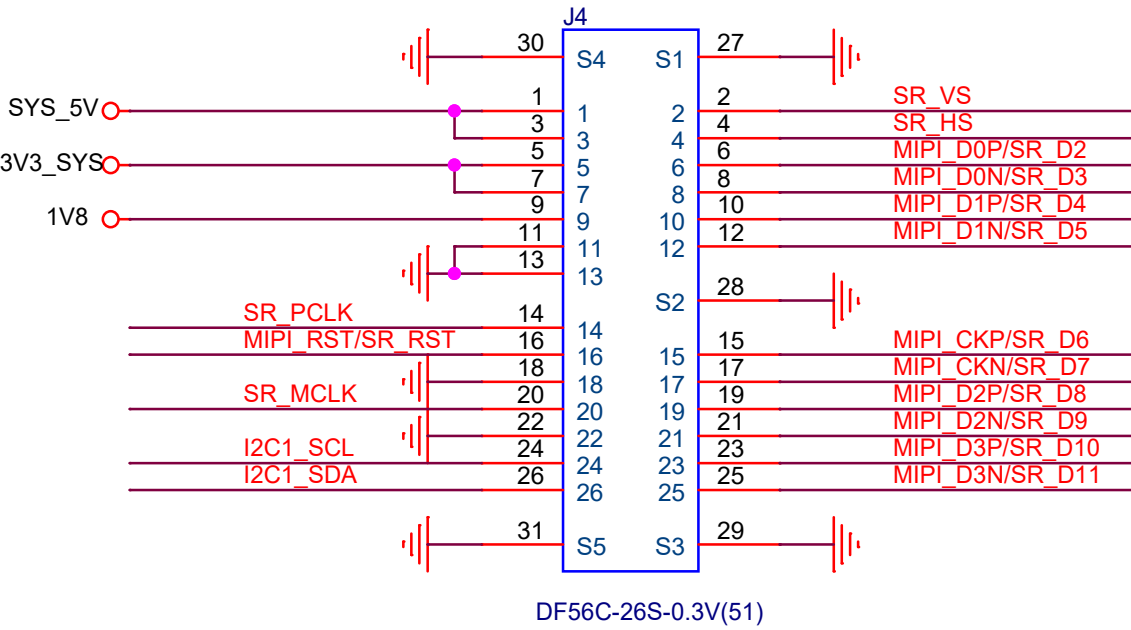
EXT. 5GPA_0



FEM



Sensor

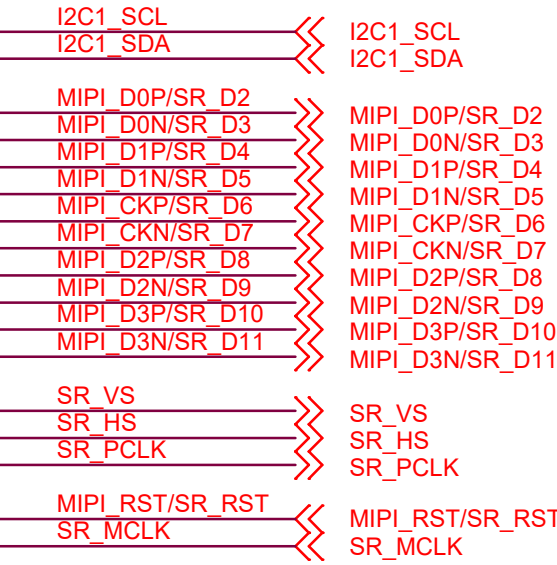


SR_RST
SR_MCLK
MIPI_D1P/D8
MIPI_D1N/D7
MIPI_D0P/D4
MIPI_D0N/D3
MIPI_CKP/D6
MIPI_CKN/D5
MIPI_D2P/D2
MIPI_D2N/D1
MIPI_D3P/D10
MIPI_D3N/D9

G-SENSOR



SENSOR I/F



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Project:	VTX_SSC338				
File:	10. TO SENSOR				
Date:	Sunday, March 17, 2024		Size:	A4	Rev: 1.0
Designed by:	Lemon	Reviewed by:	Lemon	Sheet:	10 of 10