



# ***LCD2S Revision 1, Firmware V1.00***

## **Serial LCD Display**

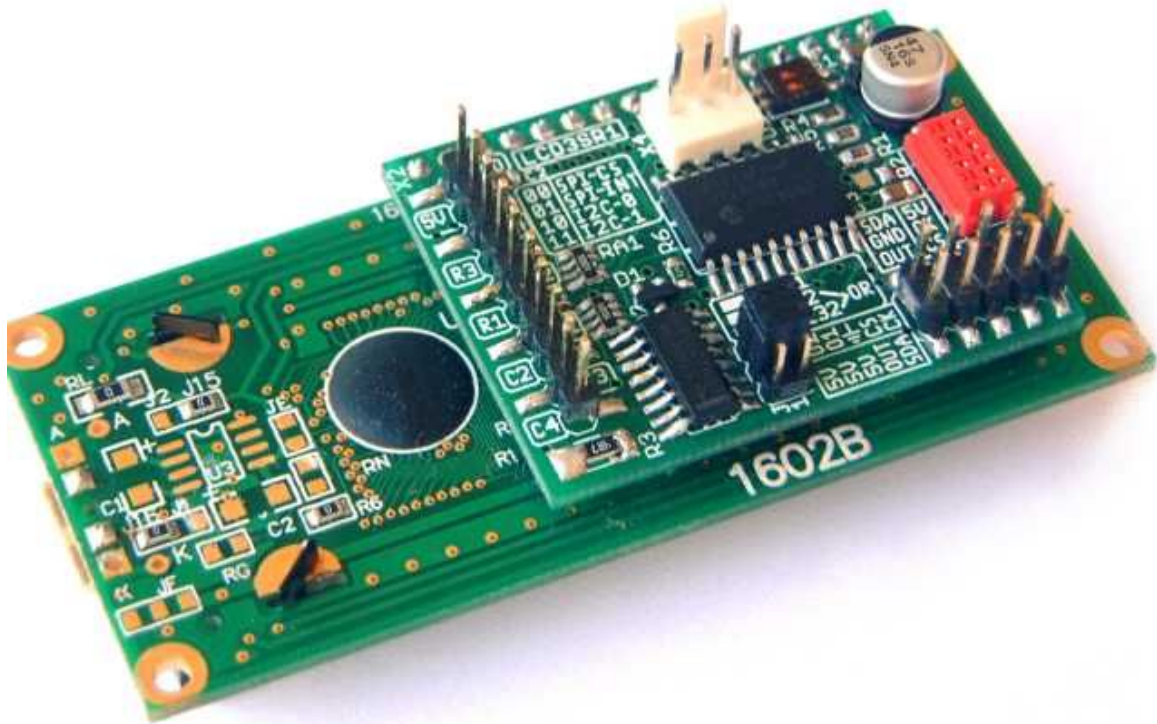
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# 1 Introduction

The following documentation is for the LCD2S Revision 1, which is marked on the PCB as LCD2S REV1!



## 2 Features

- Can be controlled via SPI or I2C bus
- Assembled with 2x16 or 4x20 line character displays, with or without back lighting
- Two high current, open collector outputs (OUT1 and OUT2), with output current of 1000mA each
- 3 general purpose input/output pins (GPIO1, GPIO2 and GPIO3) that can be used for:
  - 2 Analog inputs with 10 bit resolution and 1 digital I/O
  - 3 Digital I/O lines that can be used for inputs or outputs (1k output impedance).
- Keypad encoder for a keypad up to 16 keys (4 rows by 4 columns)
- LCD Backlighting controlled via software, has 254 brightness levels
- LCD Contrast controlled via software, has 254 contrast levels
- User configurable start up screen
- 80 Byte buffer for messages received via serial interface
- 6 Pin Micro Match connector with I2C, SPI and power signals
- 10 Pin (10 pins x 1 row), 2.54mm connector for connecting a keypad of up to 4 rows by 4 columns.
- 10 Pin (5 pins x 2 rows), 2.54mm connector with user outputs, I2C, SPI and power signals
- DIP switch for setting serial mode and I2C address
- Low current consumption - without LCD backlight it is under 1mA
- Assembled with brand name, quality components. For example, electrolytic capacitor used is extra long life rated, which is 5 times more than standard!
- PIC processor can be programmed with custom firmware via 5 in circuit serial programming pins.

## 3 Connectors

The LCD2S has 3 connectors with serial interface, power and I/O signals.

### 3.1 Connector X1 - Micro Match 6 Pin Connector

The LCD2S has a 6 pin Micro Match type connector for controlling it via the SPI or I2C interface.

<i><b>X1 – 6 pin micro match type connector</b></i>		
<i><b>Pin</b></i>	<i><b>I2C Signal</b></i>	<i><b>SPI Signal</b></i>
1	SDA – Serial Data	SDI – Serial data input
2	+5V	+5V
3	GND	GND
4	SCL – Serial Clock	SCK – Serial Clock
5		SCO – Serial Data Output
6	I2C Interrupt	CS – Chip select, active low

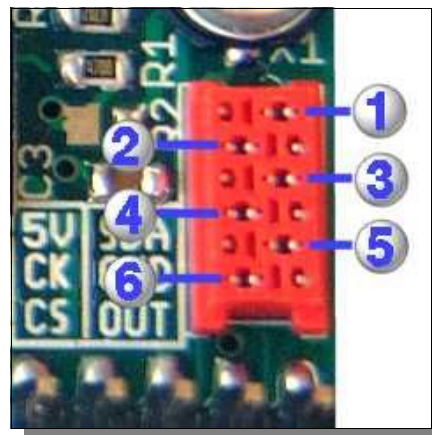


Figure 1

### 3.2 Connector X2 – Keypad and GPIO1 to GPIO3

The LCD2S has a 10 pin, 2.54mm connector that can be configured for the following functions:

- Keypad encoder up to 16 keys (4 rows by 4 columns)
- 2 Analog inputs with 10 bit resolution
- 3 General Purpose Digital I/O lines that can be used for inputs or outputs (1k output impedance).

#### *X2 – 10 pin, 2.54mm header connector*

Pin	Signal
1	Gnd
2	Vcc – 5V supply
3	Row 4
4	Row 3
5	Row 2
6	Row 1
7	Column 1 or GPIO3 (General Purpose I/O)
8	Column 2 or GPIO2 (General Purpose I/O) or AN2 (Analog Input )
9	Column 3 or GPIO1 (General Purpose I/O) or AN1 (Analog Input)
10	Column 4

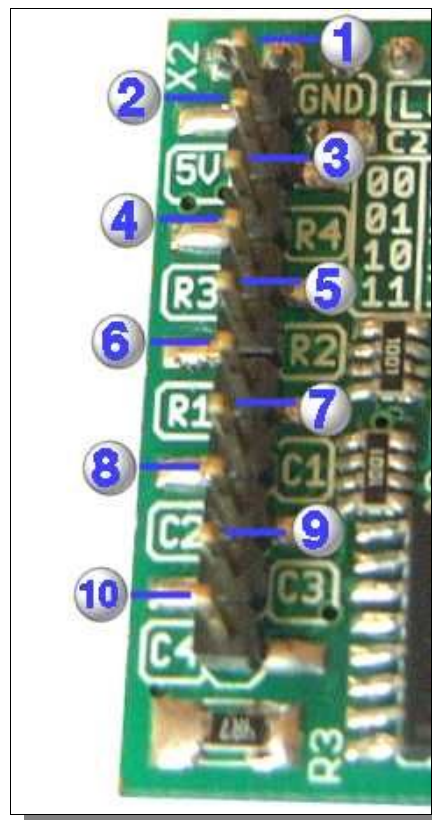


Figure 2

### 3.3 Connector X3 – SPI bus, I<sup>2</sup>C bus, Power, OUT1 and OUT2

The LCD2S has a 2x5 pin, 2.54mm connector with SPI, I<sup>2</sup>C and user output signals. This can be used as an alternative to the 6 pin Micro Match connector for accessing the SPI and I<sup>2</sup>C signals.

Additionally this connector has two user controlled 1000mA outputs.

<i>X3 – 10 pin, 2.54mm header connector</i>		
<i>Pin</i>	<i>Signal</i>	
1	+5V	
2	<b>OUT1</b> – 1000mA open collector output. When off, output is high impedance. When on, output is pulled low to GND.	
3	+5V	
4	<b>OUT2</b> – 1000mA open collector output. When off, output is high impedance. When on, output is pulled low to GND.	
5	+5V	
6	GND	
	<i>I<sup>2</sup>C Signal</i>	<i>SPI Signal</i>
7		SDO – Serial Data Output
8	SDA – Serial Data	SDI – Serial data input
9	I <sup>2</sup> C Interrupt	CS – Chip select, active low
10	SCL – Serial Clock	CLK – Serial Clock

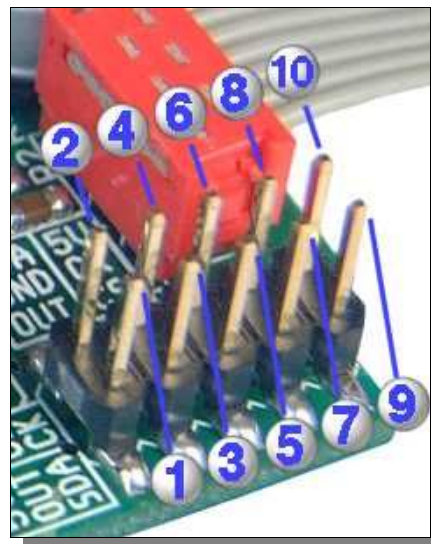


Figure 3

The pinouts on this connector have carefully been selected so that:

- Pin 1 and 2 are next to each other, and can be used for OUT1 output
- Pin 3 and 4 are next to each other, and can be used for OUT2 output
- Pin 6, 8 and 10 are next to each other, and have the two I<sup>2</sup>C signals and ground on them

## 4 OUT1 and OUT2 outputs

The LCD2S has 2 general purpose open collector outputs. These outputs can supply up to 1000mA, and have protection circuitry for driving relays. Both OUT1 and OUT2 are enabled by **default**. They are available via the X3 connector.

Output OUT2 does not share port pins with any other function, and is always enabled.

Output OUT1 shares a port pin with the keypad encoder, and can only be used when the keypad encoder is configured for a 4x3 keypad (4 rows by 3 columns = 12 keys) or smaller. It is enabled by inserting a jumper on the J1 (1x2 pin) pin header. It is enabled by default, and this jumper is present. When the keypad encoder is configured for a 4x4 keypad, this jumper **must be removed**! Output OUT1 can **not** be used when using a 16 key, 4x4 keypad!

## 5 GPIO1 to GPIO3 General Purpose I/Os

The LCD2S has 3 general purpose pins, GPIO1 to GPIO3 that can be used as Digital Inputs, Digital Outputs or Analog Inputs. They are available on the X1 connector. They all share microcontroller port pins with the keypad encoder. The full keypad encoder (16 keys) and all GPIO ports can thus not all be used at the same time. By **default**, GPIO1 to GPIO3 are disabled, and the keypad encoder is enabled! For details on configuration, see Chapter 7. “Keypad and I/O Configuration”.



## 6 Keypad

A keypad of up to 16 keys (4 rows by 4 columns) can be connected to the LCD2S. All keys are filtered via a configurable software debounce filter. Each time a key is pressed, a key code from 'a' to 'f' is added to the 16 byte keypad FIFO buffer. See Figure 4 for a wiring diagram and key codes. The keypad buffer can be read via the “Read Keypad Data” command.

In I<sup>2</sup>C mode there is an I<sup>2</sup>C interrupt line that will be pulled low by the LCD2S if it has pending data. Currently pending data will always be keypad data. If the I<sup>2</sup>C interrupt line is not used, the LCD2S has to be polled each couple of ms to see if it has any keypad data available. This is done by reading the status byte with the “Read Device Status Byte” command. The status byte will indicate if there is any pending keypad data.

## 7 Keypad and I/O Configuration

The keypad can be configured via software and jumpers for a couple of different modes. The reason for this is that some port pins are shared for use by the keypad, the OUT1 output, and the GPIO1 to GPIO3 general purpose I/Os of the LCD2S. The maximum number of keys that can be decoded are 16, when the keypad is configured as 4 rows by 4 columns. The following summarizes when the OUT1 output and GPIO1 to GPIO3 general purpose I/Os are available. A ✓ indicates the function is available.

Keypad Configuration	OUT1	GPIO1 / AN1	GPIO2 / AN2	GPIO3
4x4				
4x3 <b>(Default Configuration)</b>	✓			
4x2	✓	✓		
4x1	✓	✓	✓	
none	✓	✓	✓	✓

By **default** (at delivery), the keypad is configured for a 12 key (4 rows by 3 column) keypad, OUT1 is enabled, GPIO1 to GPIO3 are disabled, and AN1 to AN2 are disabled. The OUT1 jumper (on J1 connector) is also inserted, which is required when output OUT1 is enabled.

The keypad can be configured for the following modes:

### 7.1 Keypad with up to 16 Keys

In this mode, a keypad of up to 4 rows by 4 columns can to be used.. To configure the LCD2S for a 16 key keypad, the following must be done:

- The jumper between pins 5-6 or the J1 header (2x3 pin header) must be **removed**!
- The keypad must be configured for a 4x4 keypad via the “Configure Keypad and IO” command. This command will also disable the OUT1 output and the GPIO1, GPIO2 and GPIO3 general purpose inputs/outputs.
- The OUT1 output and GPIO1, GPIO2 and GPIO3 general purpose inputs/outputs are not available when using a 16 key keypad!

16 Key, 4 row x 4 column Keypad

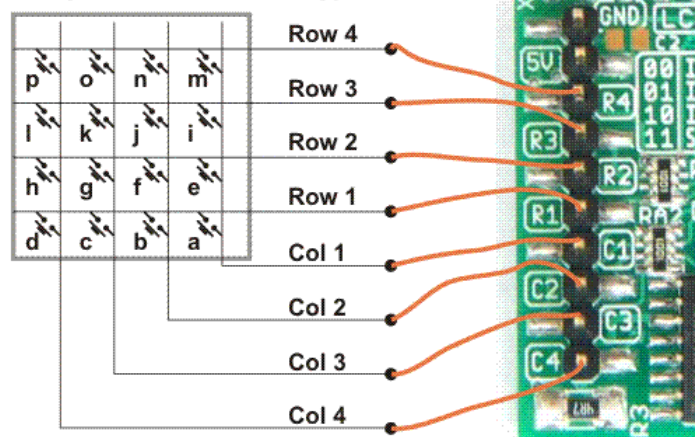


Figure 4: Wiring diagram and key codes for 4x4 keypad

## 7.2 Keypad with up to 12 Keys (Default)

This is the **default** mode. In this mode, a keypad of up to 4 rows by 3 columns can be used. To configure the LCD2S for a 12 key keypad, the following must be done:

- The keypad must be configured for a 4x3 keypad via the “*Configure Keypad and IO*” command. This command will also disable the GPIO1, GPIO2 and GPIO3 general purpose inputs/outputs.
- The GPIO1, GPIO2 and GPIO3 general purpose inputs/outputs are not available when using a 12 key keypad! The OUT1 output is available.
- The keypad must be connected to R1 to R4 (rows 1-4) and C1 to C3 (column 1-3) of the X2 connector.

## 7.3 Keypad with up to 8 Keys

In this mode, a keypad of up to 4 rows by 2 columns can be used. To configure the LCD2S for a 8 key keypad, the following must be done:

- The keypad must be configured for a 4x2 keypad via the “*Configure Keypad and IO*” command. This command will also disable the GPIO2 and GPIO3 general purpose inputs/outputs.
- The GPIO2 and GPIO3 general purpose inputs/outputs are not available when using a 8 key keypad! The OUT1 output and GPIO1 general purpose input/output are available in this mode.
- The keypad must be connected to R1 to R4 (rows 1-4) and C1 to C2 (column 1-2) of the X2 connector.

## 7.4 Keypad with up to 4 Keys

In this mode, a keypad of up to 4 rows by 1 column can be used. To configure the LCD2S for a 4 key keypad, the following must be done:

- The keypad must be configured for a 4x1 keypad via the “*Configure Keypad and IO*” command. This command will also disable the GPIO3 general purpose input/output.
- The GPIO3 general purpose input/output is not available when using a 4 key keypad! The OUT1 output and GPIO1 and GPIO2 general purpose inputs/outputs are available in this mode.
- The keypad must be connected to R1 to R4 (rows 1-4) and C1 (column 1) of the X2 connector.

## 7.5 Keypad with other number of keys

Any keypad with up to 16 keys, and a matrix of not more than 4 rows by 4 columns can be used. For example, a 4 key keypad with 2 rows by 2 columns can be used when the LCD2S is configured for a 16 (4x4), 12 (4x3) or 8 (4x2) key keypad.

# 8 Communication Protocols

The LCD2S board has a standard set of commands that can be sent to it via the I<sup>2</sup>C or SPI interface. The communication protocol used is configured via a 2 position DIP switch. The DIP switch has two switches that can be on (1) or off (0), and thus provides four combinations: 00, 01, 10 and 11.

I<sup>2</sup>C communication is enabled for the first three settings (00, 01 and 10) of the DIP switch. The I<sup>2</sup>C address for each of the 3 positions is different, allowing 3 LCD2S boards to be connected to a single I<sup>2</sup>C bus.

SPI communication is enabled for the last setting (11) of the DIP switch. Standard SPI mode 0 communication protocol is used.

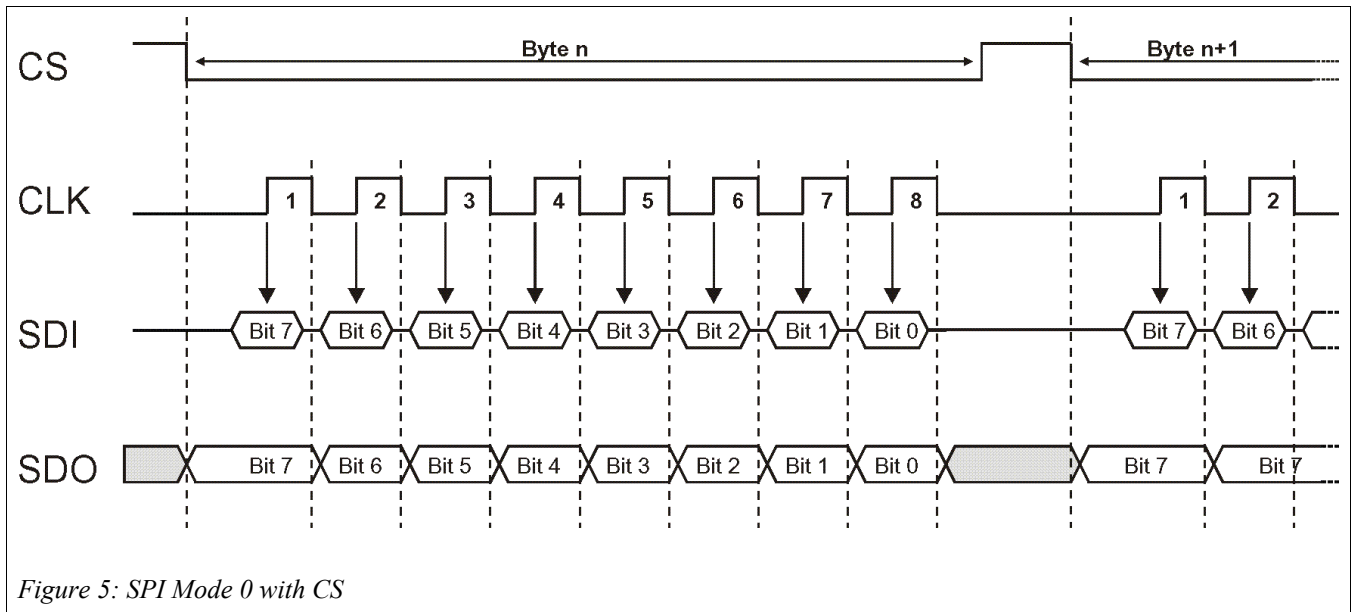
# 9 Communication via SPI

## 9.1 Overview

This communication method is selected by setting the DIP switch to 11 (both switches are in the on position). SPI mode 0 is used. This method uses 4 signals for communication, CS, CLK, SDI and SDO. The SPI CS signal is used to enable communication with the LCD2S. Whenever the CS signal goes low, the SPI port on the LCD2S becomes active. In this state the SDO line will be an output, and the SDI and CLK lines will be inputs. Applying a clock signal to the CLK input will clock data into the LCD2S via the SDI line, and data out via the SDO line.



Figure 5 below shows the timing diagram for this mode of communication.



When the CS signal is set high, the SPI port is disabled. In this state, the SDO line is tri-stated and the SDI input is disabled. The LCD2S will not respond to any data on the SPI bus, and will also not interfere with data been send between any other nodes that potentially share the SPI bus with it.

The 0xF5 (245 decimal) character is a special SYNC character used during this mode of communication. Whenever the LCD2S receives this character, it will reset it's command state machine, and assume the next character received is the start of a new command.

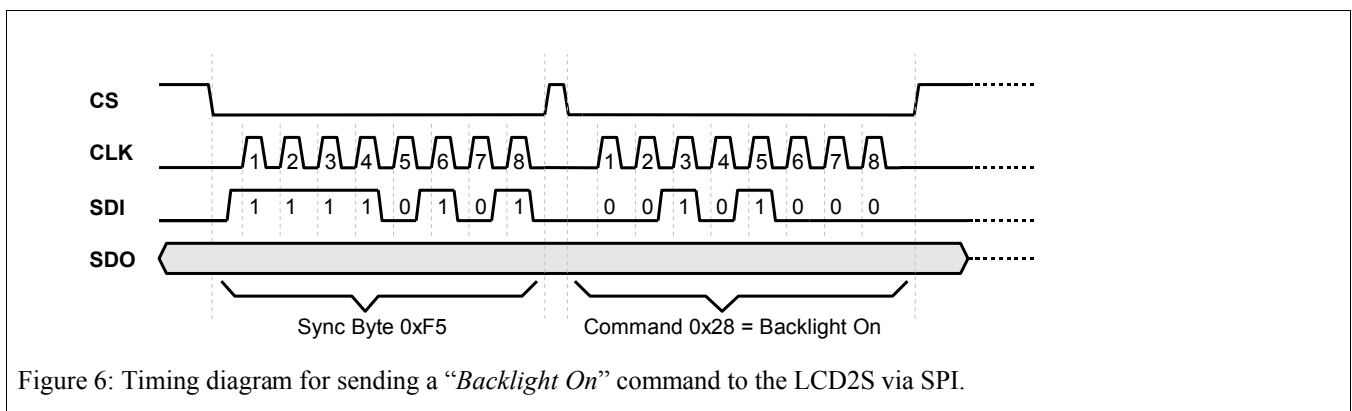
If for some reason a 0xF5 character has to be sent to the LCD2S as part of a command, and it should **not** be interpreted as a SYNC character, two 0xF5 characters have to be send after each other. Whenever the LCD2S sees two 0xF5 characters after each other, it will replace it with a single 0xF5 character, and the command state machine will **not** be reset!

## 9.2 Writing to the LCD2S

To send any of the commands listed in the commands section of this document, a 0xF5 character has to be added to the command sequence. For example, to send a “Backlight On” command to the LCD2S via this mode of communication, the following two bytes have to be sent:

0xF5, 0x28 (or in decimal: 245, 40)

Figure 6 below shows the timing diagram for sending a “Backlight On” command to the LCD2S via SPI.



For SPI timing diagrams see Chapter 14. “Specifications“.

## 9.3 Reading from the LCD2S

When using the SPI communication mode, the command byte must always be preceded by a 0xF5 Sync character. Additionally a dummy byte (value is normally 0) has to be written after the sync byte. The dummy byte is required so that the LCD2S has time to prepare the requested data. Following the dummy byte, the requested bytes can be read from the LCD2S. The bits will be shifted out with the most significant bit (MSB) first, and bit 0 last. The output bits will be valid during the high level of the CLK signal.

For example, to read the “Status Byte” of the LCD2S via this mode of communication, we have to send three bytes and read one:

Send 0xF5 Sync byte, send 0xD0 command, send 0x00 dummy, read Status byte (or in decimal: 245, 208, 0)

Figure 7 below shows the timing diagram for reading the “Status Byte” of the LCD2S.

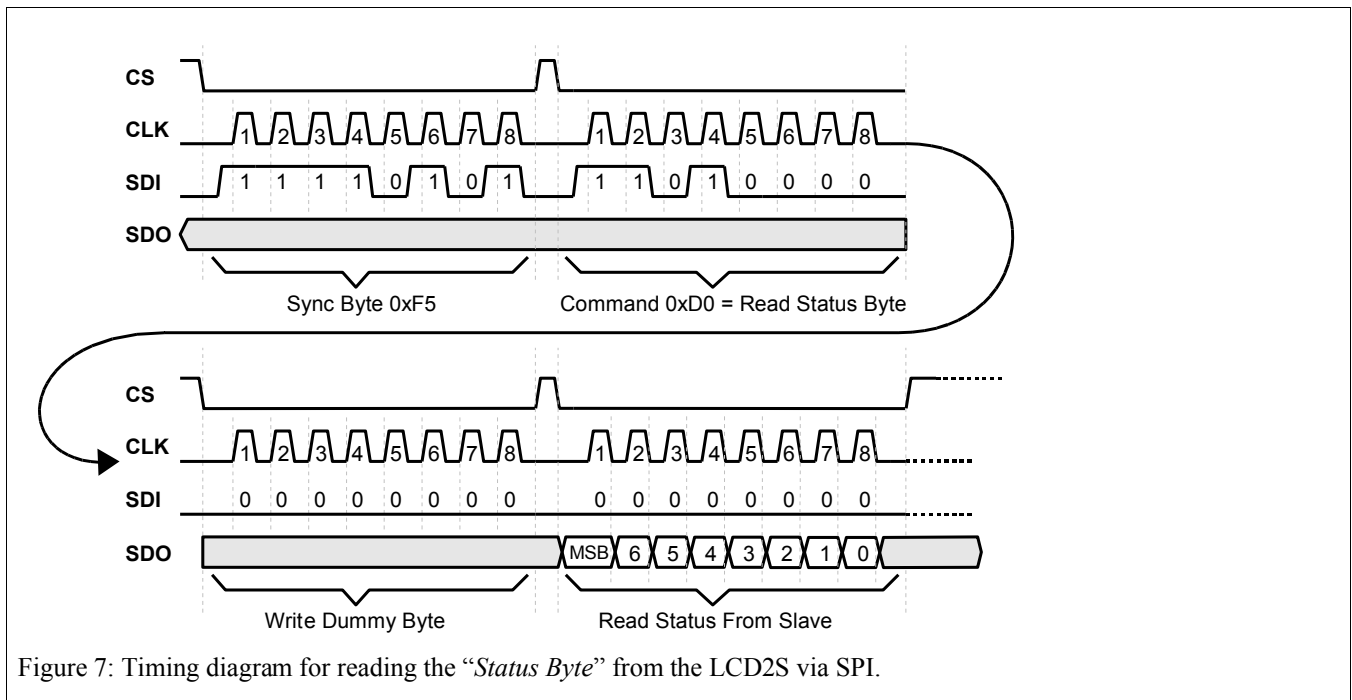


Figure 7: Timing diagram for reading the “Status Byte” from the LCD2S via SPI.

For SPI timing diagrams see Chapter 14. “Specifications”.

## 10 Communication via I<sup>2</sup>C

### 10.1 Overview

The LCD2S can be configured to be an I<sup>2</sup>C slave device by setting the DIP switches to 00, 01 or 10. The bidirectional I<sup>2</sup>C bus consists of the serial clock (SCL) and serial data (SDA) lines. Both lines must be connected to a positive supply via a pullup resistor. The LCD2S doesn't have any pullup resistors, and they will normally be provided by the bus master. A typical value is between 2k2 and 1k. Additionally there is also an I<sup>2</sup>C interrupt line that will be pulled low by the LCD2S if it has pending data. Currently pending data will always be keypad data. If the I<sup>2</sup>C interrupt line is not used, the LCD2S has to be polled each couple of ms to see if it has any keypad data available.

I<sup>2</sup>C communication with this device is initiated by a master sending a Start condition, a high-to-low transition on the SDA input/output while the SCL input is high, see Figure 8. After the Start condition, the device address byte is sent, MSB first, including the data direction bit (R/W). This device does not respond to the general call address.

After receiving the valid address byte, this device responds with an ACK, a low on the SDA input/output during the high of the ACK-related clock pulse.

On the I<sup>2</sup>C bus, only one data bit is transferred during each clock pulse. The data on the SDA line must remain stable during the high pulse of the clock period, as changes in the data line at this time will be interpreted as a Start or Stop condition, see Figure 8.

A Stop condition, a low-to-high transition on the SDA input/output while the SCL input is high, is sent by the master, see Figure 8.

Any number of data bytes can be transferred from the transmitter to the receiver between the Start and the Stop conditions. Each byte of eight bits is followed by one ACK bit. The transmitter must release the SDA line before the receiver can send an ACK bit. The device that acknowledges must pull down the SDA line during the ACK clock pulse so that the SDA line is stable low during the high pulse of the ACK-related clock period. When a slave receiver is addressed, it must generate an ACK after each byte is received. Similarly, the master must generate an ACK after each byte that it receives from the slave transmitter. Setup and hold times must be met to ensure proper operation.

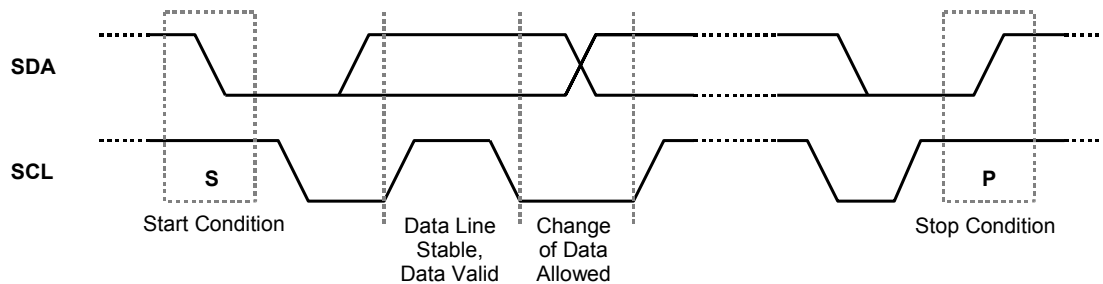


Figure 8: I<sup>2</sup>C Start condition, Stop condition, and Data

Figure 9 below shows the timing diagram for writing a command to the LCD2S.

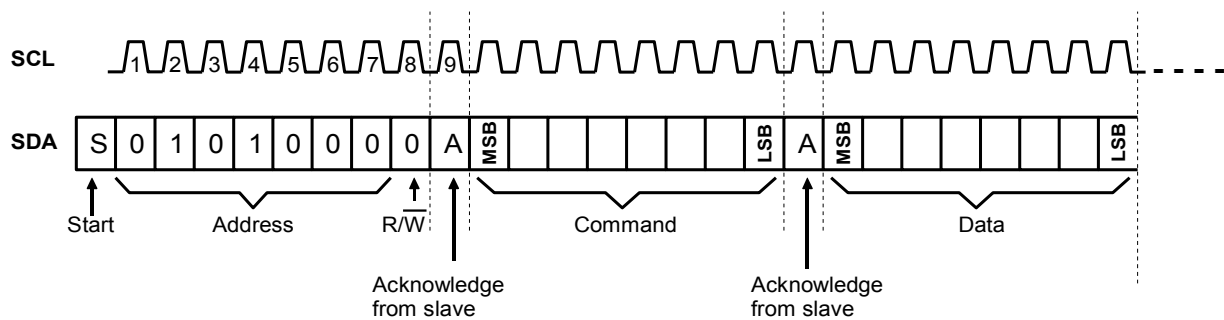
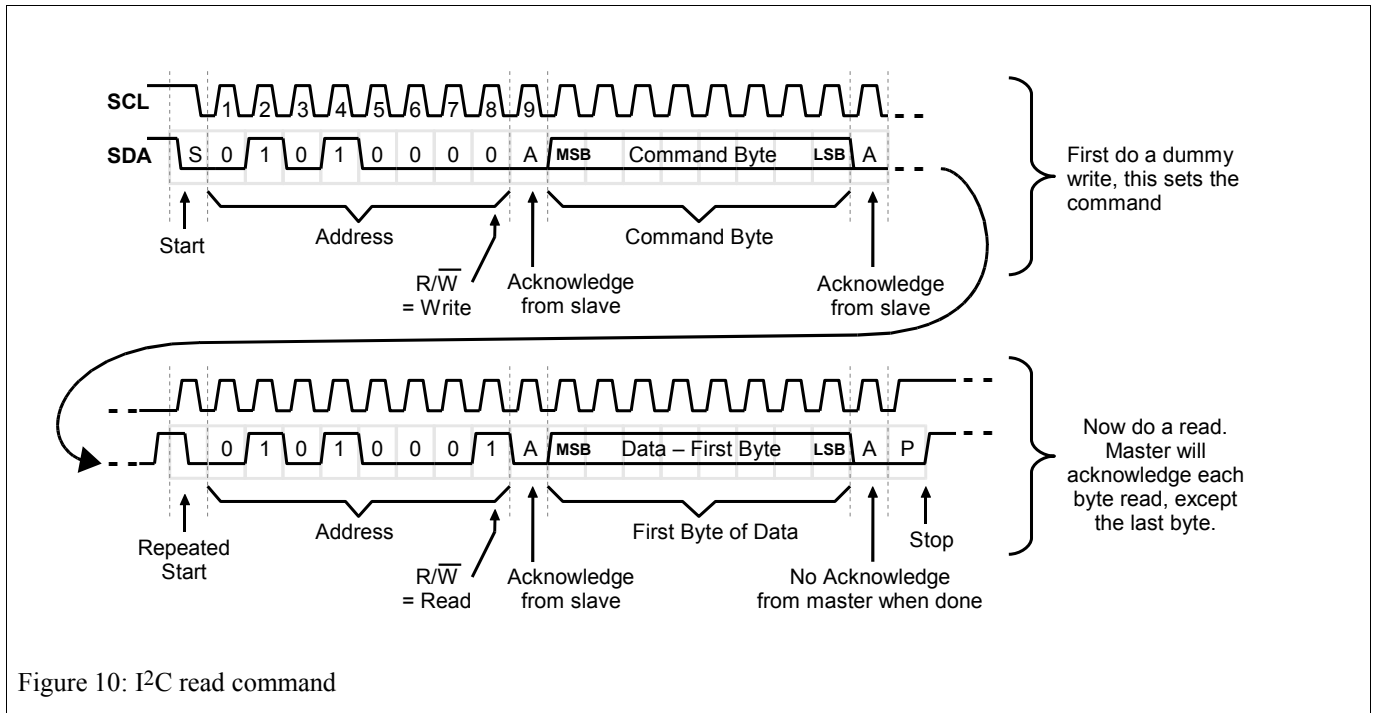


Figure 9: I<sup>2</sup>C write command

Reading data from an I<sup>2</sup>C slave device is a two stage process. Following the start condition, an address and command byte is written to the slave device. The R/W bit of the address byte is set for writing. This is followed by a repeated start condition. A repeated start condition is when a start is sent following a previous start with no stop condition send between them. Following the repeated start condition, an address byte is written to the slave (R/W bit of address is set for reading) followed by consecutive byte reads. The master will acknowledge each byte read, except the last one. The slave will thus continue sending data as long as the master acknowledges it.

Figure 10 below shows the timing diagram for reading a command from the LCD2S.



## 10.2 I2C Address

The LCD2S can be configured to be an I2C slave device by setting the DIP switches to 00, 01 or 10. Each position will configure the LCD2S to have an address relative to the I2C base address. The default I2C base address is 0x50 (decimal 80).

- For the default I2C base address, DIP switch setting 00 configures the I2C address to be 0x50 (decimal 80) for writing to the LCD2S, and 0x51 (decimal 81) for reading from it. When the I2C base address is modified via the “Set I2C base address” command, the read address is the base address, and the write address is the base address + 1.
- For the default I2C base address, DIP switch setting 01 configures the I2C address to be 0x52 (decimal 82) for writing to the LCD2S, and 0x53 (decimal 83) for reading from it. When the I2C base address is modified via the “Set I2C base address” command, the read address is the base address + 2, and the write address is the base address + 3.
- For the default I2C base address, DIP switch setting 10 configures the I2C address to be 0x54 (decimal 84) for writing to the LCD2S, and 0x55 (decimal 85) for reading from it. When the I2C base address is modified via the “Set I2C base address” command, the read address is the base address + 4, and the write address is the base address + 5.

Without modifying the I2C base address, it is thus possible having 3 LCD2S devices on a single I2C bus. By modifying the I2C base address, many more can be used. The exact number will depend on the length and speed of the bus, seeing that I2C can not work over very long distances at high speeds.

## 10.3 Writing to the LCD2S

Writing a command to the LCD2S via I2C follows the standard I2C format for doing a byte write. It is initiated with an I2C start condition. The I2C Start condition will reset the internal command state machine of the LCD2S. The next byte received will be seen as the first byte of a new command. The current command is ended as soon as a stop condition is detected. To send any of the commands listed in the commands section of this document, it has to be preceded by a start condition and the device address. For example, to send a “Backlight On” command to a LCD2S with an I2C write address of 0x50 (both DIP switches set to off), the following bytes have to be sent:

[I2C Start Condition] [0x50 byte] [0x28 byte] [I2C Stop Condition]

This is shown in Figure 11 below.

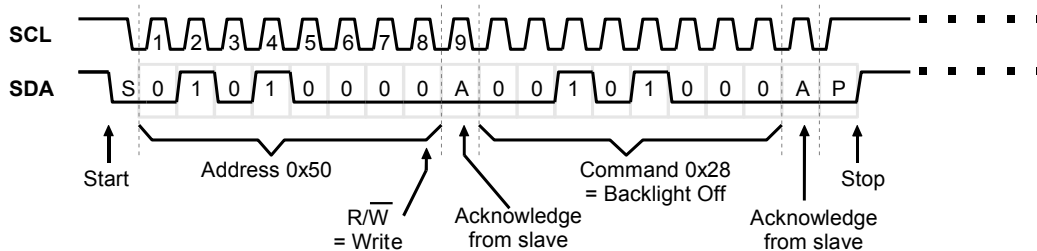


Figure 11: I<sup>2</sup>C backlight off command to LCD2S with write address 0x50

## 10.4 Reading from the LCD2S

Reading from the LCD2S via I<sup>2</sup>C follows the standard I<sup>2</sup>C format for doing a byte write followed by a byte read. It is thus a two stage process. During the byte write, only the address and command bytes are written to the LCD2S. During the second stage, the address byte is written again, followed by consecutive reads, until all required data has been obtained.

Following the start condition, an address and command byte is written to the slave device. The R/W bit of the address byte is set for writing. This is followed by a repeated start condition. A repeated start condition is when a start is sent following a previous start with no stop condition send between them. Following the repeated start condition, an address byte is written to the slave (R/W bit of address is set for reading) followed by consecutive byte reads. The master will acknowledge each byte read, except the last one. The slave will thus continue sending data as long as the master acknowledges it.

For example, to read the status byte of a LCD2S with an I<sup>2</sup>C write address of 0x50 (both DIP switches set to off), the following commands have to be sent:

[I<sup>2</sup>C Start Condition] [0x50 byte] [0xd0 byte] [I<sup>2</sup>C Repeated Start Condition] [0x51 byte] [Read Status byte] [I<sup>2</sup>C Stop Cond.]  
 The second address byte will be 0x51, seeing that the R/W bit (position 0) is set for a read. All bytes are sent from the master, except for the last byte that is sent from the LCD2S.

Figure 12 below shows the timing diagram for reading a command from the LCD2S.

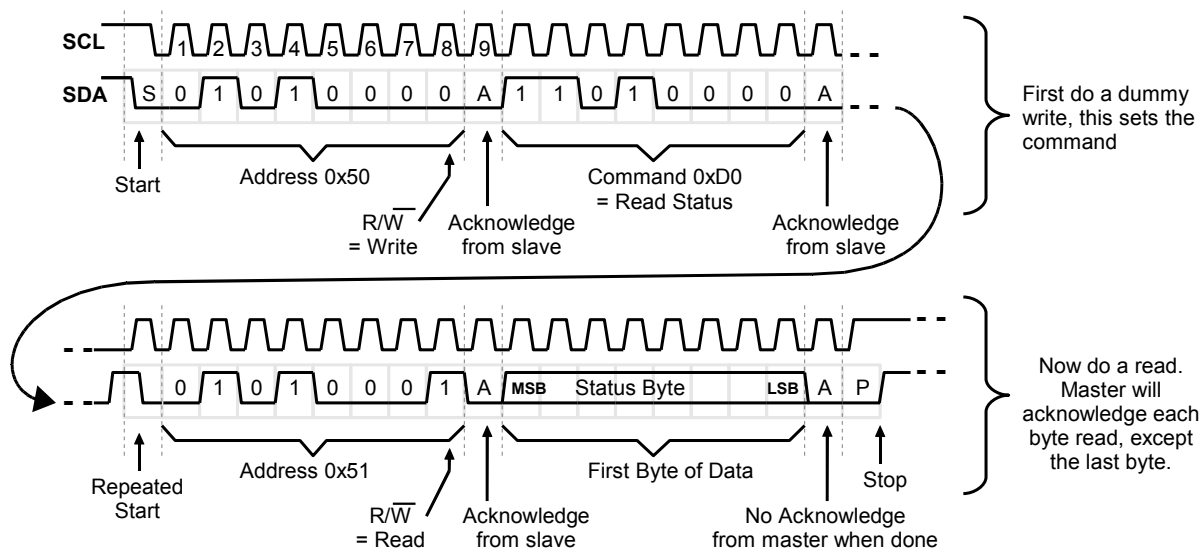


Figure 12: I<sup>2</sup>C read status byte from LCD2S with write address 0x50



## 11 Display Format

The LCD display has two rows of 16 characters each. When writing to it, characters are written to the current cursor position, after which the cursor is incremented or decremented (depending on how it is configured). The cursor can be configured to be an underline (non blinking) cursor, a blinking block cursor, both or off. When configured to move forward, the cursor will wrap around from the last position in the first row to the first position of the second row, and from the last position in the second row to the first position in the first row. To cursor can be moved to any location on the display by issuing either the “Set Cursor Address” command or the “Set Cursor Location” command. The address of each position on the display is shown in figure 13 below.

0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31

Figure 13: Display Addresses

The column numbers are shown in figure 14 below.

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16

Figure 14: Column Numbers

## 12 Commands

The LCD2S can accept commands via any of it's communication interfaces.

- Hex values are shown in 0xnn format, where nn is the hexadecimal value.
- Strings are shown as “string”, and represents a string of bytes that are mapped to the Character Set shown in Figure 23, English/European Character Set
- Byte parameters to a command are shown as [value]
- Bytes read from the LCD2S are shown as <value>

### 12.1 State On Power-up

On power up, configuration data from the non volatile memory (EEPROM) is used to configure the LCD2S. This configuration data can be modified by the user by issuing a “Remember Command” followed by the command that is to be remembered. Only the command following the “Remember Command” will be remembered. To remember multiple command, the “Remember Command” has to be issues before each one. Only certain commands can be remembered. Each command's description will specify if it can be remembered or not.

The “Remember Command” should not be used unnecessarily for the following reasons:

- It is very slow. Each byte that is stored to non volatile memory takes about 6ms.
- The non volatile memory can only be written to a limited (about 100,000 times) number of times.

Because a remember command is very slow (about 6ms) the user should check how many bytes are still available in the receive buffer (Read Status Byte command) before issuing multiple remember commands! All commands written after a remember command will also only be executed (will remain in the receive buffer) once the remember command has been executed.

### 12.2 General Commands

#### 12.2.1 Remember Command

<b>Command:</b> 0x8D	<b>Default:</b> Yes	<b>Can be remembered:</b> Yes
----------------------	---------------------	-------------------------------

The command following this command will be remembered (stored in non volatile memory), and will be restored next time the unit

is powered up. Only certain commands can be remembered. Each command's description will specify if it can be remembered or not. See Chapter 7. “State On Power-up” for details.

### 12.2.2 Read Device Status Byte

<b>Command I<sup>2</sup>C:</b> 0xD0, <read value> <b>Command SPI:</b> 0xD0, 0x00, <read value>	<b>Default:</b> -	<b>Can be remembered:</b> No
---	-------------------	------------------------------

This command reads the current device status register. When executing this command via the SPI port, a dummy byte must be sent after the 0xD0 command! The bits of this register have the following meaning:

**Bit 7 – Keypad Buffer Contains Data:** When set, this bit indicates that the keypad buffer contains data. The keypad data can be read with the “Read Keypad Data” command.

**Bits 0-6 – Receive buffer space available:** Bits 0-6 indicate how much of the 80 byte receive buffer is still available. It is very important that the receive buffer does not overflow! The user should never write more bytes to the LCD2S than it has space for in its receive buffer.

## 12.3 Configuration Commands

### 12.3.1 Configure Interrupt Pin As Open Collector Output

<b>Command:</b> 0x2A	<b>Default:</b> Yes	<b>Can be remembered:</b> Yes
----------------------	---------------------	-------------------------------

This command only affects the I<sup>2</sup>C communication mode. It configures the CS signal (used to indicate a pending interrupt on the LCD2S) to be an open collector output. This means that an external pull-up resistor is required on the CS signal. It has the advantage that multiple nodes can share the CS line to indicate they have a pending interrupt available. If only a single device will be connected to the CS line, then configure the interrupt pin to be a push-pull output. The following conditions will cause the interrupt pin to be active:

- If there is any keypad data available to be read

### 12.3.2 Configure Interrupt Pin As Push-Pull Output

<b>Command:</b> 0x22	<b>Default:</b> No	<b>Can be remembered:</b> Yes
----------------------	--------------------	-------------------------------

This command only affects the I<sup>2</sup>C communication mode. It configures the CS signal (used to indicate a pending interrupt on the LCD2S) to be a push-pull type output. No external pull-up resistor is required on the CS line, and only a single device (a LCD2S in this case) can be connected to it.

### 12.3.3 Set I2C base address

<b>Command:</b> 0x91	<b>Default:</b> 0x50	<b>Can be remembered:</b> Required
----------------------	----------------------	------------------------------------

This command only affects the I<sup>2</sup>C communication mode, and is **only** executed if preceded by the remember command! It sets the I<sup>2</sup>C base address to the given value. The I<sup>2</sup>C base address is used to determine the I<sup>2</sup>C address when reading from, or writing to the LCD2S. See the “I<sup>2</sup>C Address” section of this document for details.

### 12.3.4 Configure Keypad and IO

<b>Command:</b> 0xE0, [value]	<b>Default:</b> 1 = 4x3 keypad, OUT1 on, GPIO1 to GPIO3 disabled, AN1 and AN2 disabled	<b>Can be remembered:</b> Required
-------------------------------	--	------------------------------------

This command configures the keypad encoder, the OUT1 open collector output, and the 3 general purpose inputs/outputs GPIO1 to GPIO3. This command is **only** executed if preceded by the remember command! Because these functions share CPU port pins, they can not all be enabled at the same time. The available configuration options are:

	Keypad	OUT1	GPIO1 / AN1	GPIO2 / AN2	GPIO3
0x00	4x4				
0x01	4x3	√			
0x03	4x2	√	√		
0x07	4x1	√	√	√	
0x0f	none	√	√	√	√

√ indicates enabled

When OUT1 is configured to be disabled, the OUT1 jumper (J1 connector on PCB) must be removed. When OUT1 is configured to be enabled, the OUT1 jumper must be inserted!

At delivery, the **default configuration** is for a 4x3 keypad, OUT1 enabled, GPIO1 to GPIO3 disabled and AN1 and AN2 disabled. The OUT1 jumper (on J1 connector) is also inserted. For details on configuration, see Chapter 7. “Keypad and I/O Configuration”.

Seeing that this command is written to the LCD2S's non volatile memory (remembered), it **takes about 6ms to be executed!** Be sure to check how many bytes are still available in the receive buffer (Read Status Byte command), so that the receive buffer does not overflow! Additionally the non volatile memory can only be written to a limited (about 100,000 times) number of times, so **don't call this command too often!**

### 12.3.5 Configure GPIO1

<b>Command:</b> 0xE3, [value]	<b>Default:</b> Digital Input	<b>Can be remembered:</b> Required
-------------------------------	-------------------------------	------------------------------------

The command configures GPIO1 to be an Analog Input, Digital Input or Digital Output. This command is **only** executed if preceded by the remember command! Currently **only** the Digital output function has been implemented! The other functions will be implemented in future releases of the LCD2S. The following values are defined:

- 0x00 = Digital Input (**not implemented yet**)
- 0x01 = Digital Output
- 0x02 = Analog Input (**not implemented yet**)
- 0x05 = Digital Input with Pull-up resistor enabled (**not implemented yet**)
- 0x06 = Analog Input with Pull-up resistor enabled (**not implemented yet**)

To be able to use GPIO1, the keypad must be configured for 8 keys (4 row by 2 column) or less. For details on configuration, see Chapter 7. “Keypad and I/O Configuration”.

Seeing that this command is written to the LCD2S's non volatile memory (remembered), it **takes about 6ms to be executed!** Be sure to check how many bytes are still available in the receive buffer (Read Status Byte command), so that the receive buffer does not overflow! Additionally the non volatile memory can only be written to a limited (about 100,000 times) number of times, so **don't call this command too often!**

### 12.3.6 Configure GPIO2

<b>Command:</b> 0xE4, [value]	<b>Default:</b> Digital Input	<b>Can be remembered:</b> Required
-------------------------------	-------------------------------	------------------------------------

The command configures GPIO2 to be an Analog Input, Digital Input or Digital Output. This command is **only** executed if preceded by the remember command! Currently **only** the Digital output function has been implemented! The other functions will be implemented in future releases of the LCD2S. The following values are defined:

- 0x00 = Digital Input (**not implemented yet**)
- 0x01 = Digital Output
- 0x02 = Analog Input (**not implemented yet**)
- 0x05 = Digital Input with Pull-up resistor enabled (**not implemented yet**)
- 0x06 = Analog Input with Pull-up resistor enabled (**not implemented yet**)

To be able to use GPIO2, the keypad must be configured for 4 keys (4 row by 1 column) or less. For details on configuration, see Chapter 7. “Keypad and I/O Configuration”.

Seeing that this command is written to the LCD2S's non volatile memory (remembered), it **takes about 6ms to be executed!** Be sure to check how many bytes are still available in the receive buffer (Read Status Byte command), so that the receive buffer does not overflow! Additionally the non volatile memory can only be written to a limited (about 100,000 times) number of times, so **don't call this command too often!**

### 12.3.7 Configure GPIO3

<b>Command:</b> 0xE5, [value]	<b>Default:</b> Digital Input with Pull-up resistor	<b>Can be remembered:</b> Required
-------------------------------	---	------------------------------------

The command configures GPIO3 to be a Digital Input. GPIO3 can only be configured to be an input! This command is **only** executed if preceded by the remember command! Currently this function has **not been implemented**, and will only be implemented in future releases of the LCD2S. The following values are defined:

- 0x00 = Digital Input with Pull-up resistor enabled (**not implemented yet**)

GPIO3 can only be used when the keypad is disabled! For details on configuration, see Chapter 7. “Keypad and I/O Configuration”. When used as an input, it always has a 47k pull-up resistor enabled.

Seeing that this command is written to the LCD2S's non volatile memory (remembered), it **takes about 6ms to be executed!** Be sure to check how many bytes are still available in the receive buffer (Read Status Byte command), so that the receive buffer does not overflow! Additionally the non volatile memory can only be written to a limited (about 100,000 times) number of times, so **don't call this command too often!**

## 12.4 LCD Display Commands

### 12.4.1 Write Parsed String to LCD

<b>Command:</b> 0x80, “String”	<b>Default:</b> -	<b>Can be remembered:</b> No
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Writes the given string to the LCD. All characters are mapped to the Character Set shown in Figure 23, English/European Character Set. The string is parsed for escape sequence characters that perform the following actions:

- 0x0a ('\n') = Go to beginning of next line.
- 0x0c ('\f') = Clear display and go to beginning of first line.
- 0x0d ('\r') = Go to beginning of first line
- 0x08 ('\b') = Cursor left
- 0x09 ('\t') = Cursor Right

The escape sequence addresses don't store any characters in the Character Set, so all possible characters can still be displayed.

For example, to write “Hello” to line 1, and “world” to line two, the following string can be written to the LCD2S:  
- “\fHello\nworld”

### 12.4.2 Set Startup Screen

<b>Command:</b> 0x90, [line], “String”	<b>Default:</b> -	<b>Can be remembered:</b> Always
--	-------------------	----------------------------------

Sets a single line of the startup screen. The startup screen is displayed when the LCD is switched on.

The *line* parameter of this command gives the LCD line that the following string is for. For a two line display (16 by 2 lines for example), the line parameter can be 1 or 2. For a 4 line display (20 by 4 lines for example), the line parameter can be 1, 2, 3 or 4.

The *string* parameter of this command should be a maximum of 20 characters long. For a 2 by 16 line display, the string will be 16 characters long. For a 4 by 20 line display the string will be 20 characters long.

For example, to set line one of a 2 by 16 line display to “This is a really” and line two to “cool LCD Display”, the following two

commands have to be sent to the LCD2S:

The first command writes “This is a really” string to line 1:

Command Offset	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17
Command	0x90	0x01	T	h	i	s		i	s		a		r	e	a	l	l	y

The second command writes “cool LCD Display” string to line 2:

Command Offset	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17
Command	0x90	0x02	c	o	o	l		L	C	D		D	i	s	p	l	a	y

Because this command stores the given string to the LCD2S's internal EEPROM, it is very slow. About 6ms should be allowed for each character of the given string. This means, that if a 16 character string is written, the command will take about 16 x 6ms = 96ms to be processed.

### 12.4.3 Backlight On

<b>Command:</b> 0x28	<b>Default:</b> Yes	<b>Can be remembered:</b> Yes
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Switches the display on. The display brightness will be the value last set with the “*Set Backlight Brightness*” command.

### 12.4.4 Backlight Off

<b>Command:</b> 0x20	<b>Default:</b> Yes	<b>Can be remembered:</b> Yes
----------------------	---------------------	-------------------------------

Switches the display off. The display brightness will not be modified, and will be restored the next time the “*Display On*” command is issued.

### 12.4.5 Set Backlight Brightness

<b>Command:</b> 0x81, [value]	<b>Default:</b> Yes	<b>Can be remembered:</b> Yes
-------------------------------	---------------------	-------------------------------

Sets the current brightness level of the backlight. The “value” parameter has to be a value from 0 to 255, where 0 is off and 255 will be maximum brightness.

### 12.4.6 Cursor moves forward

<b>Command:</b> 0x09	<b>Default:</b> Yes	<b>Can be remembered:</b> Yes
----------------------	---------------------	-------------------------------

Causes the cursor (or display) to move one position forward each time a character is written to the display. The default mode of the LCD2S is for the cursor to move forward.

### 12.4.7 Cursor moves backwards

<b>Command:</b> 0x01	<b>Default:</b> No	<b>Can be remembered:</b> Yes
----------------------	--------------------	-------------------------------

Causes the cursor (or display) to move one position backwards each time a character is written to the display.

### 12.4.8 Blinking Block Cursor On

<b>Command:</b> 0x18	<b>Default:</b> No	<b>Can be remembered:</b> Yes
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Turns the blinking block cursor on. It is displayed at the current cursor position. It is a large, blinking block that is displayed at the current cursor position.



**12.4.9 Blinking Block Cursor Off**

<b>Command:</b> 0x10	<b>Default:</b> Yes	<b>Can be remembered:</b> Yes
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Turns the blinking block cursor off. It is displayed at the current cursor position. It is a large, blinking block that is displayed at the current cursor position.

**12.4.10 Underline Cursor On**

<b>Command:</b> 0x19	<b>Default:</b> No	<b>Can be remembered:</b> Yes
----------------------	--------------------	-------------------------------

Turns the underline cursor on. It is displayed at the current cursor position. This is not the blink cursor, and can not be configured to blink. It is a line that appears under the character at the current cursor position. The cursor is displayed using 5 dots in the 8th line.

**12.4.11 Underline Cursor Off**

<b>Command:</b> 0x11	<b>Default:</b> Yes	<b>Can be remembered:</b> Yes
----------------------	---------------------	-------------------------------

Turns the underline cursor off. It is displayed at the current cursor position. This is not the blink cursor, and can not be configured to blink. It is a line that appears under the character at the current cursor position.

**12.4.12 Display On**

<b>Command:</b> 0x1A	<b>Default:</b> Yes	<b>Can be remembered:</b> Yes
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Turns the entire display on. If the display is turned off, nothing is visible. This command does not change the contents of the *Display RAM*! It also does not change the value of the display contrast set by the “*Set display Contrast*” command.

**12.4.13 Display Off**

<b>Command:</b> 0x12	<b>Default:</b> No	<b>Can be remembered:</b> Yes
----------------------	--------------------	-------------------------------

Turns the entire display off. Nothing will be displayed on the display – it will be blank. This command does not change the contents of the *Display RAM*! When off, the display data remains in the *Display RAM*, and can be displayed instantly by issuing the “Display On” command.

**12.4.14 Set Display Contrast**

<b>Command:</b> 0x82, [value]	<b>Default:</b> Yes	<b>Can be remembered:</b> Yes
-------------------------------	---------------------	-------------------------------

Sets the current contrast level of the LCD display. The “value” parameter has to be a value from 0 to 254, where 0 is off and 254 will be maximum contrast.

**12.4.15 Move Cursor Right**

<b>Command:</b> 0x83	<b>Default:</b> -	<b>Can be remembered:</b> No
----------------------	-------------------	------------------------------

Moves the cursor one position forward (right). This command does not change the contents of the *Display RAM*! When shifting the cursor right along the first line, it will move to the second line when passing the 40<sup>th</sup> character of line 1. The next character written to the LCD will be at the new location of the cursor. Please note that it is possible shifting the cursor to a *Display RAM* address that is not currently visible!

**12.4.16 Move Cursor Left**

<b>Command:</b> 0x84	<b>Default:</b> -	<b>Can be remembered:</b> No
----------------------	-------------------	------------------------------

Moves the cursor one position backwards (left). This command does not change the contents of the *Display RAM*! When shifting

the cursor left along the first line, it will move to *Display RAM* address 83 when passing the first character of line 1. When shifting the cursor left along the second line, it will move to *Display RAM* address 40 when passing the first character of line 2. The next character written to the LCD will be at the new location of the cursor. Please note that it is possible shifting the cursor to a *Display RAM* address that is not currently visible!

### 12.4.17 Shift Display Right

<b>Command:</b> 0x85	<b>Default:</b> -	<b>Can be remembered:</b> No
----------------------	-------------------	------------------------------

Moves the contents of the display one position right. The last character of both rows will shift off the display, and be lost. The cursor does not move. The first characters of both rows will be filled with space characters. Doing a “Shift Display Left” after this command will NOT restore the display to it's original condition!

For example, if the display contains the text “This is a really cool LCD Display”, and this command is issued, the following will happen:

T	h	i	s		i	s		a		r	e	a	l	l	y
c	o	o	l		L	C	D		D	i	s	p	l	a	y

Figure 15: Before command

	T	h	i	s		i	s		a		r	e	a	l	l	
	c	o	o	l		L	C	D			D	i	s	p	l	a

Figure 16: After command

### 12.4.18 Shift Display Left

<b>Command:</b> 0x86	<b>Default:</b> -	<b>Can be remembered:</b> No
----------------------	-------------------	------------------------------

Moves the contents of the display one position left. The first character of both rows will shift off the display, and be lost. The cursor does not move. The last characters of both rows will be filled with space characters. Doing a “Shift Display Right” after this command will NOT restore the display to it's original condition!

For example, if the display contains the text “This is a really cool LCD Display”, and this command is issued, the following will happen:

T	h	i	s		i	s		a		r	e	a	l	l	y	
c	o	o	l		L	C	D			D	i	s	p	l	a	y

Figure 17: Before command

h	i	s		i	s		a		r	e	a	l	l	y		
o	o	l		L	C	D			D	i	s	p	l	a	y	

Figure 18: After command

### 12.4.19 Shift Display Up

<b>Command:</b> 0x87	<b>Default:</b> -	<b>Can be remembered:</b> No
----------------------	-------------------	------------------------------

Copied the contents of row 2 to row 1, and fills row 2 with space characters. The contents of row 1 is lost after this command, and issuing a “Shift Display Down” command after this command will NOT restore the display to it's original condition! The cursor does not move. For example, if the display contains the text “This is a really cool LCD Display”, and this command is issued, the following will happen:

T	h	i	s		i	s		a		r	e	a	l	l	y
c	o	o	l		L	C	D		D	i	s	p	l	a	y

Figure 19: Before command

c	o	o	l		L	C	D		D	i	s	p	l	a	y
---	---	---	---	--	---	---	---	--	---	---	---	---	---	---	---

Figure 20: After command

### 12.4.20 Shift Display Down

<b>Command:</b> 0x88	<b>Default:</b> -	<b>Can be remembered:</b> No
----------------------	-------------------	------------------------------

Copied the contents of row 1 to row 2, and fills row 1 with space characters. The contents of row 2 is lost after this command, and issuing a “Shift Display Up” command after this command will NOT restore the display to it's original condition! The cursor does not move. For example, if the display contains the text “This is a really cool LCD Display”, and this command is issued, the following will happen:

T	h	i	s		i	s		a		r	e	a	l	l	y
c	o	o	l		L	C	D		D	i	s	p	l	a	y

Figure 21: Before command

T	h	i	s		i	s		a		r	e	a	l	l	y

Figure 22: After command

### 12.4.21 Set Cursor Address

<b>Command:</b> 0x89, [cursor position]	<b>Default:</b> -	<b>Can be remembered:</b> No
---	-------------------	------------------------------

Sets the cursor location to the given *Display RAM* address. If no display shift commands have been issues:

- The first character of line 1 will be at *Display RAM* address 0
- The last character of line 1 will be at *Display RAM* address 15
- The first character of line 2 will be at *Display RAM* address 64
- The last character of line 2 will be at *Display RAM* address 83

If display shift commands have been issued, the user will have to keep track of what *Display RAM* locations are currently visible!

### 12.4.22 Set Cursor Position

<b>Command:</b> 0x8A, [row], [column]	<b>Default:</b> -	<b>Can be remembered:</b> No
---------------------------------------	-------------------	------------------------------

Moves the cursor to the given row and column location. This command will compensate for any display shifts that have been done!

- Row 1, column 1 will always be the first character of row 1
- Row 1, column 16 will always be the last character of row 1
- Row 2, column 1 will always be the first character of row 2
- Row 2, column 16 will always be the last character of row 2

### 12.4.23 Cursor Home

<b>Command:</b> 0x8B	<b>Default:</b> -	<b>Can be remembered:</b> No
----------------------	-------------------	------------------------------

This command sets the cursor position to the first character of row 1.

### 12.4.24 Clear Display

<b>Command:</b> 0x8C	<b>Default:</b> -	<b>Can be remembered:</b> No
----------------------	-------------------	------------------------------

This command sets the cursor position to the first character of row 1 and writes space characters (0x20) to all Display RAM locations.

## 12.5 Large Number Font Commands

This command is **only relevant when the display has 4x20** lines. When using a 4x20 line display, the LCD2S can be configured for displaying large number fonts. In this mode, each number is 3 characters high and 2 characters wide. This allows a total of 7 numbers to be displayed next to each other, with an empty column between each number.

### 12.5.1 Special Character Set

<b>Command:</b> 0x8E, [character set]	<b>Default:</b> -	<b>Can be remembered:</b> No
---------------------------------------	-------------------	------------------------------

This command is used to configure the large character set to use. The following large character sets are defined:

- 0: Large 3x2 Numbers. Defines character set for displaying numbers that are 3 character high, and 2 character wide.

### 12.5.2 Write Large Number String to LCD

<b>Command:</b> 0x8F, "String"	<b>Default:</b> -	<b>Can be remembered:</b> No
--------------------------------	-------------------	------------------------------

This command is **only relevant when the display has 4x20** lines. Writes the given large number (3 characters high, 2 characters wide) string to the LCD. Only the following characters are accepted:

**0, 1, 2, 3, 4, 5, 6, 7, 8, 9, ., :**

The string is parsed for the following escape sequence characters that perform the following actions:

- 0x09 ('\t') = Cursor Right
- 0x0c ('\f') = Clear display and go to beginning of first line.
- 0x0d ('\r') = Go to beginning of first line

The escape sequence addresses don't store any characters in the Character Set, so all possible characters can still be displayed.

For example, to write "12:10:00" in the top 3 lines of the display, the following string can be written to the LCD2S:

- "\f12:10:00"

## 12.6 Keypad Commands

### 12.6.1 Set Keypad Debounce Time

<b>Command:</b> 0xE1, [value]	<b>Default:</b> 3 = 24ms	<b>Can be remembered:</b> Yes
-------------------------------	--------------------------	-------------------------------

Sets the keypad debounce time. This is the software filter time used for the keypad encoding, and has to be a value between 1 and 15. The debounce time is calculated as follow:

Debounce Time = value x 8ms

For example, a value of 1 sets the debounce time to 8ms, a value of 2 sets it to 16ms, ....., a value of 15 sets it to 120ms.

The default of 24ms (a value of 3) should be fine for most keypads. If a key press is registered twice by the LCD2S when a button is pressed, then the debounce time should be increased.

### 12.6.2 Read Keypad Data

<b>Command I<sup>2</sup>C:</b> 0xD1, <read key> <b>Command SPI:</b> 0xD1, 0x00, <read key>	<b>Default:</b> -	<b>Can be remembered:</b> No
---	-------------------	------------------------------

Reads the next byte from the keypad buffer. When executing this command via the SPI port, a dummy byte must be sent after the 0xD1 command! If there is no key data in the keypad buffer, 0 is returned. If there is key data in the keypad buffer, a key code from 'a' to 'f' is returned, depending on what key was pressed. For details on what keys return what values, see Chapter 7. "Keypad and I/O Configuration".

The LCD2S has a 16 byte keypad buffer. Each time a key is pressed, it's code is added to the keypad buffer.

## 12.7 Input/Output Commands

### 12.7.1 Set OUT1 and OUT2

<b>Command:</b> 0xE2, [value]	<b>Default:</b> -	<b>Can be remembered:</b> Yes
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Sets the value of OUT1 and OUT2 general purpose outputs. Bit 0 of the given value sets the state of OUT1. Bit 1 of the given value sets the state of OUT2. For example:

- 0 (0x00) will turn OUT1 and OUT2 off
- 1 (0x01) will turn OUT1 on, and OUT2 off
- 2 (0x02) will turn OUT1 off, and OUT2 on
- 3 (0x03) will turn OUT1 and OUT2 on

### 12.7.2 OUT1 On

<b>Command:</b> 0x38	<b>Default:</b> No	<b>Can be remembered:</b> Yes
----------------------	--------------------	-------------------------------

Turns OUT1 output on. For this command to work, the LCD2S has to be configured to use the OUT1 general purpose output. For details on configuration, see Chapter 7. "Keypad and I/O Configuration".

### 12.7.3 OUT1 Off

<b>Command:</b> 0x30	<b>Default:</b> Yes	<b>Can be remembered:</b> Yes
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Turns OUT1 output off. For this command to work, the LCD2S has to be configured to use the OUT1 general purpose output. For details on configuration, see Chapter 7. "Keypad and I/O Configuration".

### 12.7.4 OUT2 On

<b>Command:</b> 0x39	<b>Default:</b> No	<b>Can be remembered:</b> Yes
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Turns OUT2 output on. For this command to work, the LCD2S has to be configured to use the OUT2 general purpose output. For details on configuration, see Chapter 7. "Keypad and I/O Configuration".

### 12.7.5 OUT2 Off

<b>Command:</b> 0x31	<b>Default:</b> Yes	<b>Can be remembered:</b> Yes
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Turns OUT2 output off. For this command to work, the LCD2S has to be configured to use the OUT2 general purpose output. For details on configuration, see Chapter 7. "Keypad and I/O Configuration".

### 12.7.6 GPIO1 On

<b>Command:</b> 0x48	<b>Default:</b> No	<b>Can be remembered:</b> Yes
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Turns GPIO1 output on. For this command to work, GPIO1 has to be configured as a digital output:

- GPIO1 must be enabled via the "Configure Keypad and IO" command.
- GPIO1 must be configured as a digital output with the "Configure GPIO1" command.

### 12.7.7 GPIO1 Off

<b>Command:</b> 0x40	<b>Default:</b> No	<b>Can be remembered:</b> Yes
----------------------	--------------------	-------------------------------



Turns GPIO1 output off. For this command to work, GPIO1 has to be configured as a digital output:

- GPIO1 must be enabled via the “*Configure Keypad and IO*” command.
- GPIO1 must be configured as a digital output with the “*Configure GPIO1*” command.

### 12.7.8 GPIO2 On

<b>Command:</b> 0x49	<b>Default:</b> No	<b>Can be remembered:</b> Yes
----------------------	--------------------	-------------------------------

Turns GPIO2 output on. For this command to work, GPIO2 has to be configured as a digital output:

- GPIO2 must be enabled via the “*Configure Keypad and IO*” command.
- GPIO2 must be configured as a digital output with the “*Configure GPIO2*” command.

### 12.7.9 GPIO2 Off

<b>Command:</b> 0x41	<b>Default:</b> No	<b>Can be remembered:</b> Yes
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Turns GPIO2 output off. For this command to work, GPIO2 has to be configured as a digital output:

- GPIO2 must be enabled via the “*Configure Keypad and IO*” command.
- GPIO2 must be configured as a digital output with the “*Configure GPIO2*” command.

## 13 Character set

The Character Set of the LCD2S board depends on the LCD Character Module used with it, but is normally the English/European character set.

Upper 4 bit Lower 4 bit	LLLL	LLLH	LLHL	LLHH	LHLL	LHLH	LHHL	LHHH	HLLL	HLLH	HLHL	HLHH	HHLL	HHLH	HHHL	HHHH
LLLL	CG RAM ( 1 )	!	"	#	\$	%	&	'	(	)	*	+	,	-	.	:
LLLH	CG RAM ( 2 )	;	<	=	>	?@	AB	CD	EF	GH	IJ	KL	MN	OP	QR	ST
LLHL	CG RAM ( 3 )	UV	WX	YZ	[	\	^	_	`	a	b	c	d	e	f	g
LLHH	CG RAM ( 4 )	h	i	j	k	l	m	n	o	p	q	r	s	t	u	v
LHLL	CG RAM ( 5 )	w	x	y	z	{		}	~	¡	¢	£	¤	¥	¦	§
LHLH	CG RAM ( 6 )	¨	©	ª	«	¬	®	¯	°	±	²	³	´	µ	¶	·
LHHL	CG RAM ( 7 )	¸	¹	º	»	¼	½	¾	¿	À	Á	Â	Ã	Ä	Å	Æ
LHHH	CG RAM ( 8 )	Ç	Ð	Ñ	Ò	Ó	Ô	Õ	Ö	×	Ø	Ù	Ú	Û	Ü	Ý
HLLL	CG RAM ( 1 )	Þ	ß	à	á	â	ã	ä	å	æ	ç	ð	ñ	ò	ó	ô
HLLH	CG RAM ( 2 )	õ	ö	÷	ø	ù	ú	û	ü	ý	þ	ß	à	á	â	ã
HLHL	CG RAM ( 3 )	ä	å	æ	ç	ð	ñ	ò	ó	ô	õ	ö	÷	ø	ù	ú
HLHH	CG RAM ( 4 )	û	ü	ý	þ	ß	à	á	â	ã	ä	å	æ	ç	ð	ñ
HHLL	CG RAM ( 5 )	ò	ó	ô	õ	ö	÷	ø	ù	ú	û	ü	ý	þ	ß	à
HHLH	CG RAM ( 6 )	á	â	ã	ä	å	æ	ç	ð	ñ	ò	ó	ô	õ	ö	÷
HHHL	CG RAM ( 7 )	ä	å	æ	ç	ð	ñ	ò	ó	ô	õ	ö	÷	ø	ù	ú
HHHH	CG RAM ( 8 )	ü	ý	þ	ß	à	á	â	ã	ä	å	æ	ç	ð	ñ	ò

Figure 23, English/European Character Set

## 14 Specifications

### 14.1 Absolute Maximum Ratings

The operating and storage temperature depends on the LCD display used. The LCD2S without and display

<i>Item</i>	<i>Symbol</i>	<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
Operating Temperature of LCD2S without display	Topnd	-20		+85	°C
Storage Temperature of LCD2S without display	Tstnd	-55		150	°C

The standard operating temperature for an LCD display is 0°C to 50°C. The operating temperature for an wide operating temperature LCD display is -20°C to 70°C.

### 14.2 Electrical Characteristics

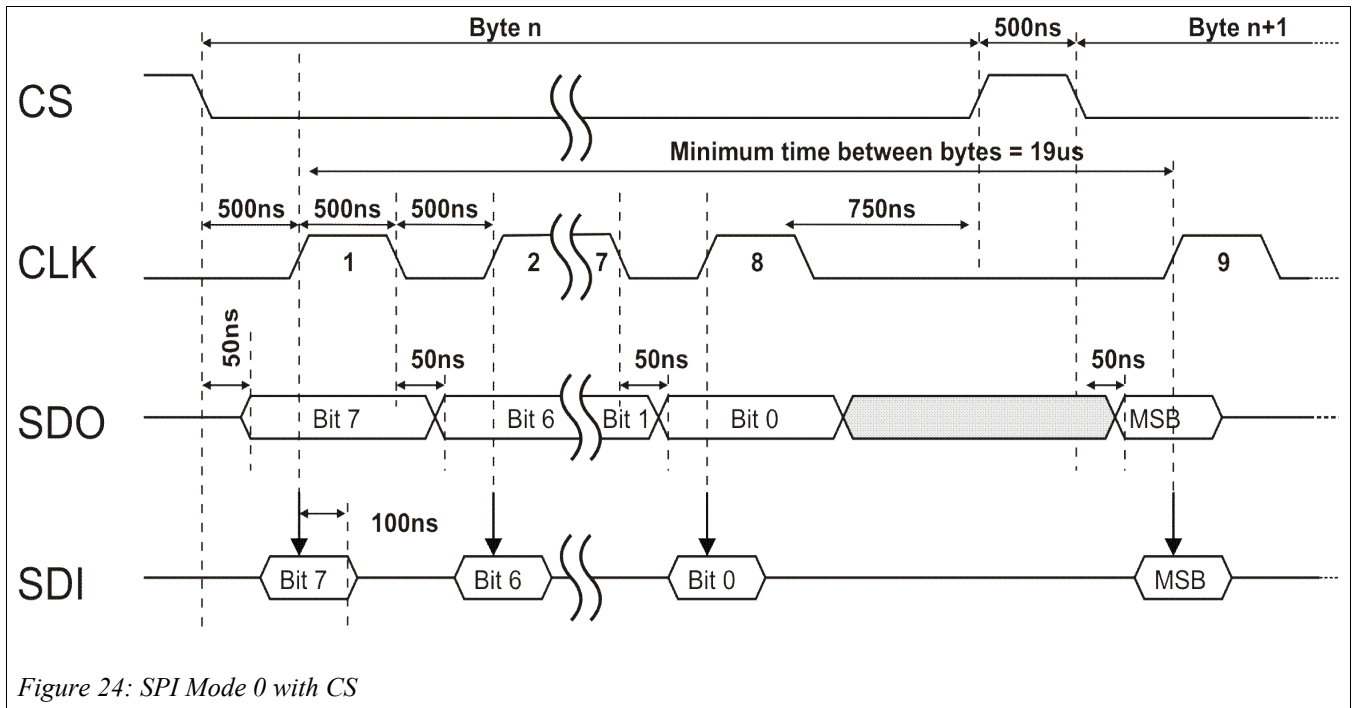
The following readings were done with a 2x16 line character LCD display, with an array LED backlight.

<i>Item</i>	<i>Symbol</i>	<i>Condition</i>	<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
DC Supply Voltage:	Vdd	-	4.5		5.5	V
Typical Operating Current with backlight off	Idd	Vdd = 5V		4		mA
Typical Operating Current with backlight at 50%	Idd	Vdd = 5V		40		mA
Typical Operating Current with backlight at 100%	Idd	Vdd = 5V		76		mA

### 14.3 D.C. Characteristics of user I/O pins

<i>Item</i>	<i>Symbol</i>	<i>Condition</i>	<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
OUT1 and OUT2 outputs	Iout		0		1000mA	mA
GPIO1 to GPIO3 Input Low Voltage	VIL		0		1	V
GPIO1 to GPIO3 Input High Voltage	VIH		2.05		5	V
GPIO1 and GPIO2 Output High Voltage	VOL				0.6	V
GPIO1 and GPIO2 Output Low Voltage	VOH		4.3			V
GPIO1 and GPIO2 Capacitive loading	CIO			50		pF

## 14.4 SPI



## 15 Dimensions

The LCD2S dimensions as seen from the side are shown below.

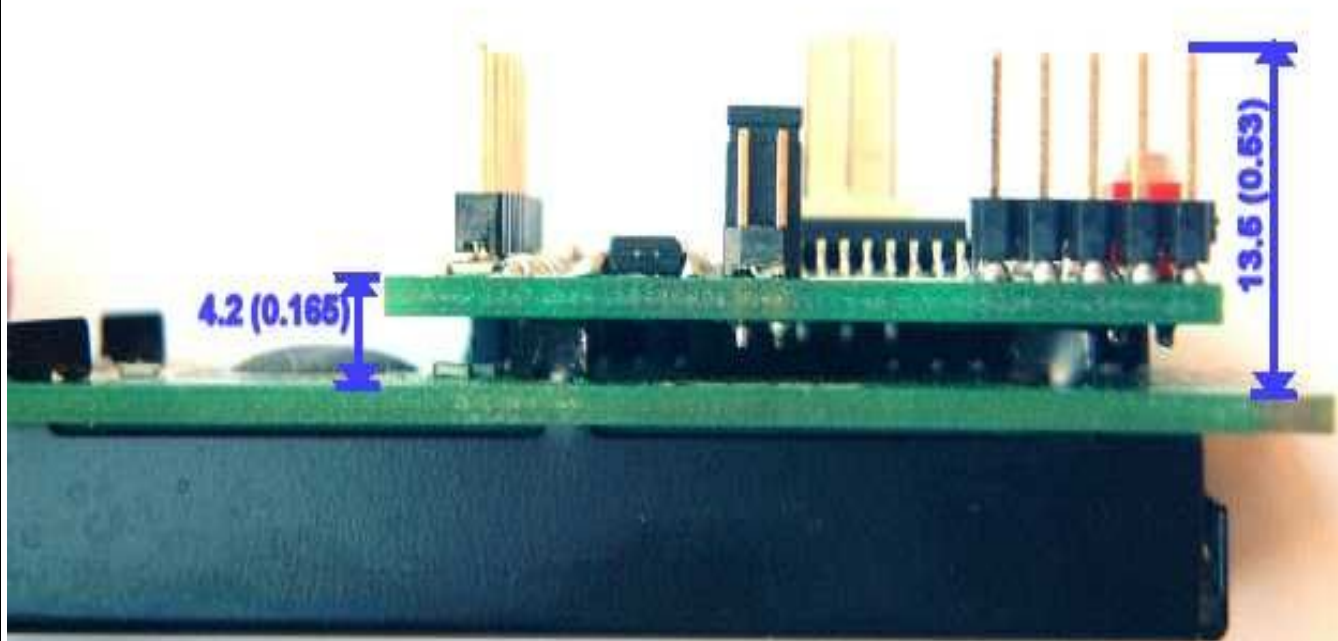


Figure 25