

2.5 Pinout Number Order

NO	NAME	SUPPLIES	FUNCTIONAL	TYPE	I/O	DESCRIPTION
			BLOCK			
9	VCCRTC	VCCRTC /AGND	RTC	Power	I	RTC power supply
65	OSC32KIN	VCCRTC /DGND		Analog	I	32KHz crystal oscillator input
66	OSC32KOUT	VCCRTC /DGND		Analog	O	32KHz crystal oscillator output
68	CLK32KOUT1	VCCRTC /DGND		Digital	O	32KHz clock output 1,OD output (always on)
67	CLK32KOUT2	VCCRTC /DGND		Digital	O	32KHz clock output 2,OD output
37	VREF	VCCA /REFGND	REFERENCE	Analog	O	bandgap voltage
64	VREFGND	REFGND		Analog	Gnd	reference ground
36	VCCA	VCCA /GNDA	Analog Power	Power	I	power supply for
6	VPPOTP	VPPOTP /GNDA	Analog Power	Power	I	OTP power supply
45	VCC1	VCC1 /GND1	BUCK1	Power	I	buck1 dc-dc power supply
44	VCC1	VCC1 /GND1		Power	I	buck1 dc-dc power supply
43	SW1	VCC1 /GND1		Power	I/O	buck1 dc-dc switch output
42	SW1	VCC1 /GND1				
41	GND1	VCC1 /GND1		Power	Gnd	buck1 dc-dc switch ground
40	GND1	VCC1 /GND1				
39	VFB1	VCC1 /REFGND		Analog	I	buck1 dc-dc switch feedback voltage
23	VCC2	VCC2 /GND2	BUCK2	Power	I	buck2 dc-dc power supply
24	VCC2	VCC2 /GND2		Power	I	buck2 dc-dc power supply
25	SW2	VCC2 /GND2		Power	I/O	buck2 dc-dc switch output
26	SW2	VCC2 /GND2		Power	I/O	buck2 dc-dc switch output

NO	NAME	SUPPLIES	FUNCTIONAL	TYPE	I/O	DESCRIPTION
			BLOCK			
27	GND2	VCC2 /GND2		Power	Gnd	buck2 dc-dc switch ground
28	GND2	VCC2 /GND2		Power	Gnd	buck2 dc-dc switch ground
29	VFB2	VCC2 /REFGND		Analog	I	buck2 dc-dc switch feedback voltage
59	VCC3	VCC3 /GND3	BUCK3	Power	I	buck3 dc-dc power supply
58	SW3	VCC3 /GND3		Power	I/O	buck3 dc-dc switch output
57	GND3	VCC3 /GND3		Power	Gnd	buck3 dc-dc switch ground
56	VFB3	VCC3 /REFGND		Analog	I	buck3 dc-dc switch feedback voltage
60	VCC4	VCC4 /GND4	BUCK4	Power	I	buck4 dc-dc power supply
61	SW4	VCC4 /GND4		Power	I/O	buck4 dc-dc switch output
62	GND4	VCC4 /GND4		Power	Gnd	buck4 dc-dc switch ground
63	VFB4	VCC4 /REFGND		Analog	I	buck4 dc-dc switch feedback voltage
47	NC					
46	GND5	VCCA /GND5		Power	Gnd	ground
48	NC					
32	VCC6	VCC6 /AGND	LDO 1~8, SWITCH1,2	Power	I	LDO1,LDO2 power supply
4	VCC7	VCC7 /AGND		Power	I	LDO3,LDO7 power supply
8	VCC8	VCC8 /AGND		Power	I	SWITCH1 power supply
13	VCC9	VCC9 /AGND		Power	I	LDO4,LDO5 power supply
1	VCC10	VCC11 /AGND		Power	I	LDO6 power supply
16	VCC11	VCC11 /AGND		Power	I	LDO8 power supply
10	VCC12	VCC12 /AGND		Power	I	SWITCH2 power supply
31	VLDO1	VCC7 /AGND		Power	O	LDO1 regulator output
33	VLDO2	VCC7		Power	O	LDO2 regulator output

NO	NAME	SUPPLIES	FUNCTIONAL	TYPE	I/O	DESCRIPTION
			BLOCK			
		/AGND				
3	VLDO3	VCC8		Power	O	LDO3 regulator output
		/AGND				
12	VLDO4	VCC9		Power	O	LDO4 regulator output
		/AGND				
14	VLDO5	VCC10		Power	O	LDO5 regulator output
		/AGND				
2	VLDO6	VCC9		Power	O	LDO6 regulator output
		/AGND				
5	VLDO7	VCC1		Power	O	LDO7 regulator output
		1/AGND				
15	VLDO8	VCC11		Power	O	LDO8 regulator output
		/AGND				
7	VSWOUT1	VCC8		Power	O	Switch 1 output
		/AGND				
11	VSWOUT2	VCC12		Power	O	Switch 2 output
		/AGND				
30	AGND	POWER PAD	Analog ground	Power	Gnd	Analog ground
35	VLDOA	POWER PAD	LDOA	Power	I	supply for internal analog circuit
38	DGND	POWER PAD	Digital ground	Power	Gnd	Digital ground
17	VDDIO	VDDIO	IO	Power	I	Digital I/O power supply
		/DGND				
50	SLEEP	VDDIO		Digital	I	Active-Sleep state transition control signal
		/DGND				
20	NRESPWRON	VDDIO		Digital	O	Power off reset for AP/ External reset digital core(excludes RTC)
		/DGND				
49	INT	VDDIO	IO	Digital	O	Interrupt flag (polarity is I2C programmable, default active high)
		/DGND				
51	PWRON	VCCRTC		Digital	I	External switch-on control signal(ON button)
		/DGND				
18	SDA	VDDIO		Digital	I/O	I2C data signal
		/DGND				
19	SCL	VDDIO		Digital	I/O	I2C clock signal
		/DGND				
52	BOOT0	VCCRTC /DGND	IO	Digital	I	Power-up sequence selection
53	BOOT1	VCCRTC /DGND		Digital	I	Power-up sequence selection

NO	NAME	SUPPLIES	FUNCTIONAL	TYPE	I/O	DESCRIPTION
			BLOCK			
55	EXT_EN	VCCRTC /DGND		Digital	O	Output enable for external BUCK in two-battery-cells application
22	DVS1	VDDIO		Digital	I	BUCK1 DVS voltage /normal voltage transition control signal(polarity is I2C programmable, default active high)
		/DGND				
21	DVS2	VDDIO		Digital	I	BUCK2 DVS voltage /normal voltage transition control signal(polarity is I2C programmable, default active high)
		/DGND				
54	DVSOK	VDDIO		Digital	O	BUCK1 and BUCK2 power good flag after dynamic voltage setting
		/DGND				
34	VDC	VDC		Digital	I	Adapter voltage detect input
		/AGND				