

Jetson Orin SOM Custom Carrier Boards V1.0

Design By: Mohamed Maher
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www.infinitytech.ltd

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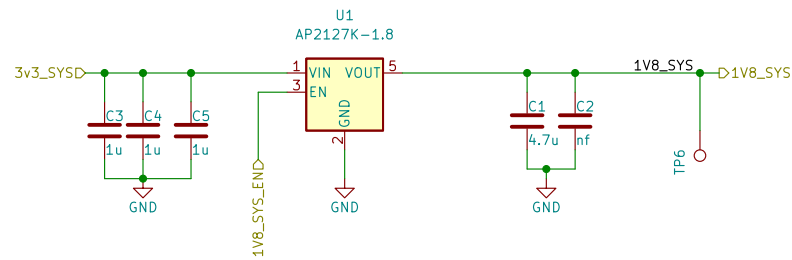
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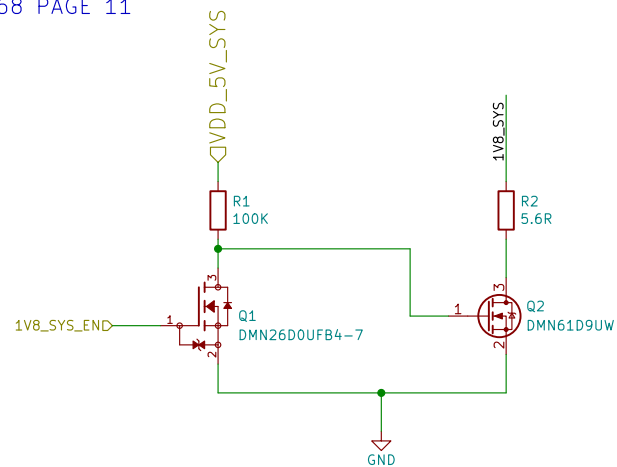
KiCad E.D.A. eeschema (6.0.0)

Rev:

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1V8 Discharge Circuit
- P3768 PAGE 11



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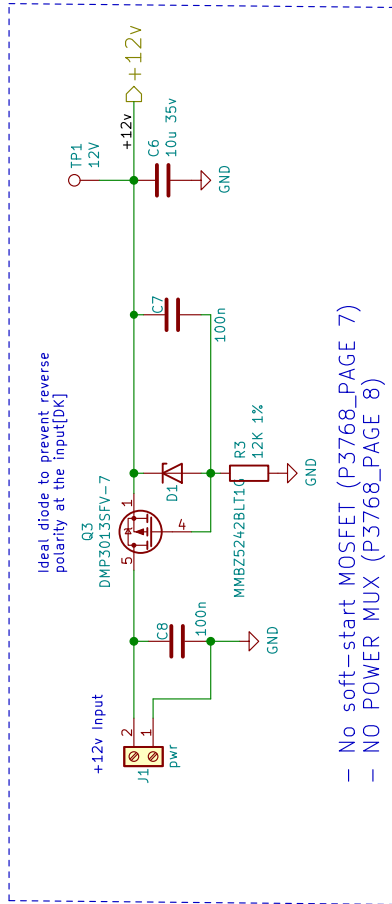
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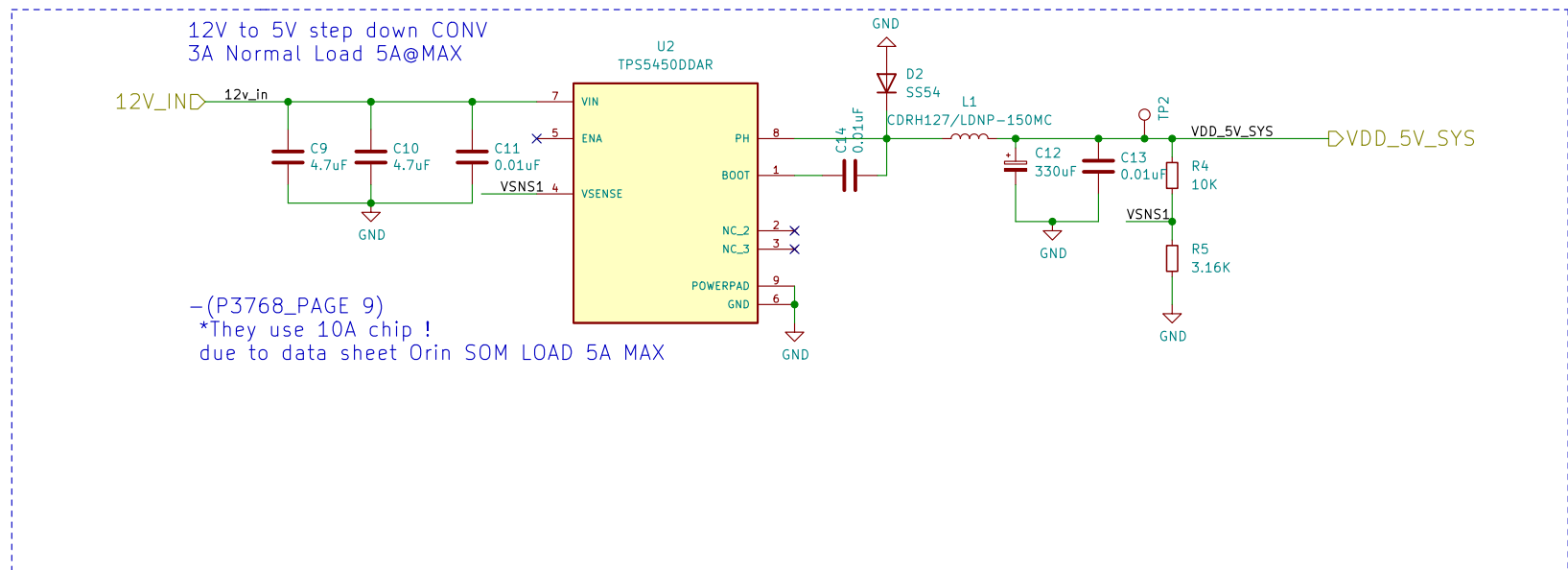
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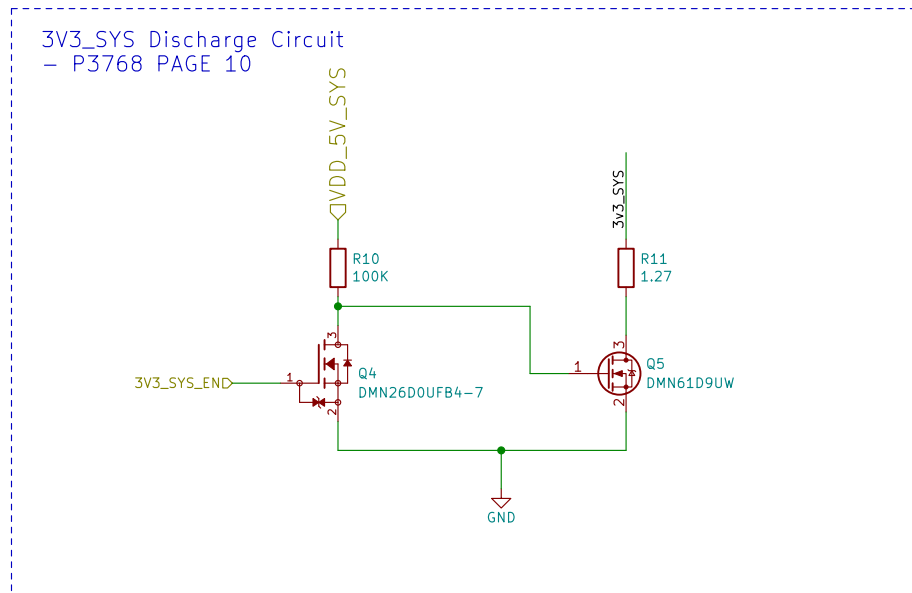
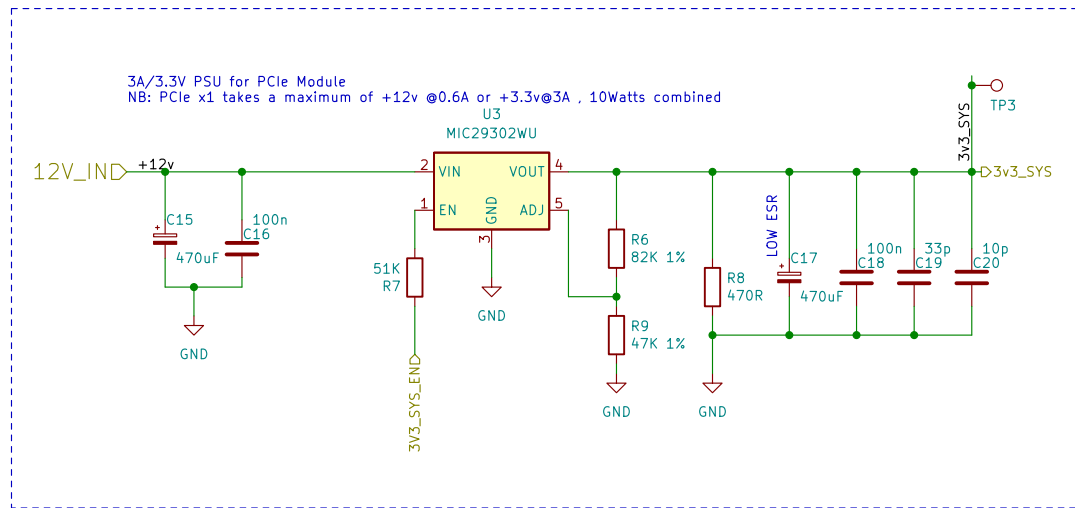
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Date: 2023-05-28

Rev:

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Sheet: /PSUs/3V3_SYS_DCDC/
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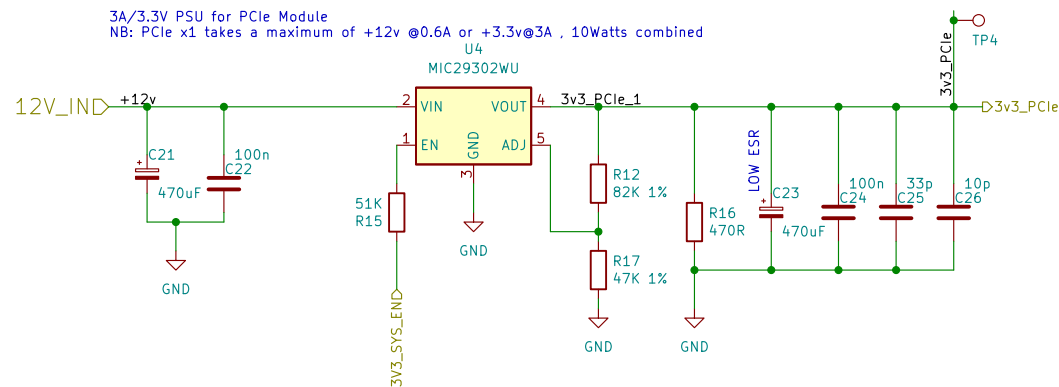
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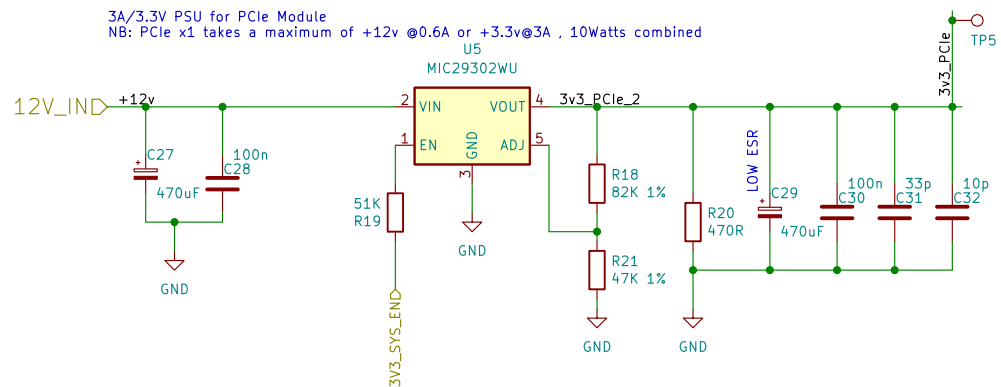
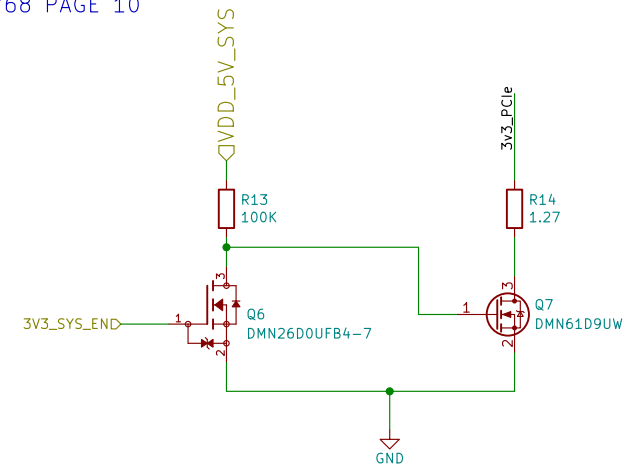
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3V3_SYS Discharge Circuit - P3768 PAGE 10



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Sheet: /PSUs/3V3_Pcle_DCDC/
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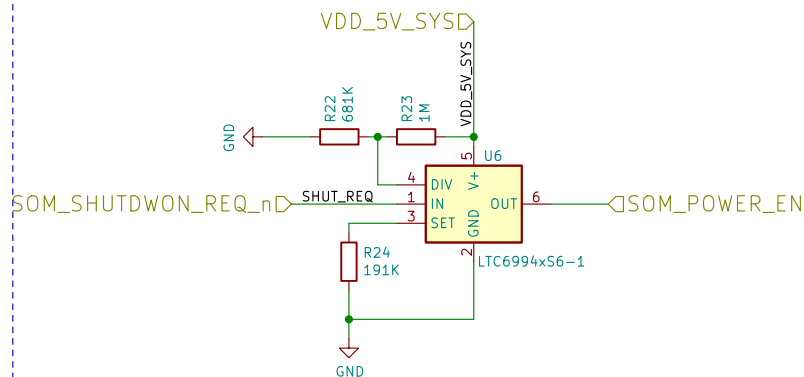
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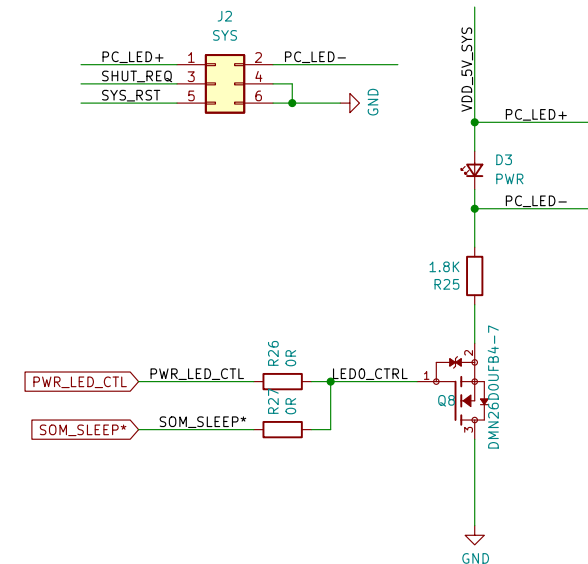
1SEC Delay Circuit for Power_EN signal
 Datasheet recommed 400mS Min Delay
 In case of Shutdown Rqt, Pin driven Low
 to start power down sequence and power Rails Discharge



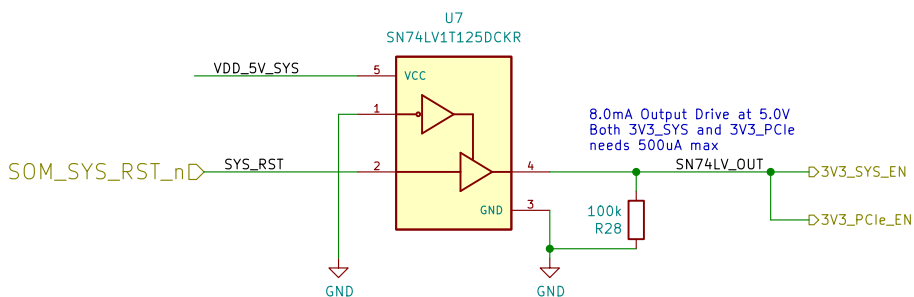
Notes:

1- LTC6994 DelayBox Calculator:
<http://beta-tools.analog.com/timerblox/LTC6994>
 1-Sec Delay, Rising Edge Only

2- 3.3V & 1.8V Power Rails are controlled by SOM_SYS_RSTn



PWR LED CONTROL
 P3768_A04 PAGE 4



8.0mA Output Drive at 5.0V
 Both 3V3_SYS and 3V3_PClE
 needs 500uA max

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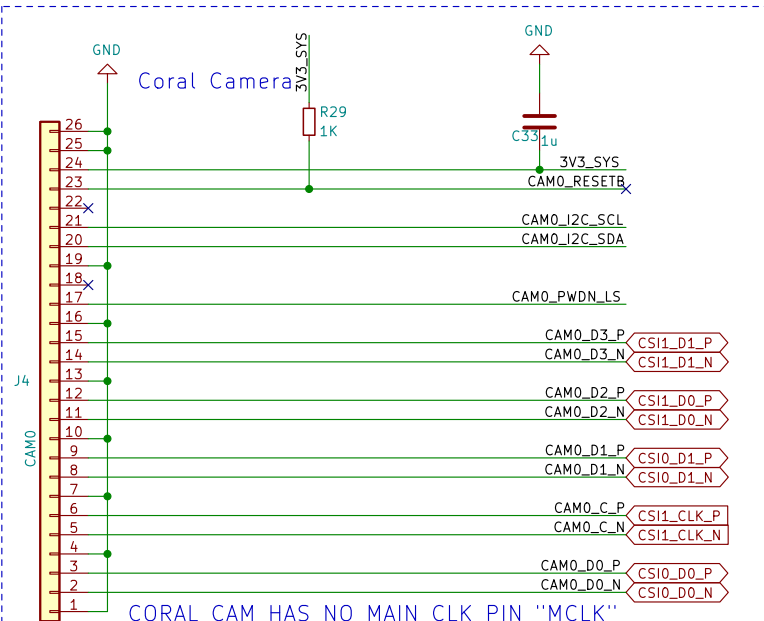
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Sheet: /PSUs/P_Seq_M/
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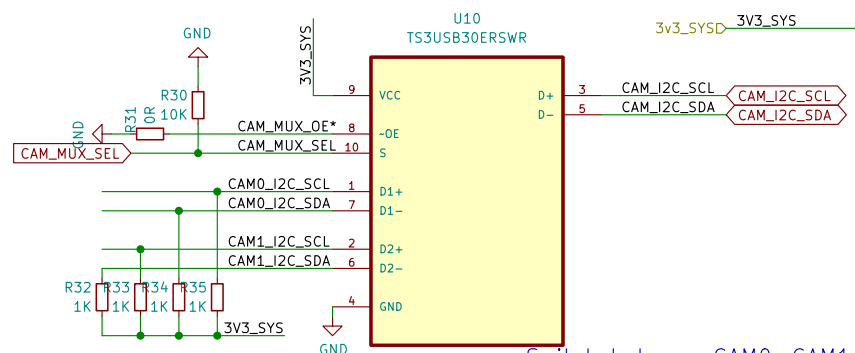
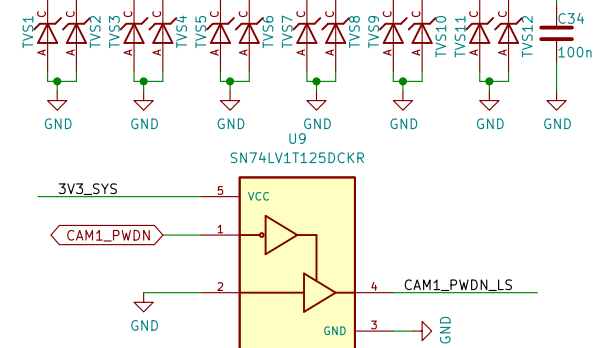
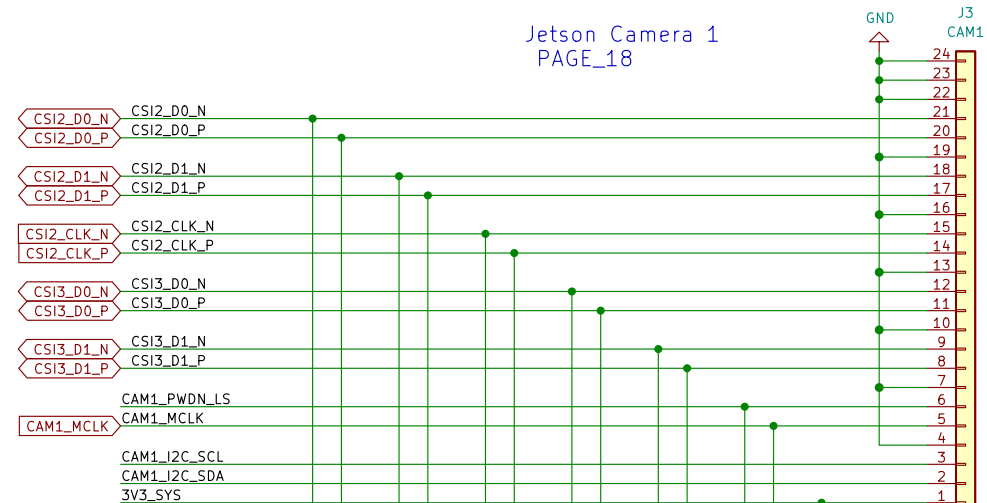
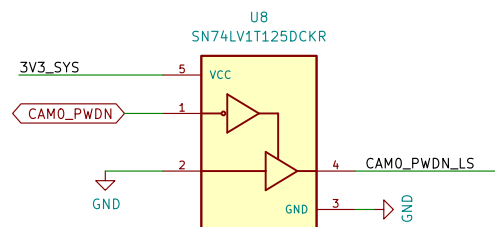
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CORAL CAM HAS NO MAIN CLK PIN "MCLK"
Jetson SOM Does not support CAM RESET PIN



Switch between CAM0, CAM1
PAGE_18

Sheet: /Cameras/
File: Cameras.kicad_sch

Title:

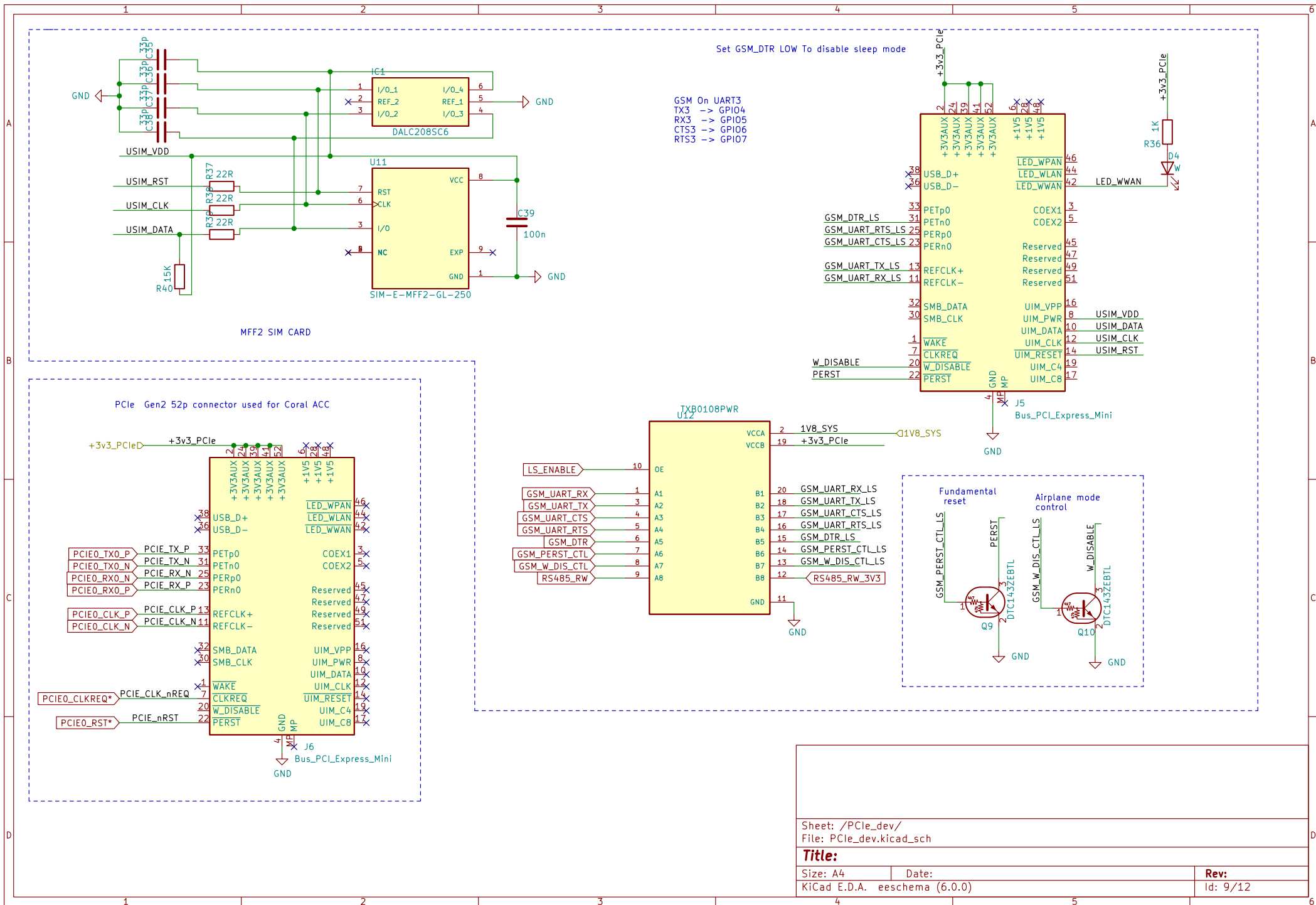
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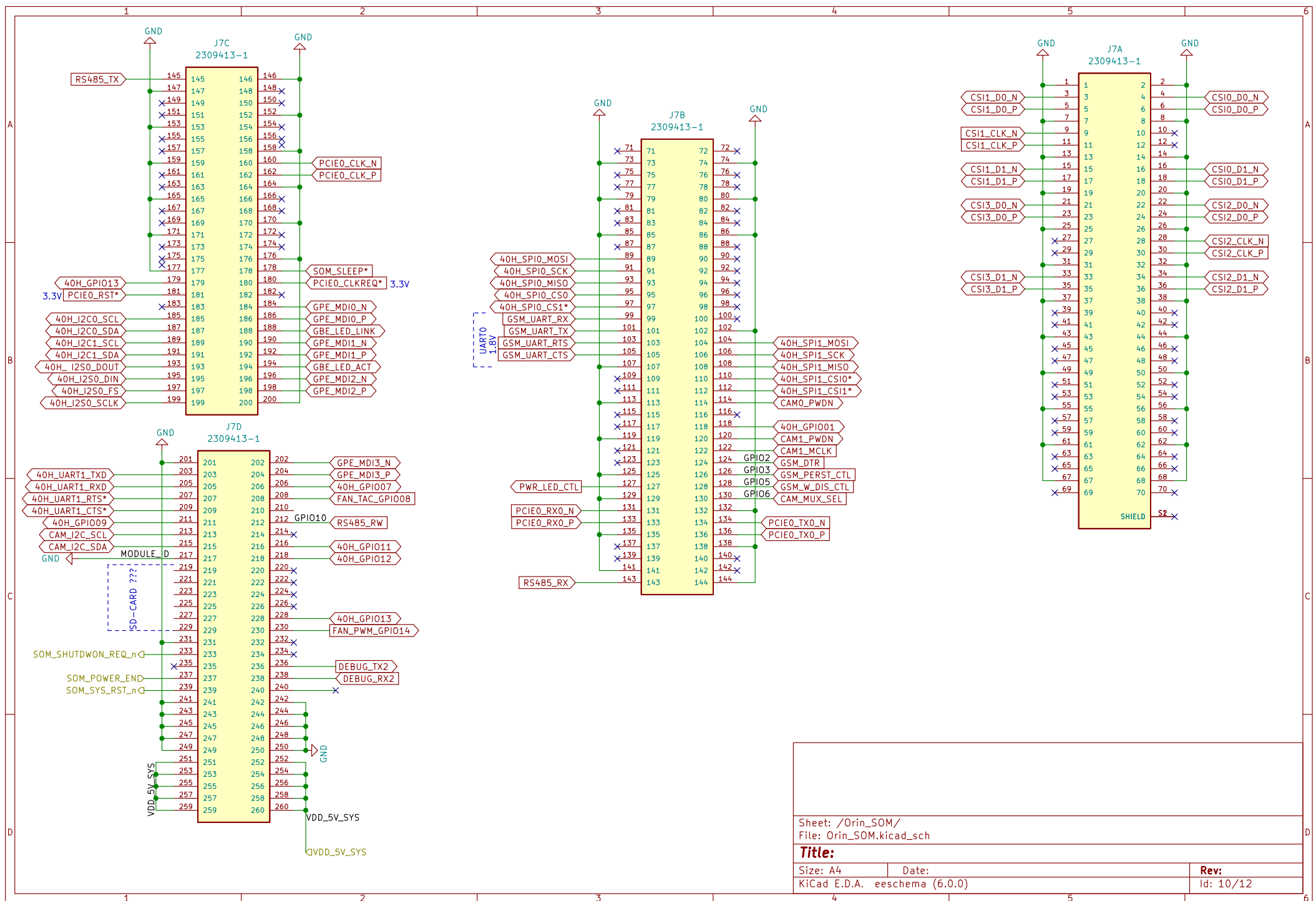
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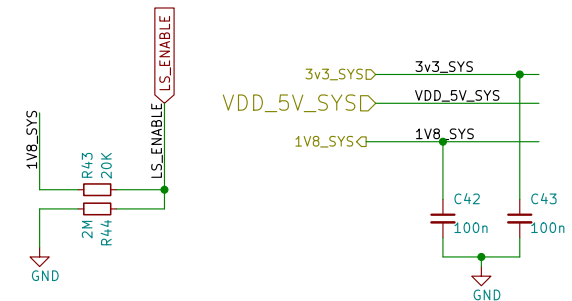
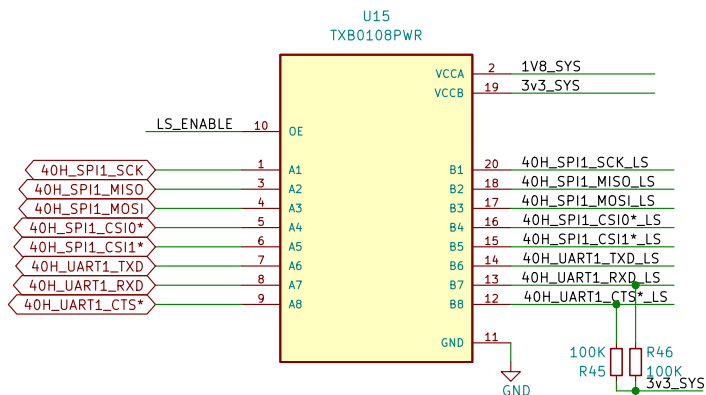
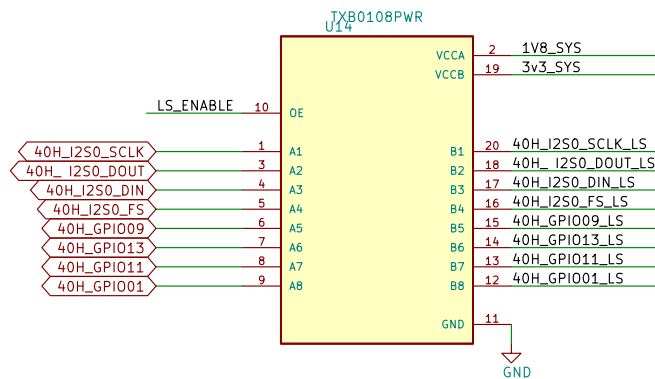
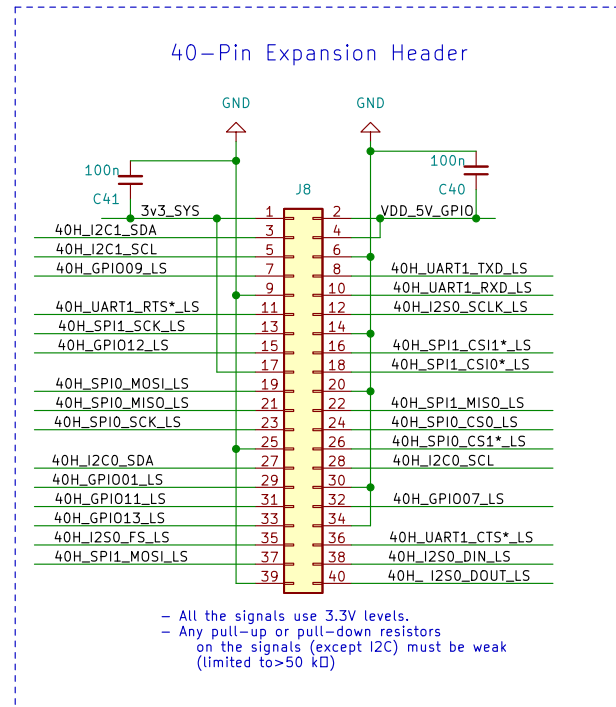
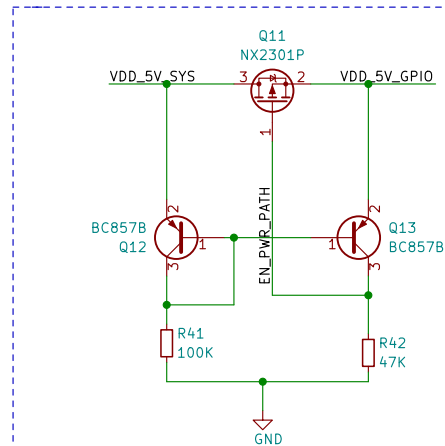
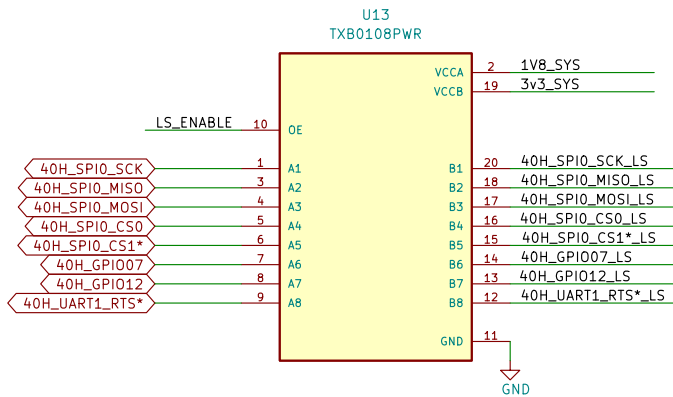
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Size: A4

Date:

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