

# Freescalé MQX RTOS Example Guide

## FlexBusMRAM example

This document describes one of the most basic example applications for FlexBusMRAM. The example FlexBusMRAM handles only one task which validates the access to external memory.

## Running the example

Start a terminal application on your PC and set the serial connection for 115200 baudrate, 8 data bits, 1 stop bit, no parity and no flow control.

Start FlexBusMRAM example on the target platform. Then you will see the printed message as the following.

### Note:

- J11 and J16 of the TWR-MEM should be unconnected, because the CLPD will affect the MRAM signal.
- Before building MQX libraries it's necessary to enable `BSPCFG_ENABLE_FLEXBUS_MRAM` and disable `BSPCFG_ENABLE_SDRAMC` in `user_config.h` file:
  - o `#define BSPCFG_ENABLE_FLEXBUS_MRAM 1`
  - o `#define BSPCFG_ENABLE_SDRAMC 0`

### MQX FlexBusMRAM DEMO EXAMPLE.

```
Block validated:      0
Block validated:      1
Block validated:      2
Block validated:      3
Block validated:      4
Block validated:      5
...
```

```
Block validated:  511
```

### *MRAM Validation Report*

```
Total block count: 512
Right block count: 512
Error block count: 0
```

The "MRAM Validation Report" will report the result of test.