2   Hardware

This section describes the F5529 LaunchPad hardware.

Figure 7 shows the LaunchPad, with its important features and configuration controls. These controls are described in this section.
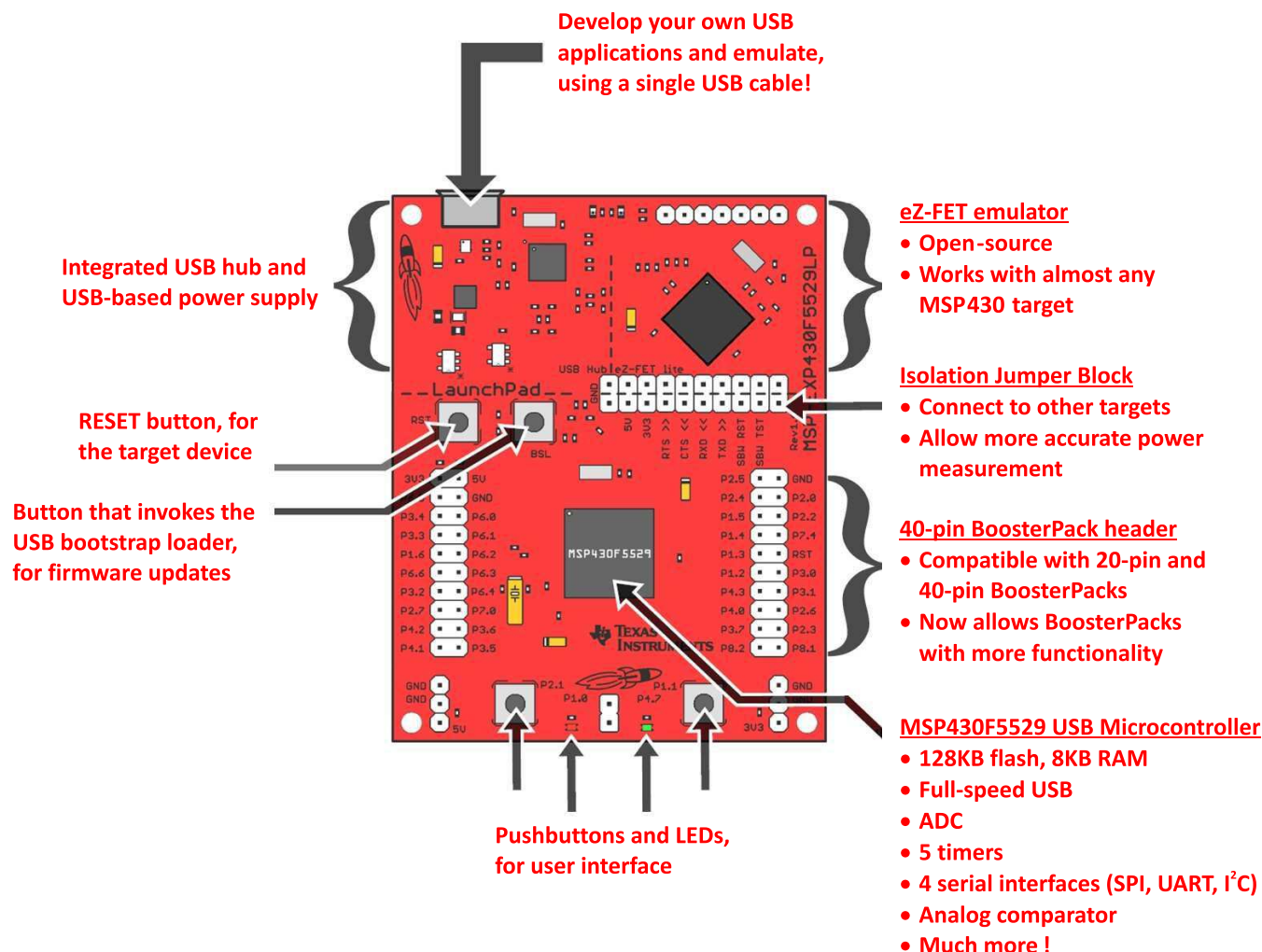

Figure 7. EVM Features and Controls

2.1 Block Diagram
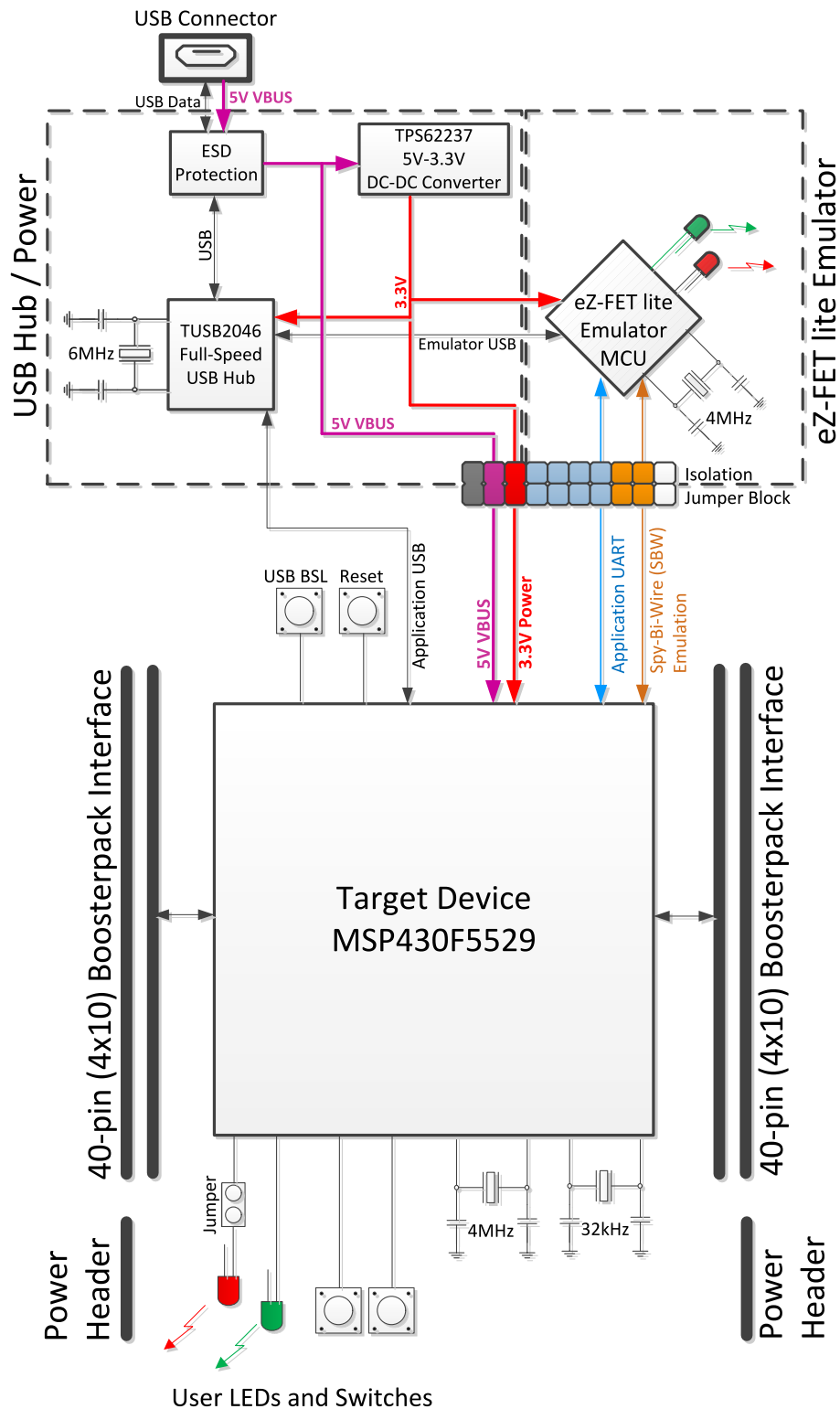
Figure 8 shows a functional block diagram of the board.

Figure 8. Block Diagram

2.2 Hardware Features

2.2.1   MSP430F5529

The MSP430F552x is one of several USB-equipped MSP430 derivative families. It offers:

- 1.8-V to 3.6-V operation
- Up to 25-MHz system clock
- 128KB flash memory, 8KB RAM (in addition to 2KB shared RAM with the USB module)
- Ultralow-power operation
- Full-speed USB with 14 endpoints – enough for almost any USB application
- Five timers, up to four serial interfaces (SPI, UART, or I^{2}C), 12-bit analog-to-digital converter, analog comparator, hardware multiplier, DMA, and more
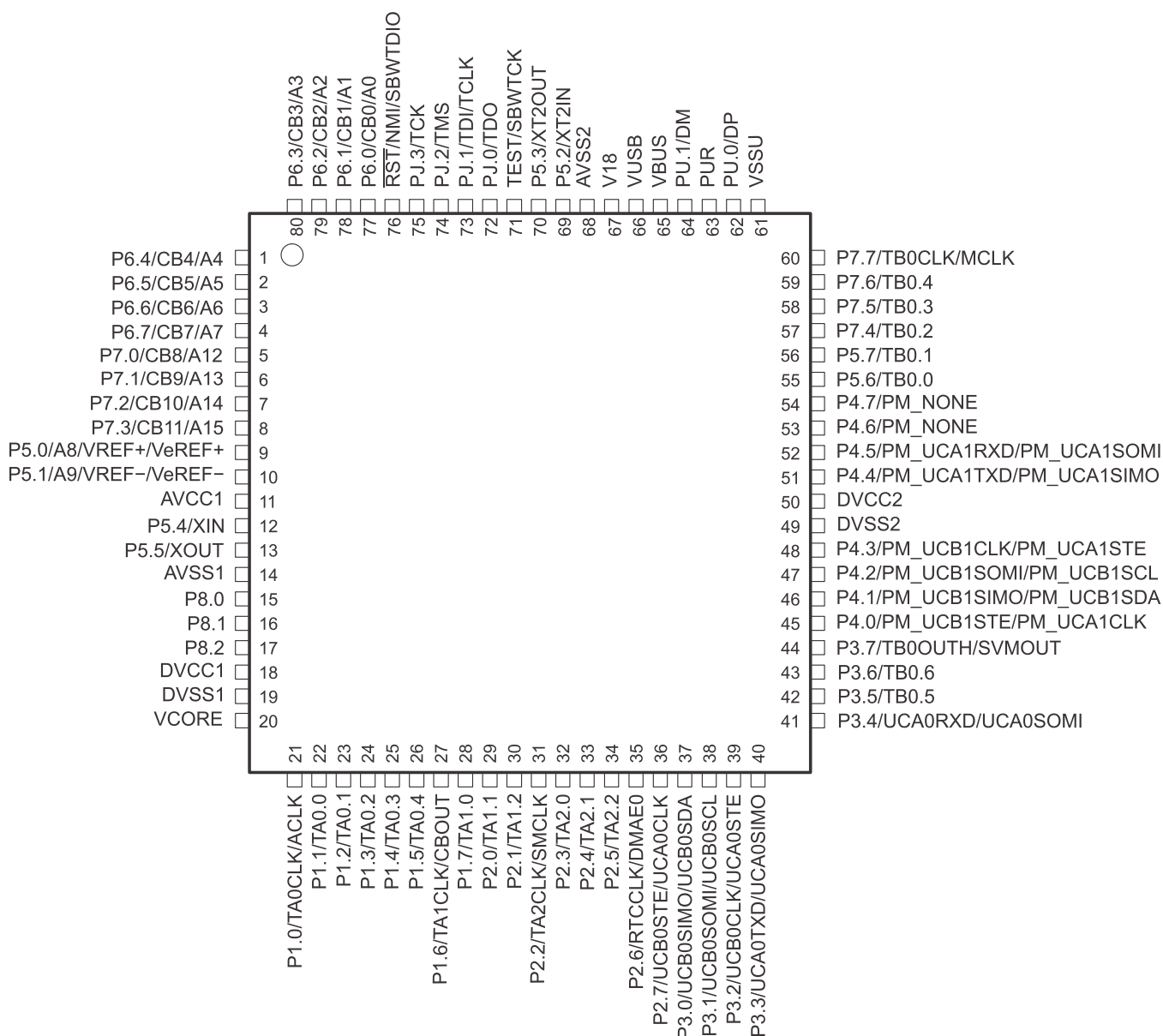
Figure 9 shows the pinout of the MSP430F5529 in the PN package (LQFP).

Figure 9. MSP430F5529 Pinout