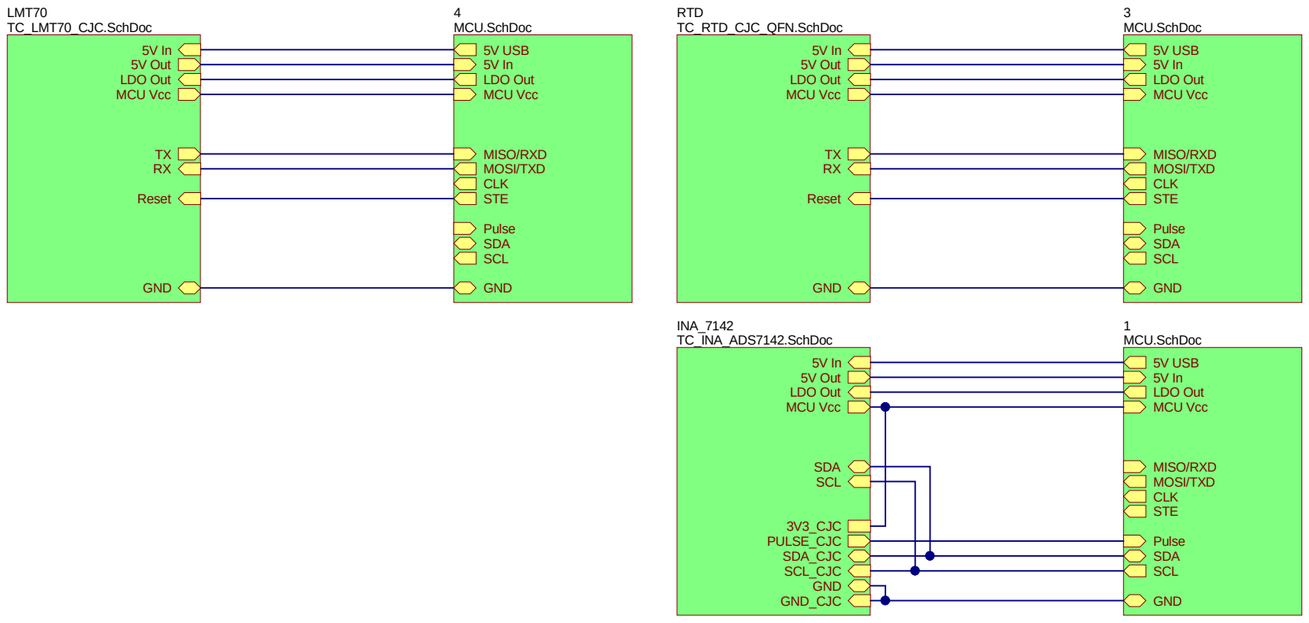
Revision History

Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A



Hardware
EVM_Hardware.SchDoc

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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 2/1/2019
TID #: N/A	Project Title: RTD Replacement	
Number: TIDA-010019 Rev: E2	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 1 of 9
Drawn By:	File: CoverSheet.SchDoc	Size: B
Engineer: Steffen Graf	Contact: http://www.ti.com/support	

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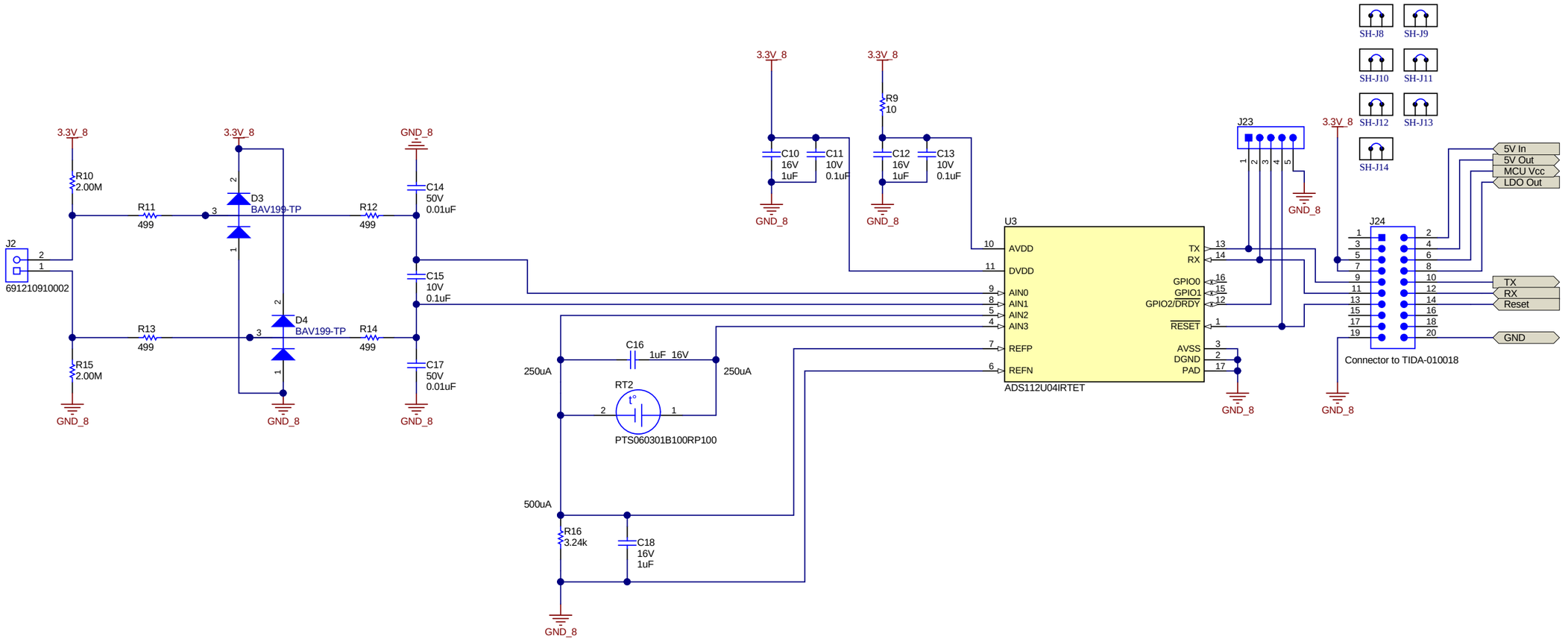
TC Interface with RTD CJC



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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 2/4/2019
TID #: N/A	Project Title: RTD Replacement	
Number: TIDA-010019 Rev: E2	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 2 of 9
Drawn By:	File: TC_RTD_CJC_QFN.SchDoc	Size: B
Engineer: Steffen Graf	Contact: http://www.ti.com/support	

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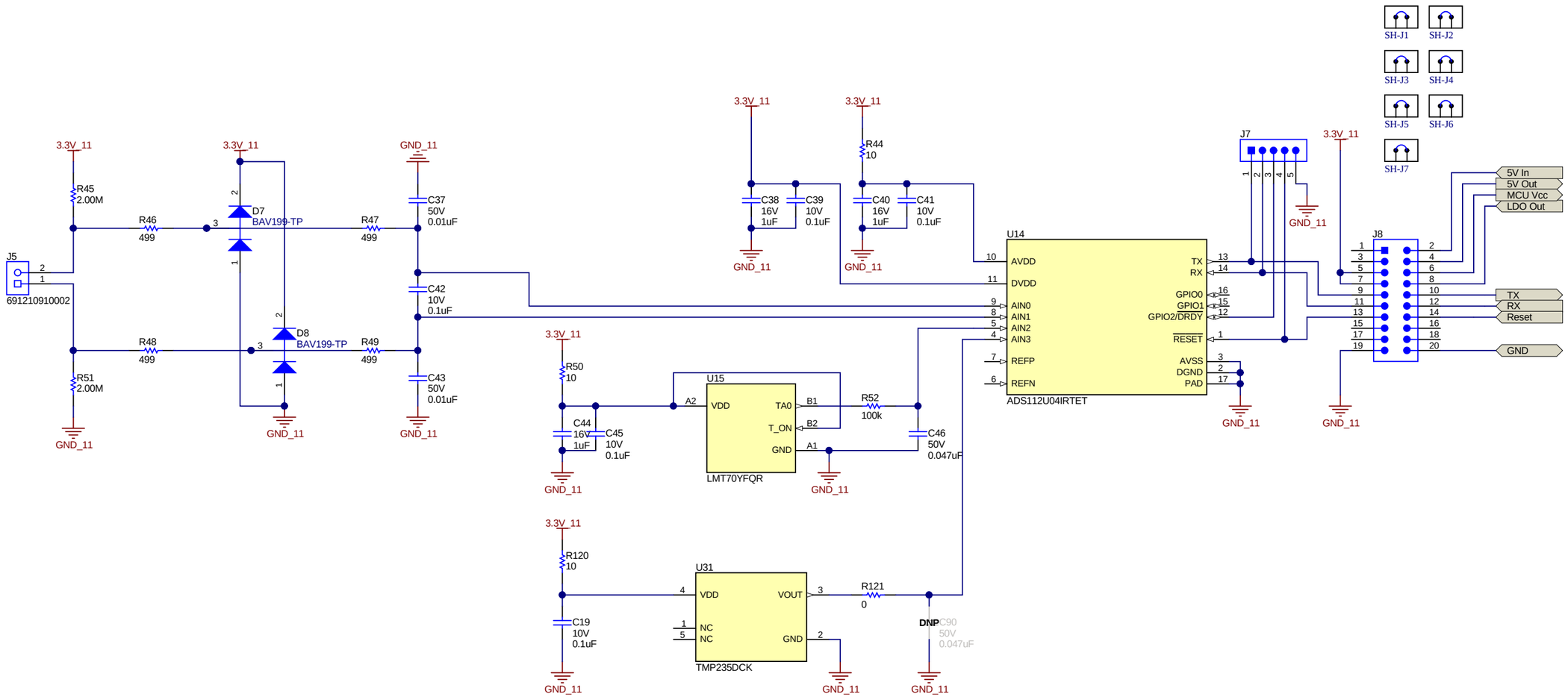

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TID #: N/A	Project Title: RTD Replacement	
Number: TIDA-010019 Rev: E2	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 3 of 9
Drawn By:	File: TC_LMT70_CJC.SchDoc	Size: B
Engineer: Steffen Graf	Contact: http://www.ti.com/support	

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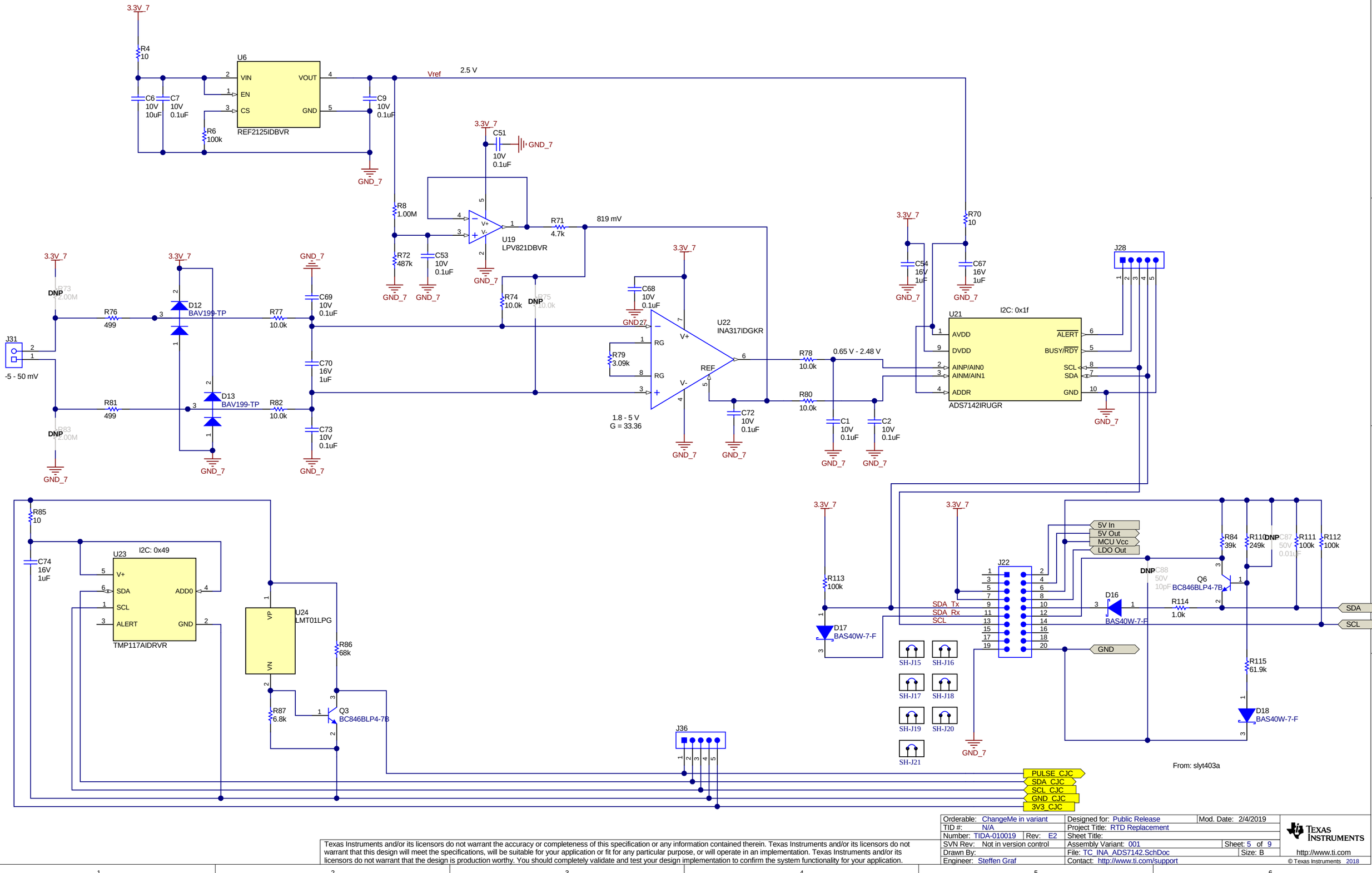

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TID #: N/A		Project Title: RTD Replacement	
Number: TIDA-010019	Rev: E2	Sheet Title:	
SVN Rev: Not in version control		Assembly Variant: 001	Sheet: 5 of 9
Drawn By:		File: TC_INA_ADS7142.SchDoc	Size: B
Engineer: Steffen Graf		Contact: http://www.ti.com/support	

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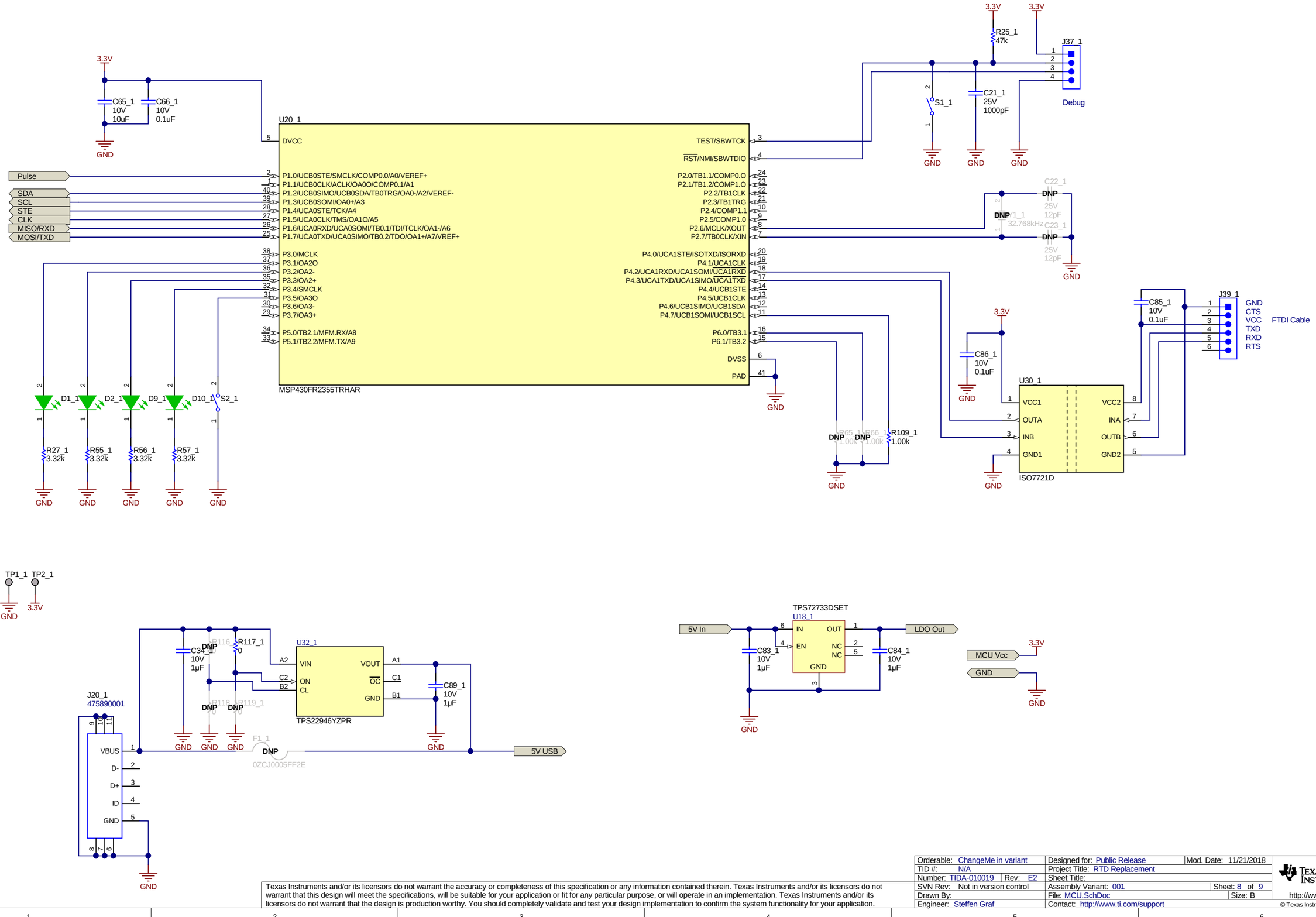

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Orderable: ChangeMe in variant	Designed for: Public Release	Mod. Date: 11/21/2018
TID #: N/A	Project Title: RTD Replacement	
Number: TIDA-010019 Rev: E2	Sheet Title:	
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 8 of 9
Drawn By:	File: MCU.SchDoc	Size: B
Engineer: Steffen Graf	Contact: http://www.ti.com/support	

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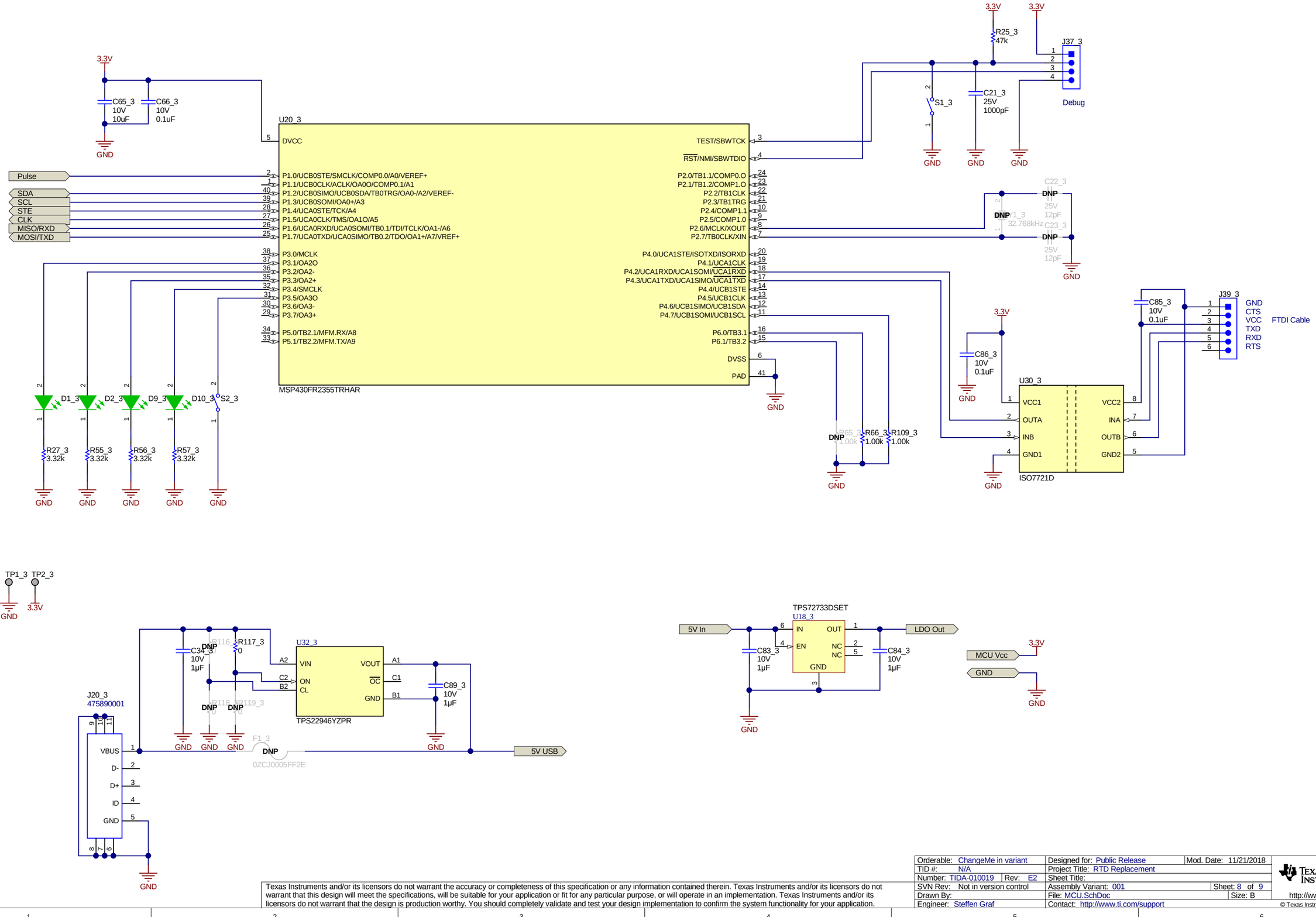

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SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 8 of 9
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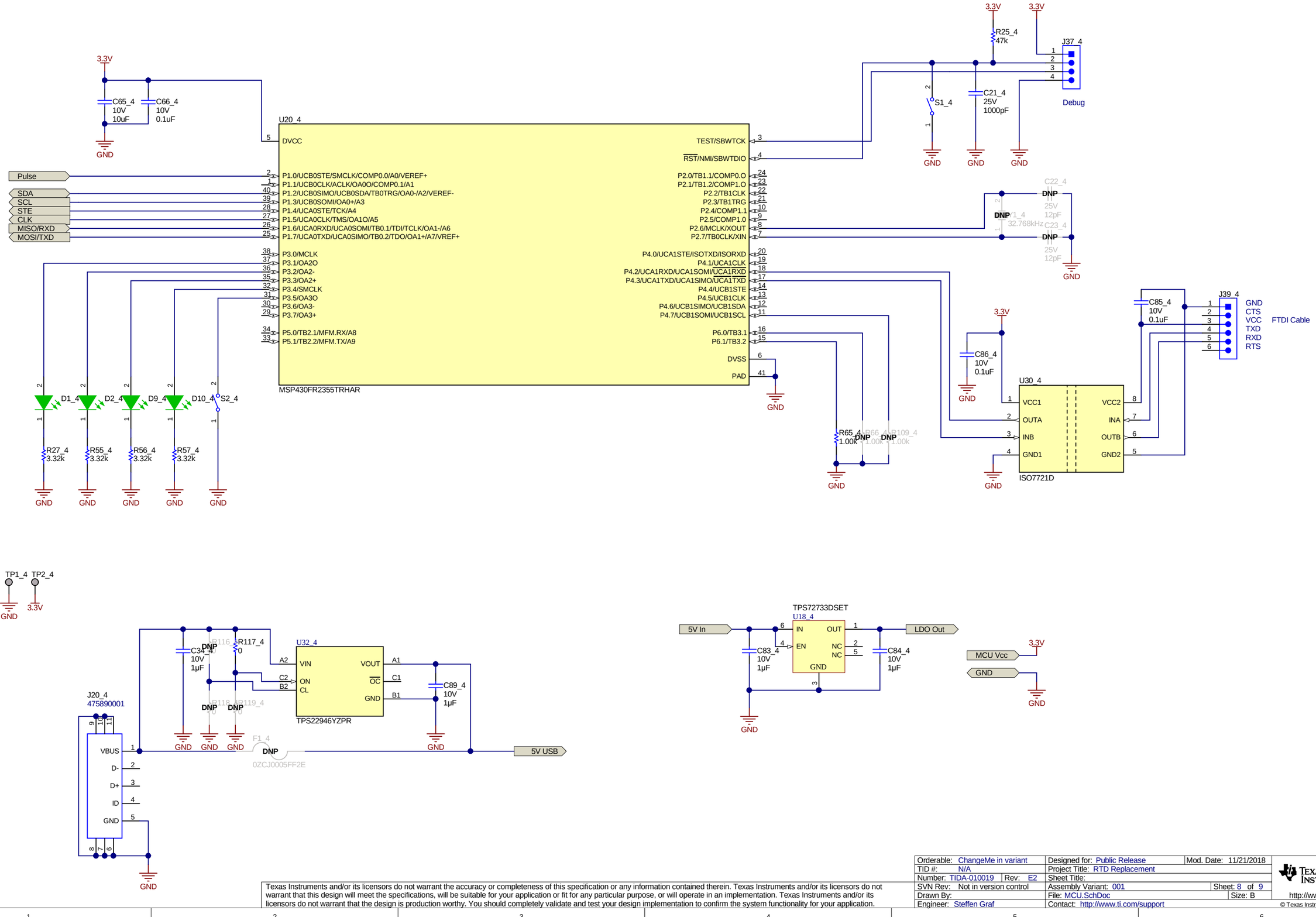

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H9 1 NY PMS 440 0025 PH
H10 1 NY PMS 440 0025 PH
H11 1 NY PMS 440 0025 PH
H12 1 NY PMS 440 0025 PH
H17 1 NY PMS 440 0025 PH
H18 1 NY PMS 440 0025 PH
H19 1 NY PMS 440 0025 PH
H20 1 NY PMS 440 0025 PH

H13 1902C
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H22 1902C
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H24 1902C

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H38 1 NY PMS 440 0025 PH
H39 1 NY PMS 440 0025 PH
H40 1 NY PMS 440 0025 PH

H45 1902C
H46 1902C
H47 1902C
H48 1902C

DNP FID4
DNP FID5
DNP FID6
DNP FID1
DNP FID2
DNP FID3

PCB LOGO Texas Instruments
PCB LOGO FCC disclaimer
PCB LOGO WEEE logo

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PCB Number: TIDA-010019
PCB Rev: E2

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Variant/Label Table	
Variant	Label Text
001	Default

LBL1
PCB Label
THT-14-423-10
Size: 0.65" x 0.20 "

LBL3
PCB Label
THT-14-423-10
Size: 0.65" x 0.20 "

LBL4
PCB Label
THT-14-423-10
Size: 0.65" x 0.20 "

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 9 of 9
Drawn By:	File: EVM_Hardware.SchDoc	Size: B
Engineer: Steffen Graf	Contact: http://www.ti.com/support	

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