

Jacob Jordan

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Profile:

A Student at Mississippi State University pursuing a Bachelor of Science in Computer Engineering with a minor in Mathematics. Currently a Senior and will graduate in the Winter of 2024.

GPA: 3.1

Technical Skills:

- Experienced in hardware design.
- Experience with radio communications.
- Proficient in Soldering delicate hardware.
- FPGA Experience.
- Experienced with Microcontrollers.
- Experience with Virtual Machines

Program Experience:

- Familiar with Quartus, Vivado, Microsoft Excel, PowerPoint, Visual Studio, VMware, Ubuntu and Mathematica.
- Has implemented code onto Arduino, Basys3 FPGA Board, and Raspberry Pi.
- Experience with SolidProfessor's CAD program.

Programming Languages:

- Assembly, Verilog, VHDL, MATLAB, Python, C++, C, Arduino

Notable Projects:

- Part of the EcoCar Programming Team.
- Built a functional radio antenna from scratch.
- Created a multiplexer from scratch inside assembly.
- Has created multiple Seven-Segment Displays on Multiple hardware programs on multiple different FPGA circuit boards.
- Created a digital I2C from scratch inside Vivado.
- Built a hardware timer on Quartus and Vivado and implemented it on FPGA boards.