

## Pre-Lab

Experiment No. 2 - Comparators,  
Adders, and Subtractors.

\*Student name: Abdulrahman  
shaheen.

\* ID#: 1211753.

\* Instructor Name: Dr. Qadri Mayyala

\* Date: 4/5/2023.

\* Section: 2.

At first there is the truth table and Boolean equations for the half adder

- Truth table:

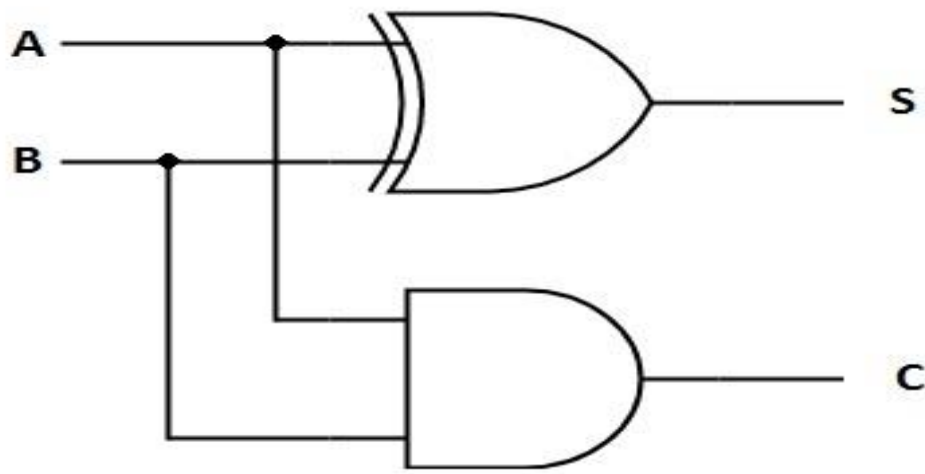
Inputs		Outputs	
A	B	Sum	Carry
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

Boolean equation in min terms:

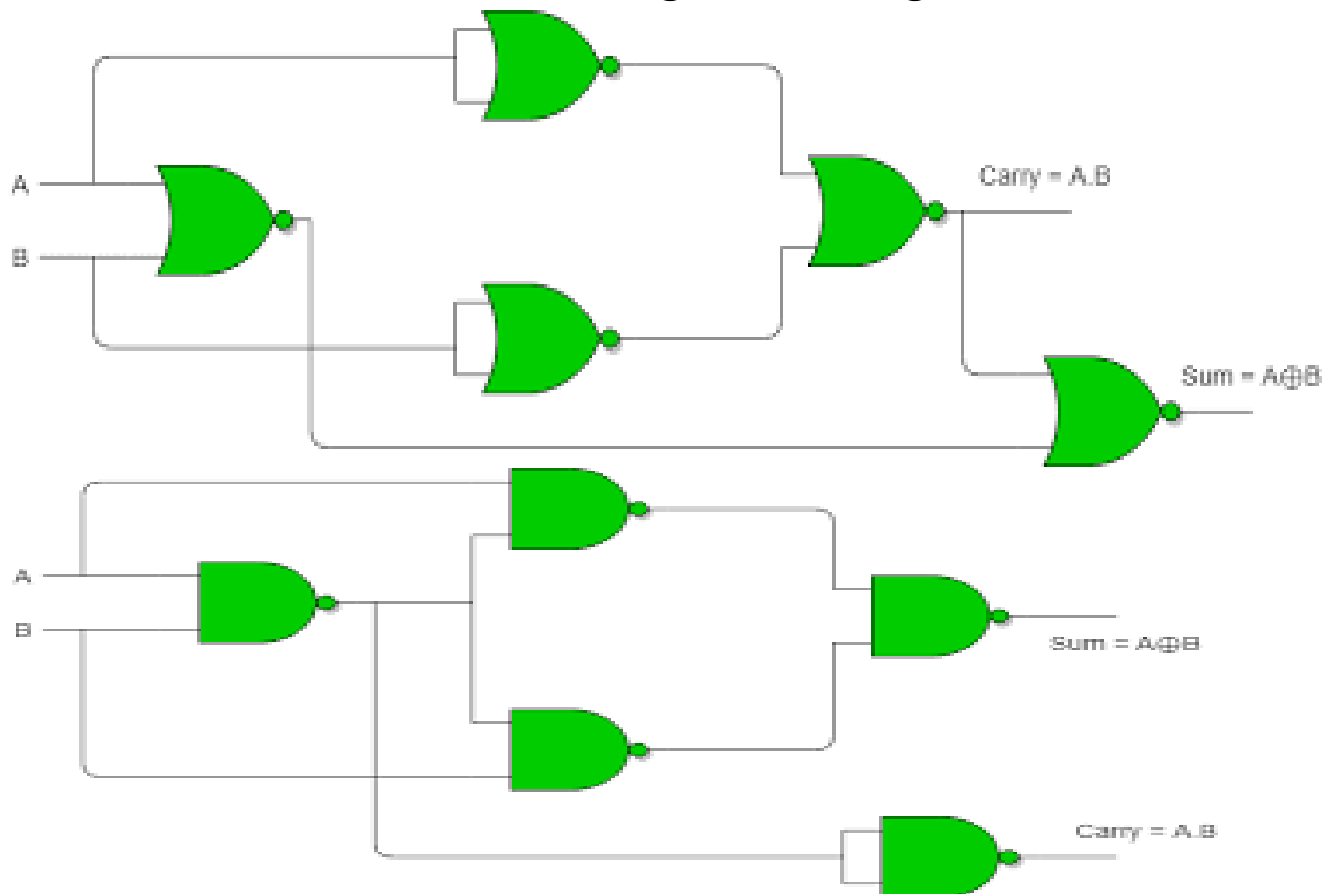
$$\text{Sum} = X'Y + XY'$$

$$\text{Carry} = XY$$

Q2: Build half adder using basic gates:



Q3: Build the above circuit using universal gates:



At first there is the truth table and Boolean equations for the full adder:

Truth table:

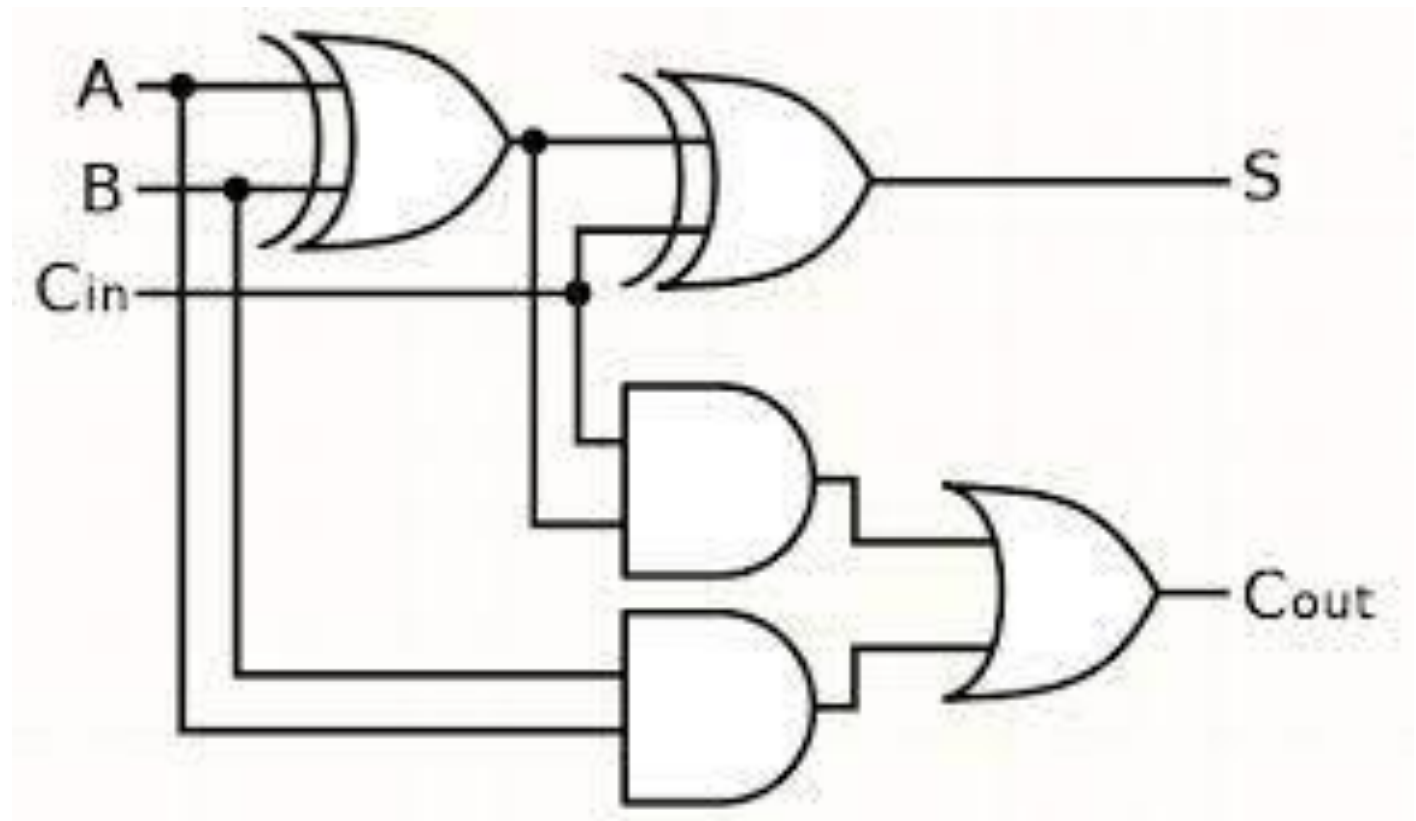
Inputs			Outputs	
A	B	C – IN	Sum	C – Out
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1

Boolean equation in min terms:

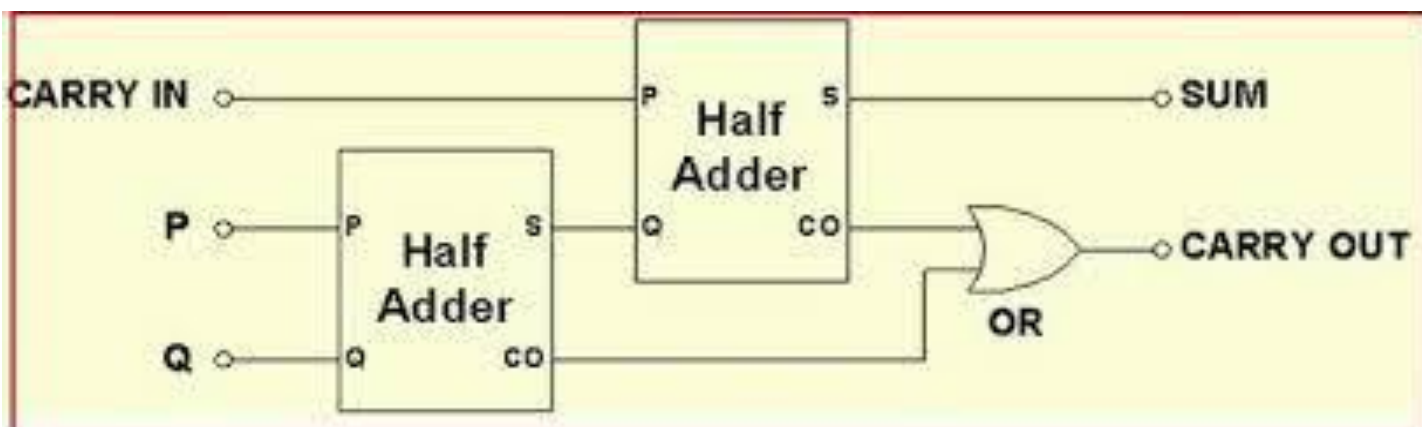
$$\text{Sum} = X'Y'C' + X'YC' + XY'C' + XYZ$$

$$\text{Carry} = XY + YC + XC$$

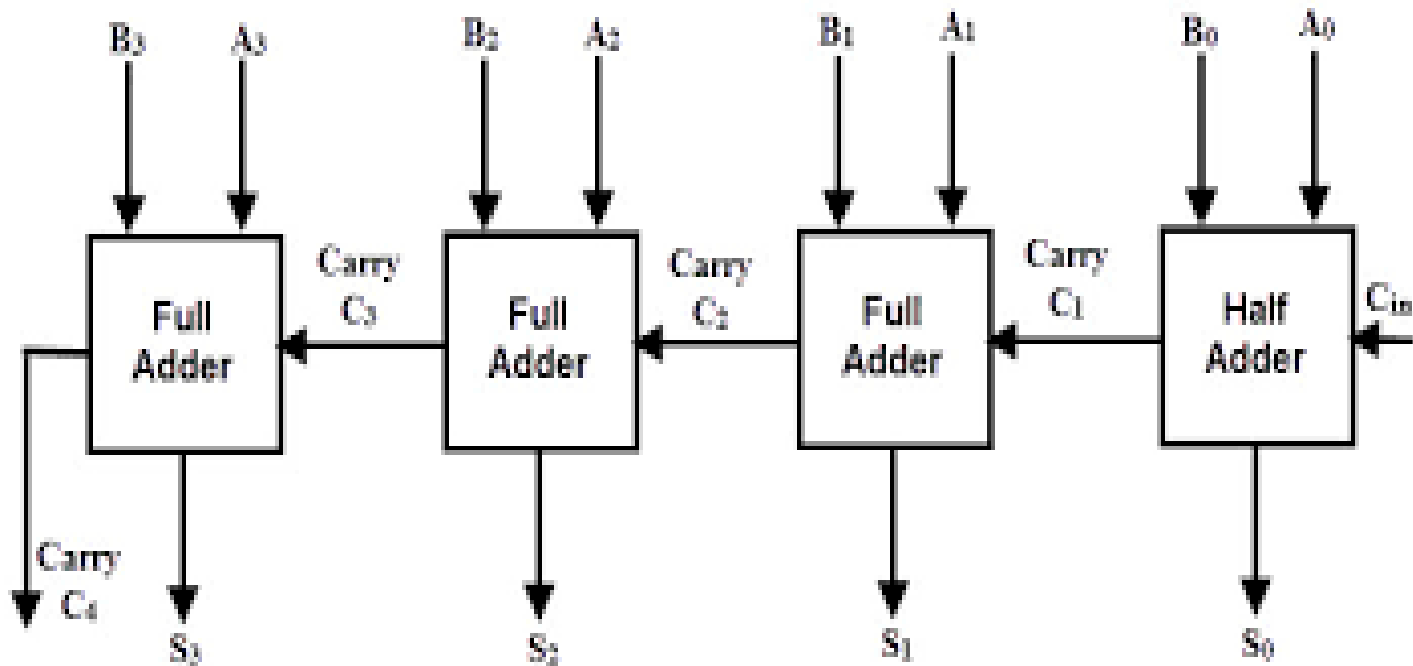
Q4: Build a full adder using basic gates:



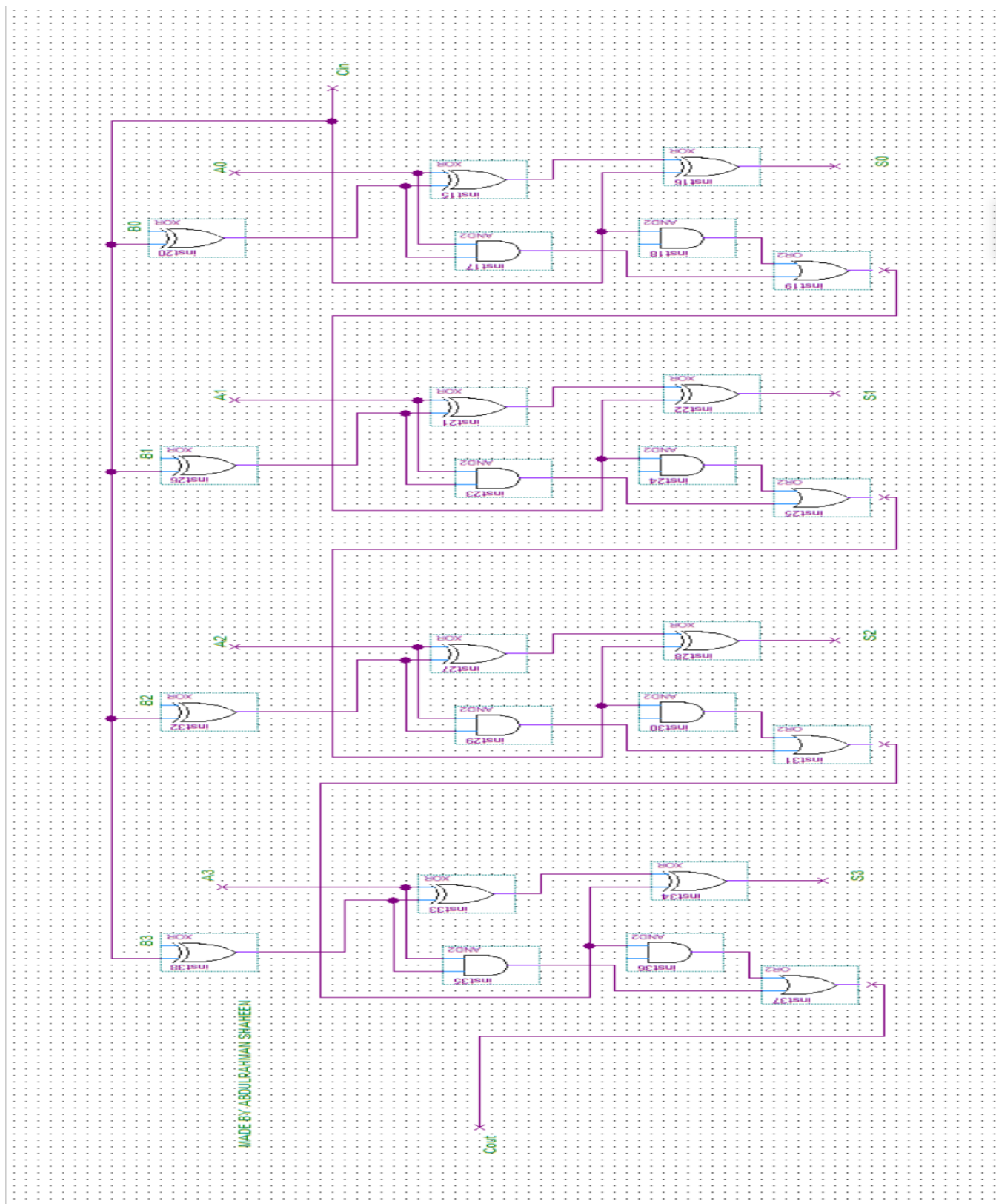
Q5: Build a full adder using half adder and another gate:



Q6: Build a 4-bit adder using a full adder:



Q7: Build a 4-bit subtractor using basic gates.

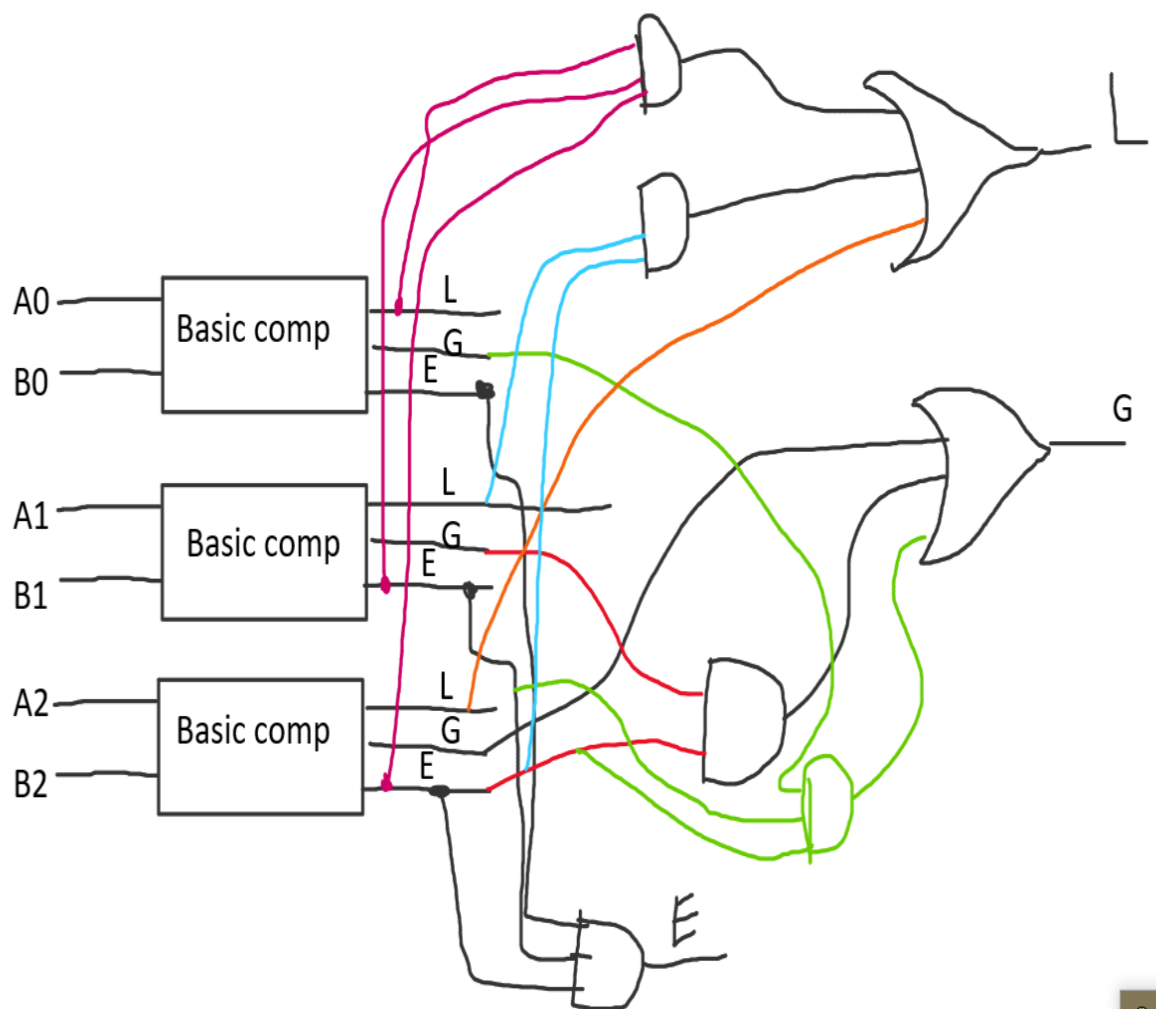






Q1: all sections:

Design a three-bit comparator (using the basic comparator) and hand it out to your TA. (Pre Lab).



Hand out, Design, Boolean function, and truth table of half- and full-adder to your TA. (Pre Lab).

Done already in the previous sections.

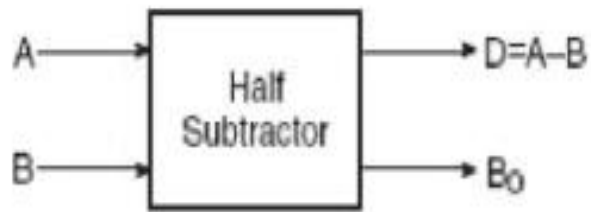
$Q_2 + Q_4$

Hand out, Design, Boolean function, and truth table of half- and full-subtractor to your TA. (Pre Lab).

Half subtractor:

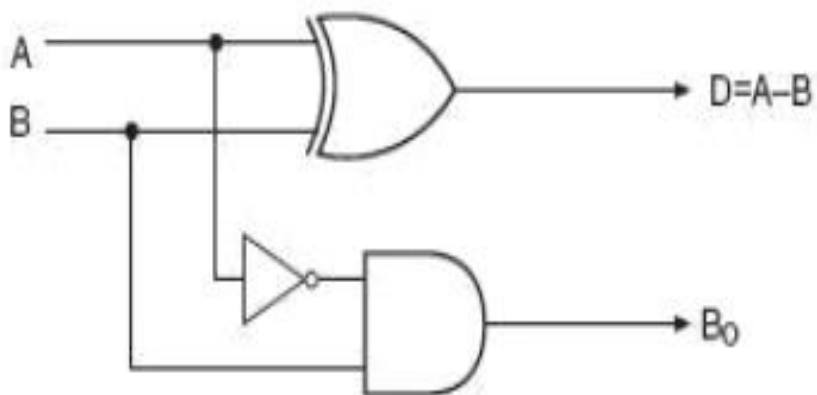
$$D = \bar{A}.B + A.\bar{B}$$

$$B_0 = \bar{A}.B$$



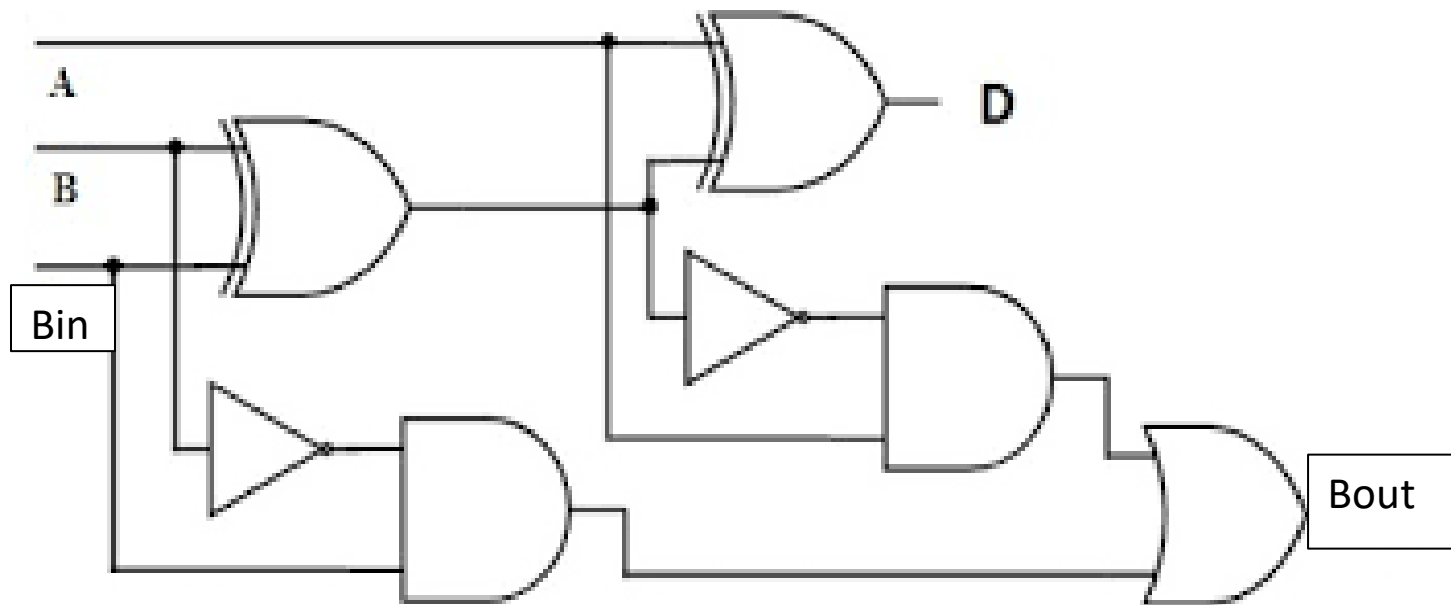
A	B	D	B <sub>0</sub>
0	0	0	0
0	1	1	1
1	0	1	0
1	1	0	0

Half Subtractor



full subtractor:

Inputs			Outputs	
A	B	Bin	Diff	Bout
0	0	0	0	0
0	0	1	1	1
0	1	0	1	1
0	1	1	0	1
1	0	0	1	0
1	0	1	0	0
1	1	0	0	0
1	1	1	1	1



$$D = A \oplus B \oplus C$$

$$Bout = A'B + A'Bin + BBin$$