post-Lab

Experiment No. 4 - Digital Circuits Implementation using Breadboard

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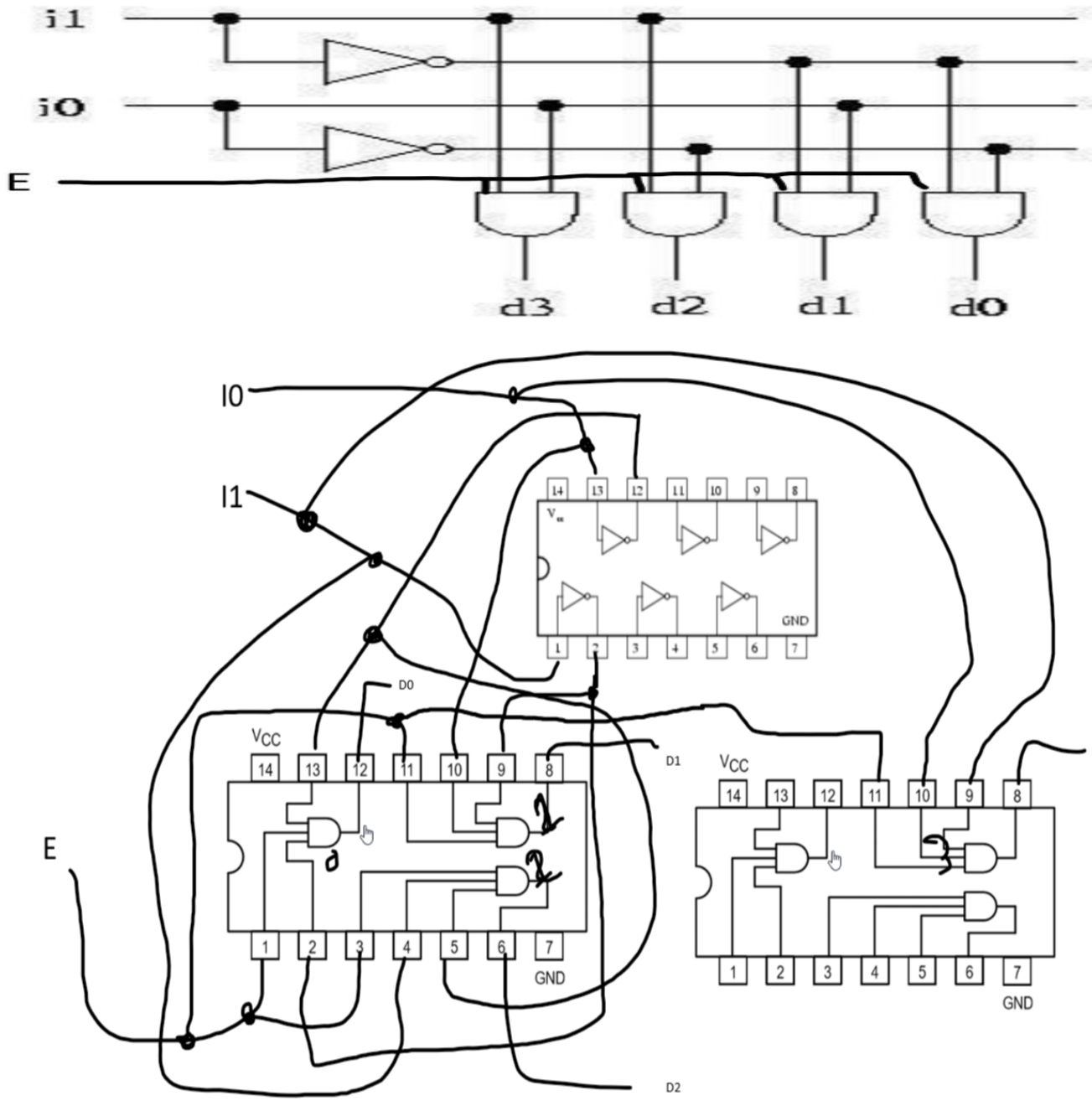
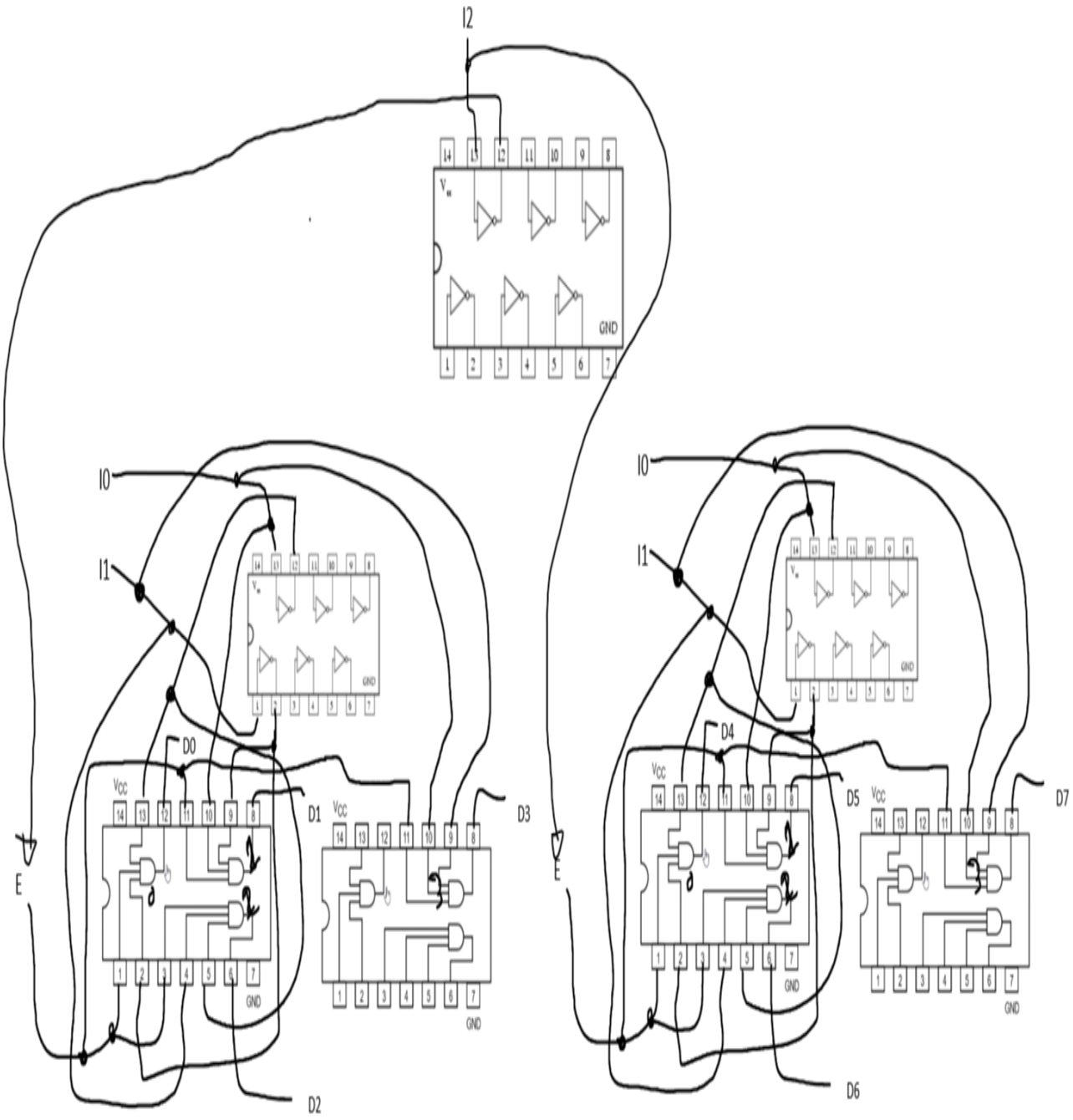
* Date: 3/5/2023.

* Section: 2.

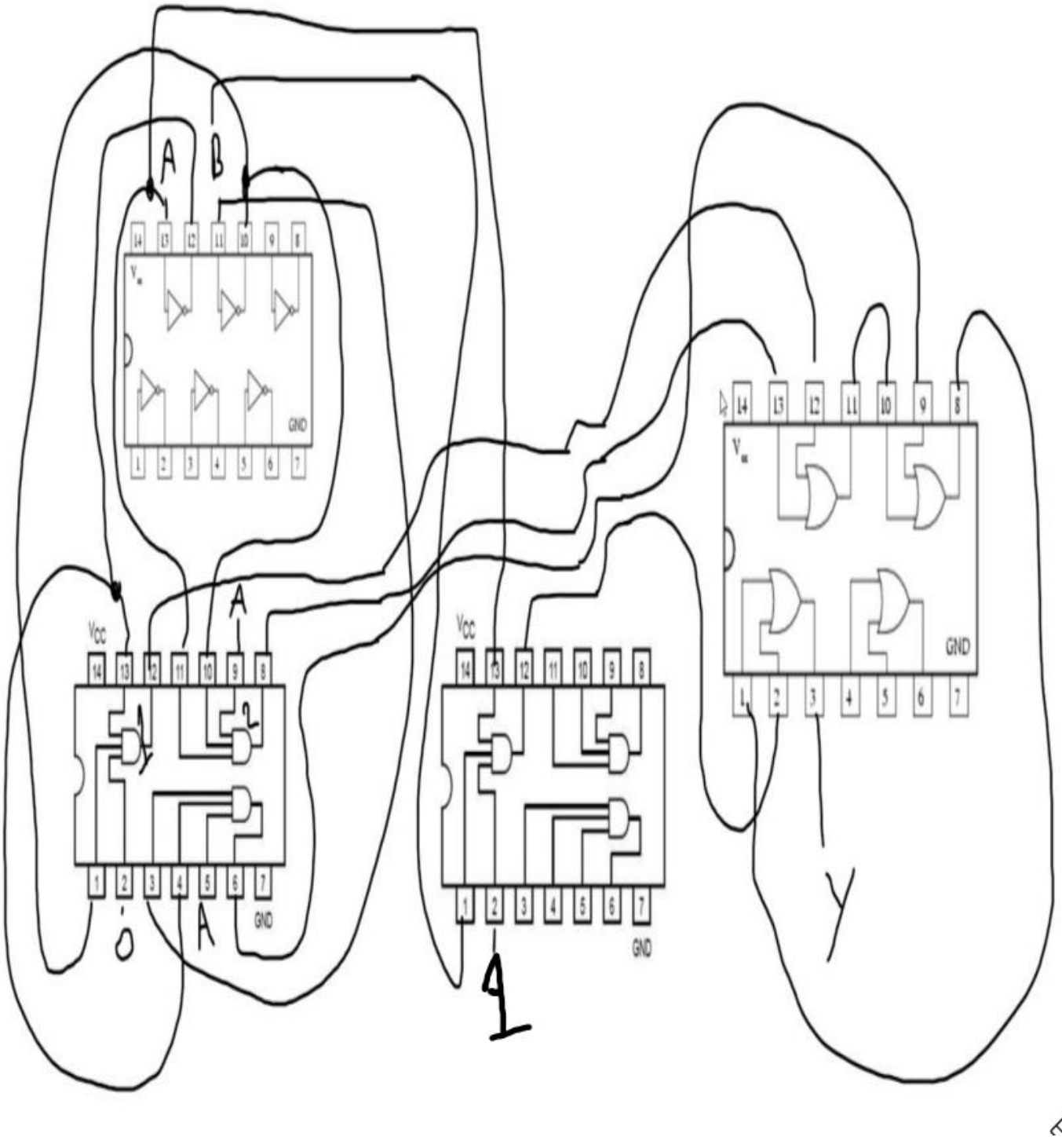
Q1) by just adding the enable signal to each and gate:

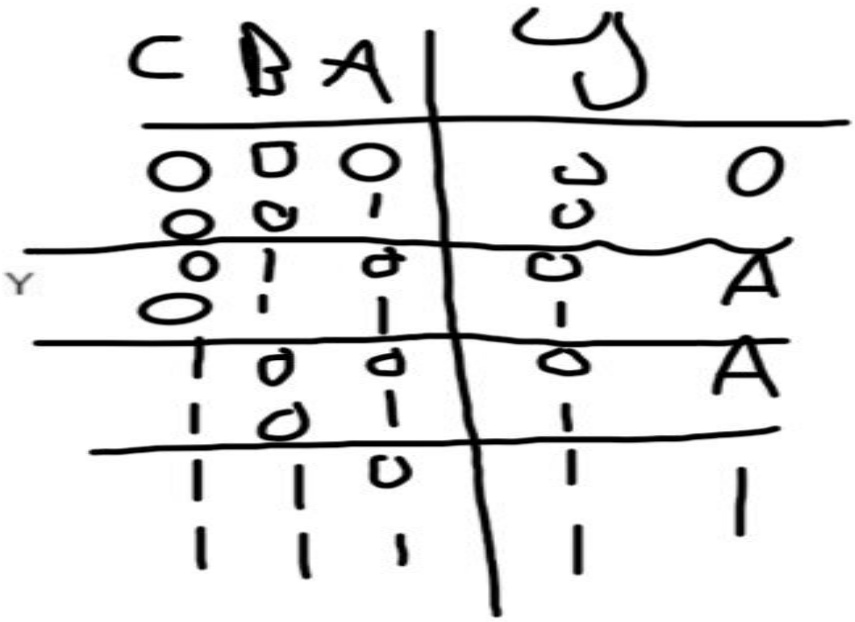


Q2)

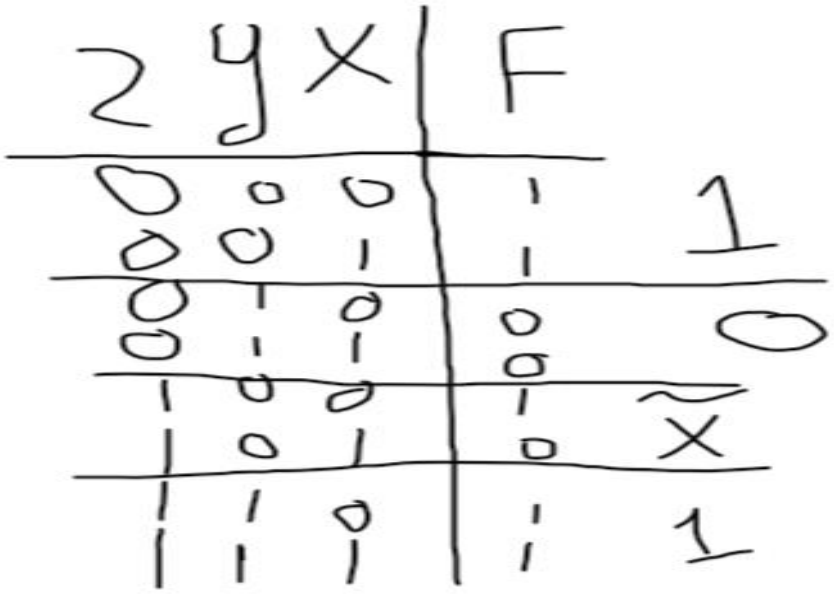


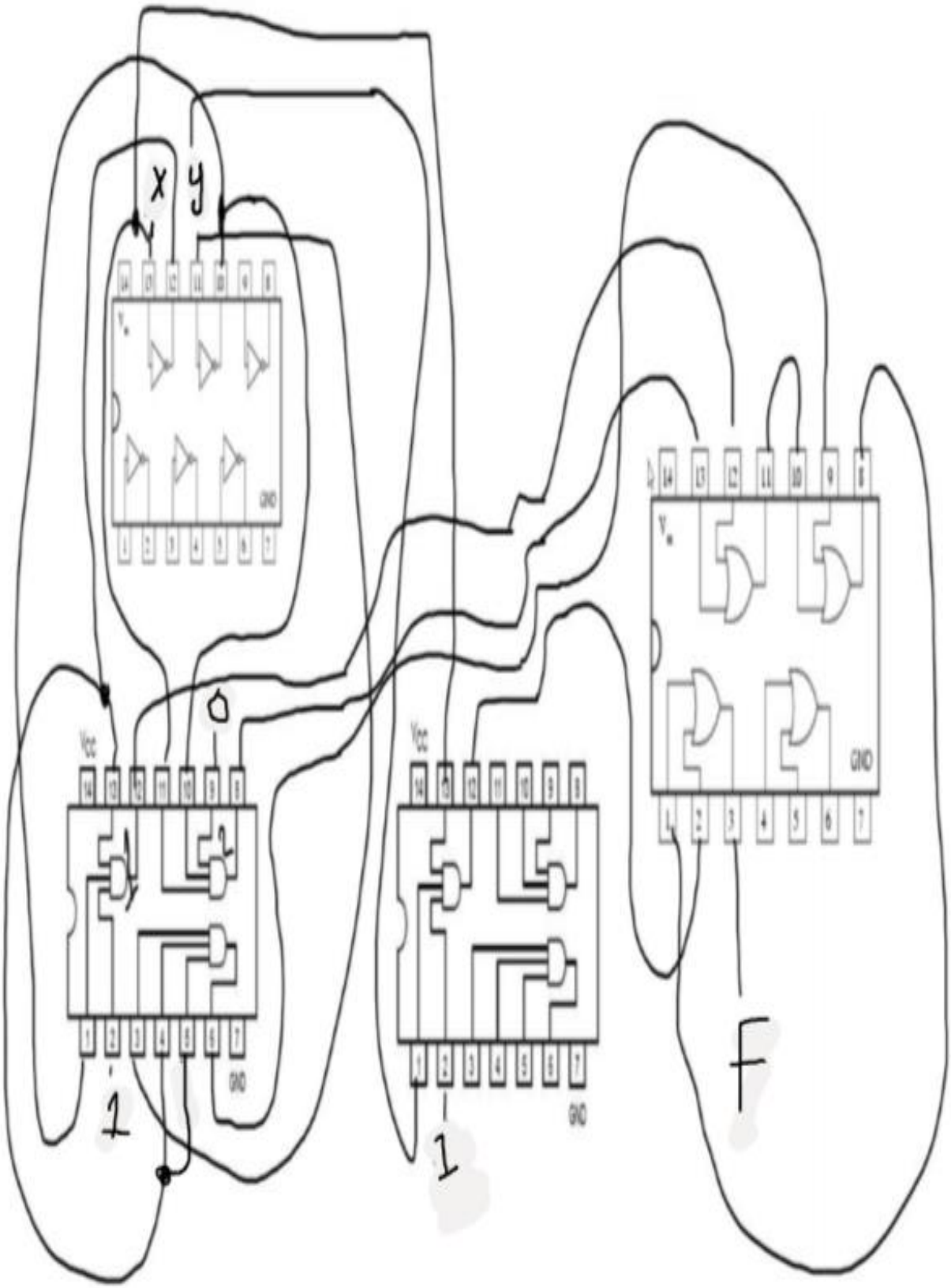
Q3) done already in the pre-lab:




C B A | J
0 0 0 | 0
0 0 1 |
0 1 0 | A
0 1 1 |
1 0 0 | A
1 0 1 |
1 1 0 | 1
1 1 1 |

Q4)


Z Y X | F
0 0 0 | 1
0 0 1 | 1
0 1 0 | 0
0 1 1 | 0
1 0 0 | ~
1 0 1 | X
1 1 0 | 1
1 1 1 | 1


x
y
Vcc
GND
1
1
f