Birzeit University logo

Faculty of Engineering and Technology

Electrical and Computer Engineering Department

Advanced Digital Design ENCS3310

Homework#2

1. Given the following primitive flow table, go through design procedure steps and implement the circuit using SR latches. **[20 points]**

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Stable State | Inputs | | output | Notes |
| x1 | x2 | Q |
| a | 1 | 1 | 1 | After c |
| b | 0 | 1 | 0 | After e |
| c | 0 | 1 | 1 | After a, f |
| d | 1 | 0 | 0 | After a, e, f |
| e | 1 | 1 | 0 | After b, d |
| f | 0 | 0 | 1 | After b , c, d |

1. Show the primitive flow table for positive edge T-FF **[10 points]**