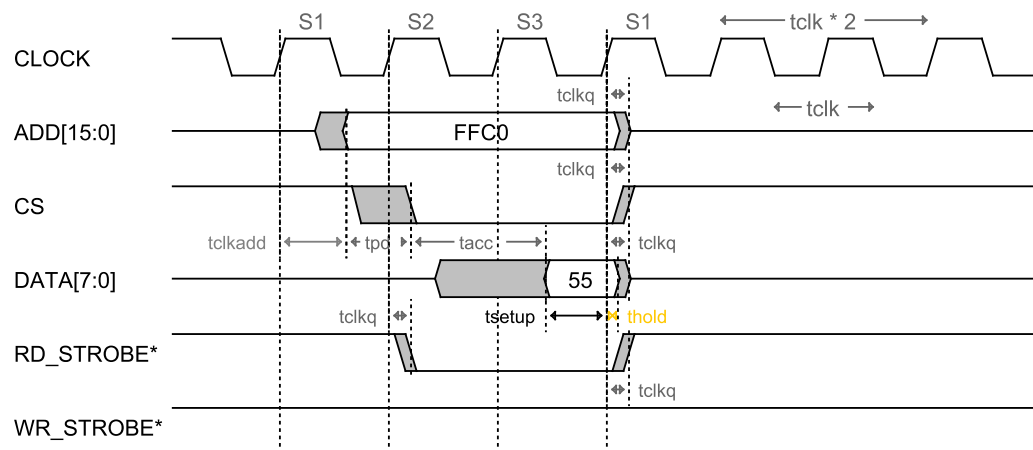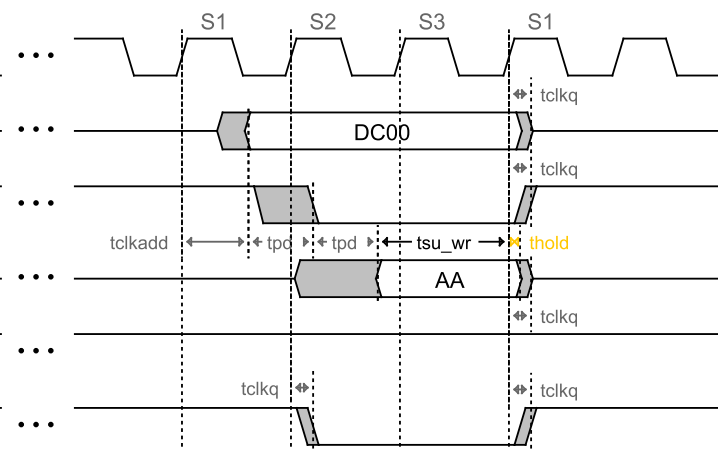Memory Read Cycle

Memory Write Cycle

Pulse Width Labels, Delays, and Constraints can cross TimeWarps