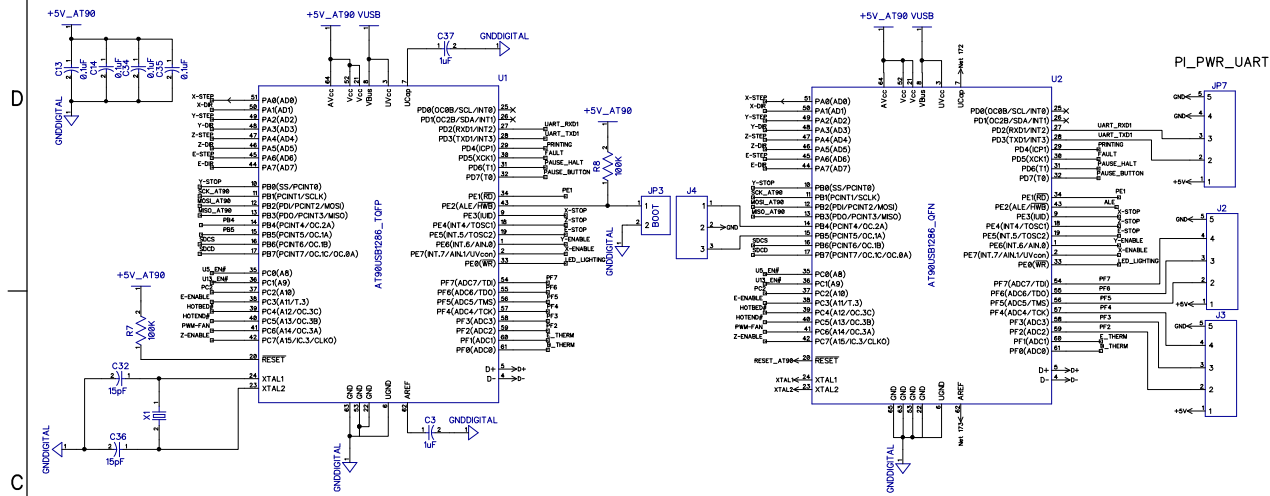


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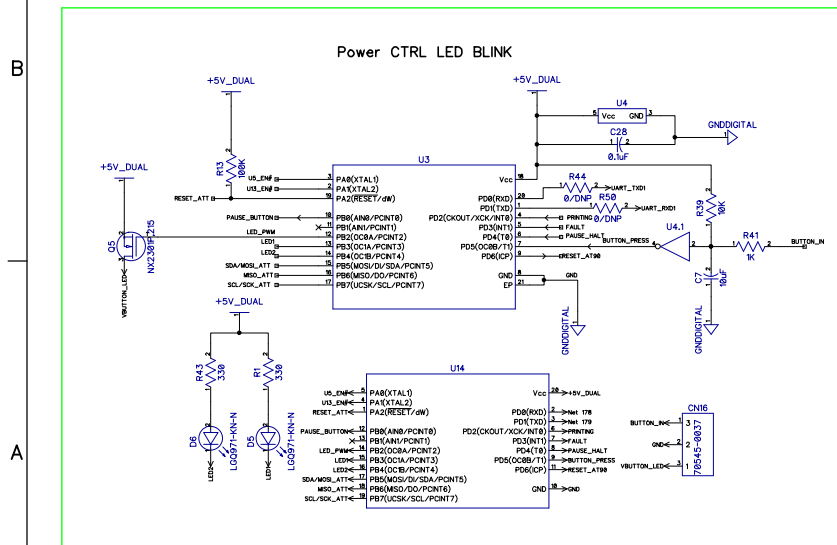
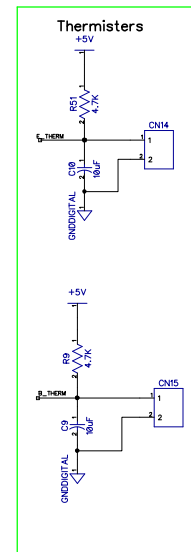
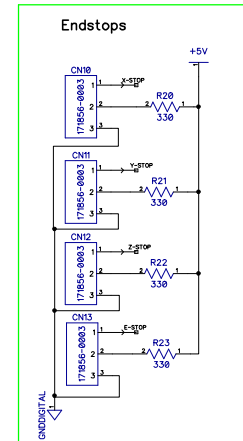
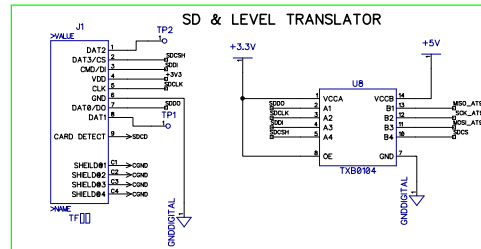
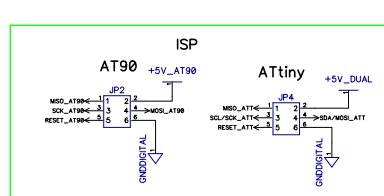
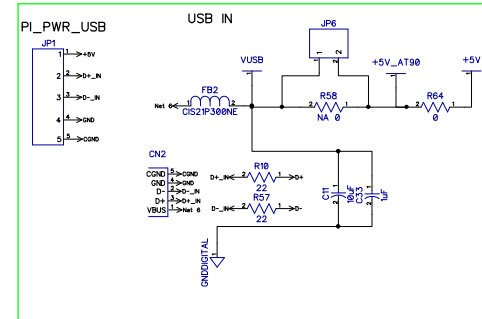
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| REVISIONS | | | | |
|-----------|-----|-------------|------|----------|
| ZONE | REV | DESCRIPTION | DATE | APPROVED |



| | | | | |
|---|----------|---------|--------|--------|
| Designed by RUSHdesign for Solidoodle | | | | |
| IC's, USB, microSD, Endstops, Thermistors | | | | |
| SIZE | FSCM NO. | DWG NO. | SD5AA | REV DA |
| SCALE | | | Sheet1 | |

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Build Options

| | |
|-----|---|
| 1 | Necessary |
| 1.1 | AT90-TQFP Cannot build 1.2 in conjunction Includes Build option 1 |
| 1.2 | AT90-QFN Cannot Build 1.1 in conjunction Includes Build option 1 |
| 1.3 | USB via CN2 Cannot Build 1.4 in conjunction Includes Build option 1 |
| 1.4 | USB via JP5 Cannot build 1.3 in conjunction Includes Build option 1 |
| 2 | Optional 12V ports |
| 3 | Micro SD card Option |
| | |

| REVISIONS | | | | |
|-----------|-----|-------------|------|----------|
| ZONE | REV | DESCRIPTION | DATE | APPROVED |

| Issue | Date | Who | Fix Date | comments | Who | Review |
|---|----------|------|----------|--|-----|--------|
| Sense R's Need to Be larger than 0603 | 4/3/2014 | Matt | 4/4/2014 | Switched to 0805 for all controllers | Tom | |
| UPDATED POWER BLOCK See notes | | | | | | |
| GND ISSUES AGND AND GND HAVE SAME NAME | | | | Seperated GNDs | | |
| Added Fuse and Dcap to input see RED | | | | | | |
| Electrolitic caps missing Values | | | | Matched all to 100uF | | |
| add PN for electrolitic caps 36VTol | | | | | | |
| Need to review fet function Sim shows High loss | | | | | | |
| No current limit on Stop Headers | | | | Added 220 current limiter | | |
| Pattern F1 | 4/10 | Matt | 4/10 | Added New Pattern Library 'fuses' | Tom | |
| Pattern U\$8 | 4/10 | Matt | 4/10 | Added new pattern to crystal library is now X1 | | |
| Pattern S1 to be removed as AT90 will do this | 4/10 | Matt | 4/10 | | | |
| Pattern JP14 | 4/10 | Matt | 4/10 | Updated to rush 2pin .100* and is now JP5 | | |
| LED Buton Header and Debounce | | | 4/12 | | | |
| Motor dirver headers | | | 4/13 | | | |
| LED's for ATTiny | | | 4/12 | Both are green now and 603 | | |
| Silk on pin headers | | | 4/13 | JP1 Left as a non locking 5 pin header | | |
| L1 & L2 Pattern | | | 4/13 | | | |
| Add test points for var signals | | | | | | |
| U7 Pattern | | | 4/13 | | | |
| Q6,7,8 Import error from Eagle S and D swap | | | 11/06/14 | | | |
| | | | | | | |

Designed by RUSHdesign for Solidoodle

Change log

| SIZE | FSCM NO. | DWG NO. | SD5AA | REV DA |
|-------|----------|---------|---------|--------|
| SCALE | | | Sheet 3 | |

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