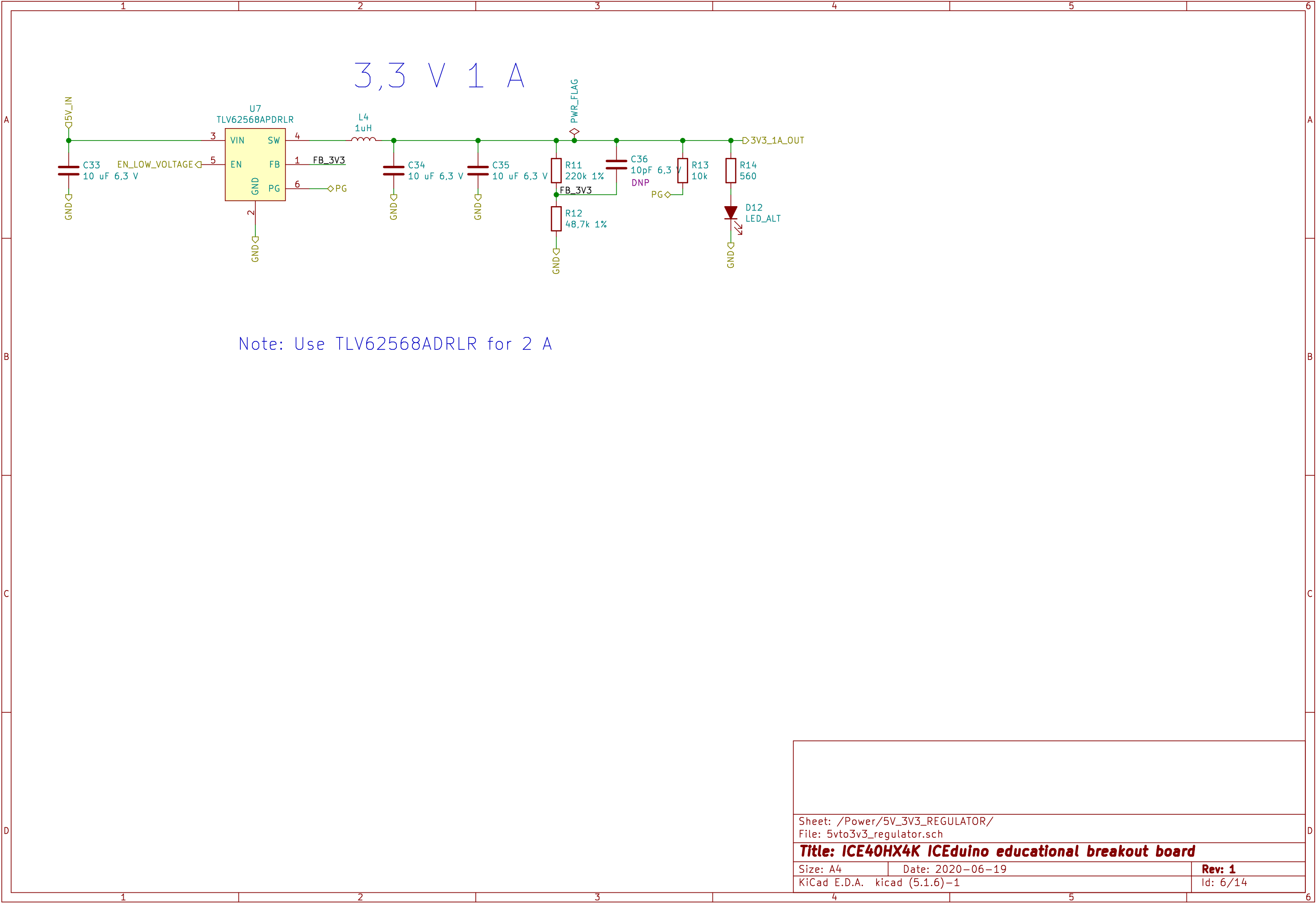
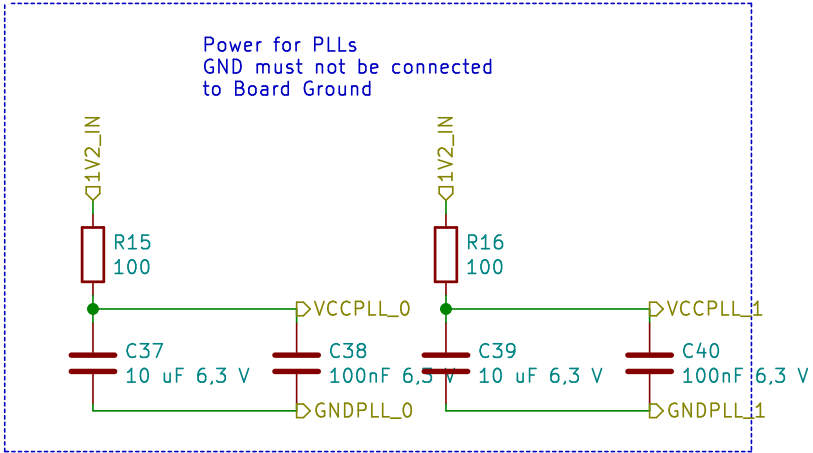


Id: 5/14





Sheet: /Power/PLL_POWER/
File: pll_power.sch

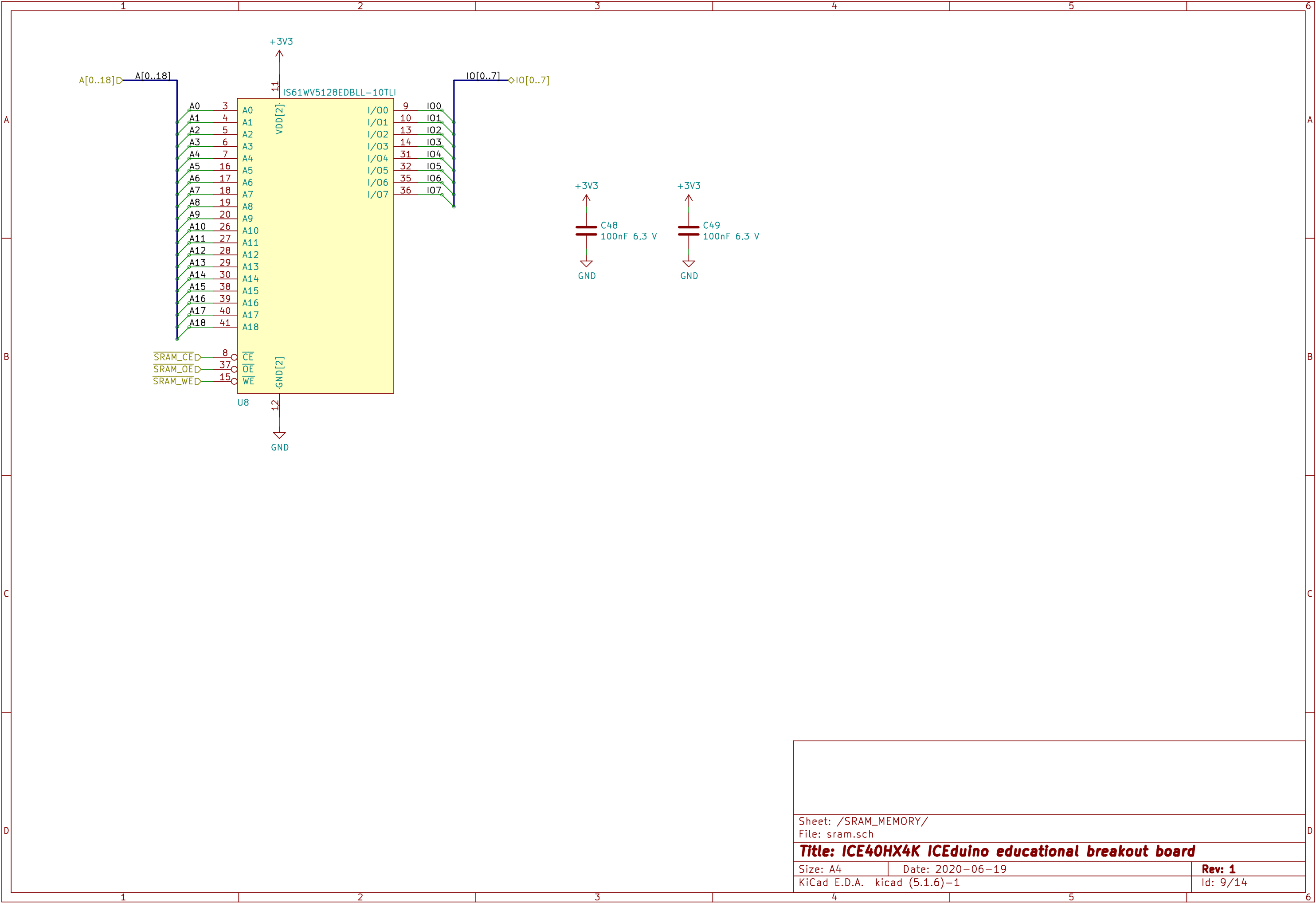
Title: ICE40HX4K ICeduino educational breakout board

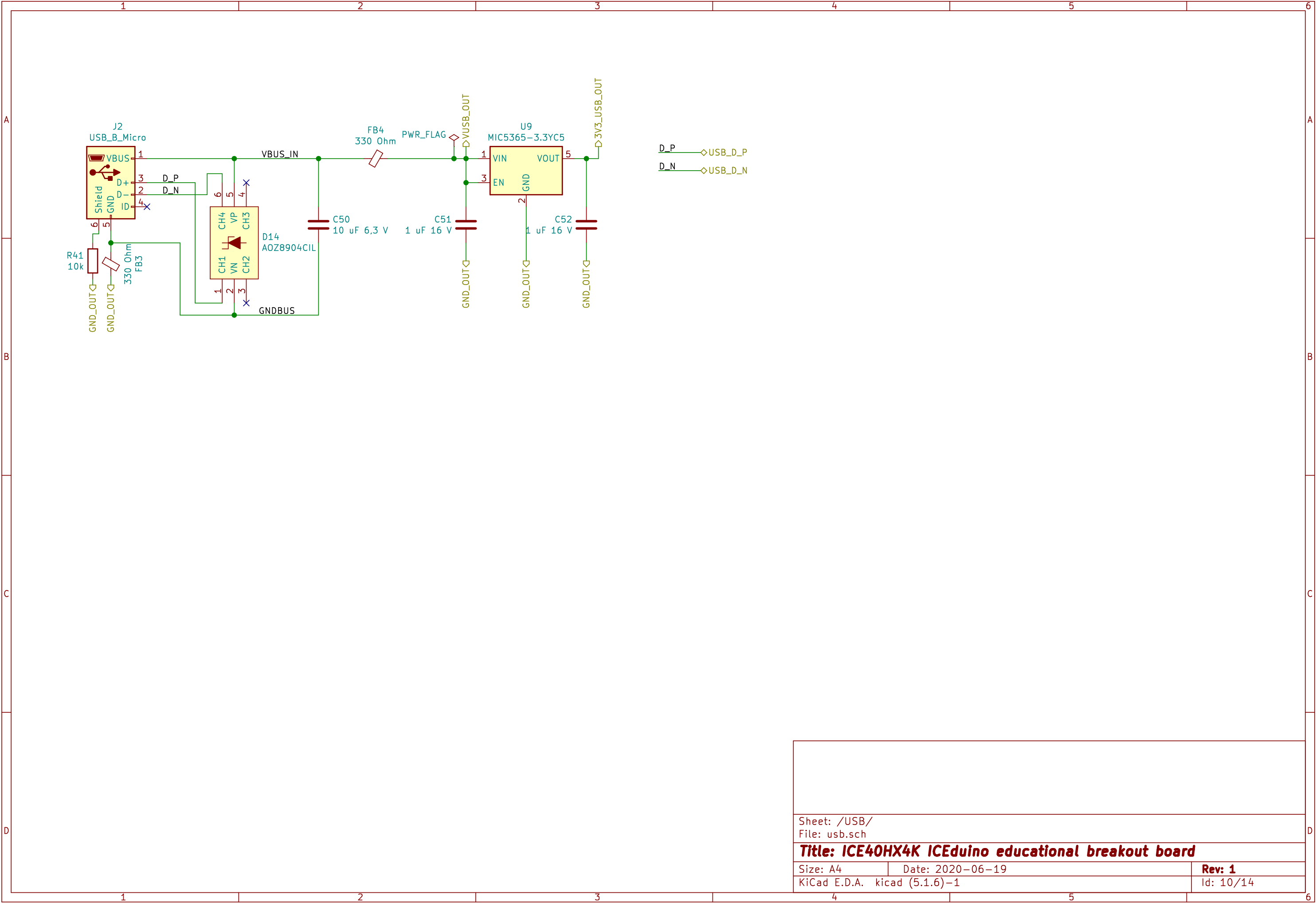
Size: A4 Date: 2020-06-19

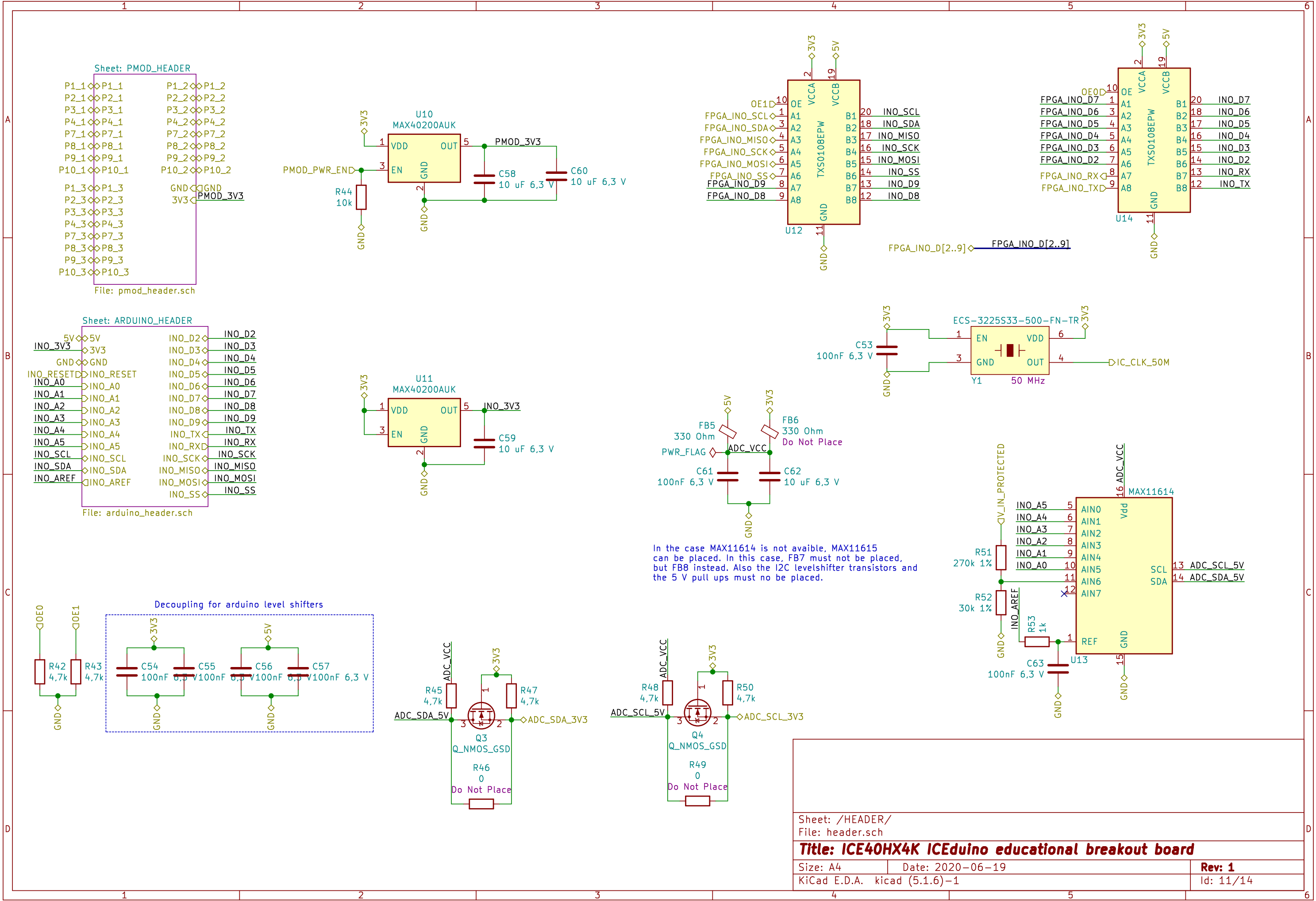
Rev: 1

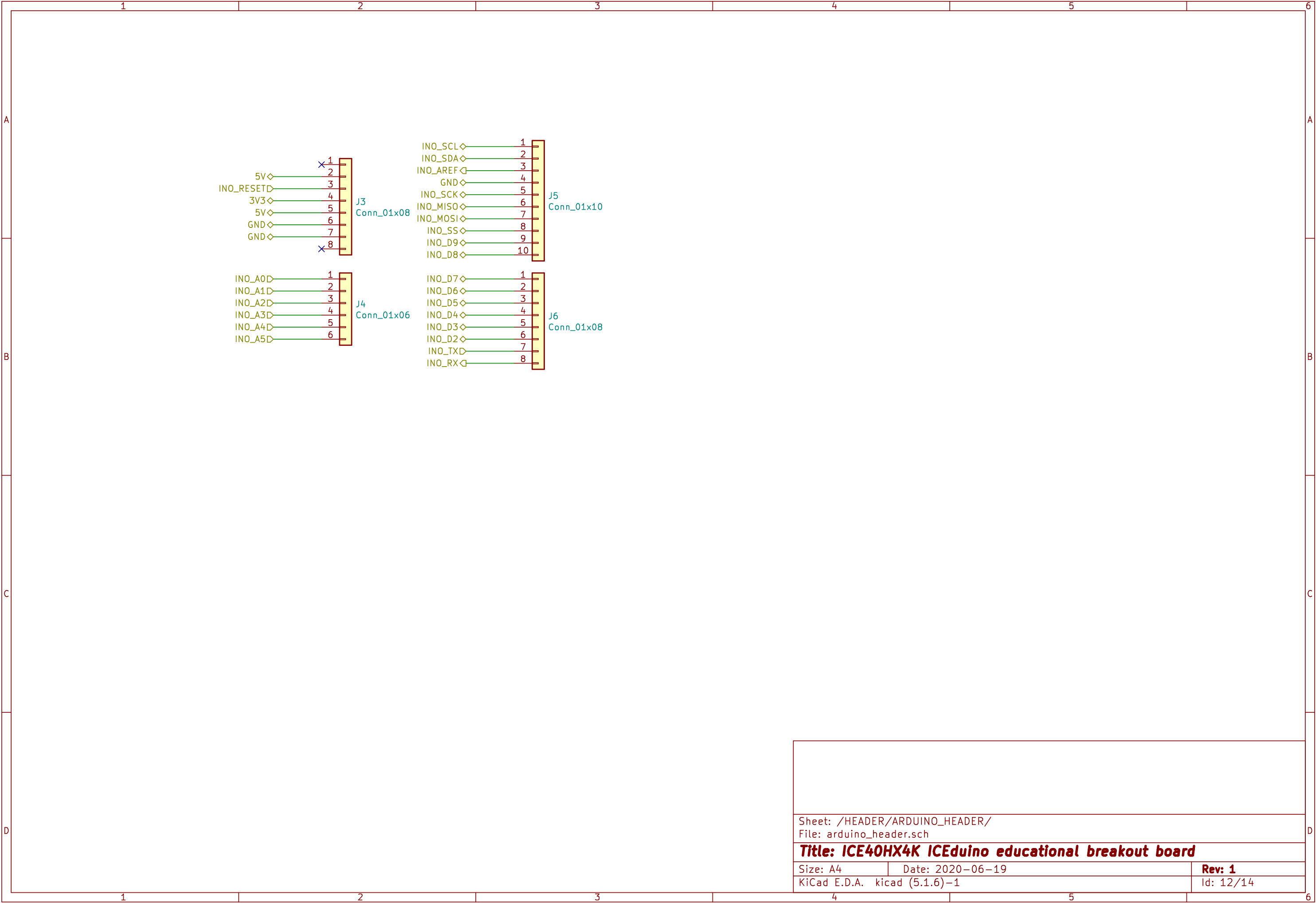
KiCad E.D.A. kicad (5.1.6)-1

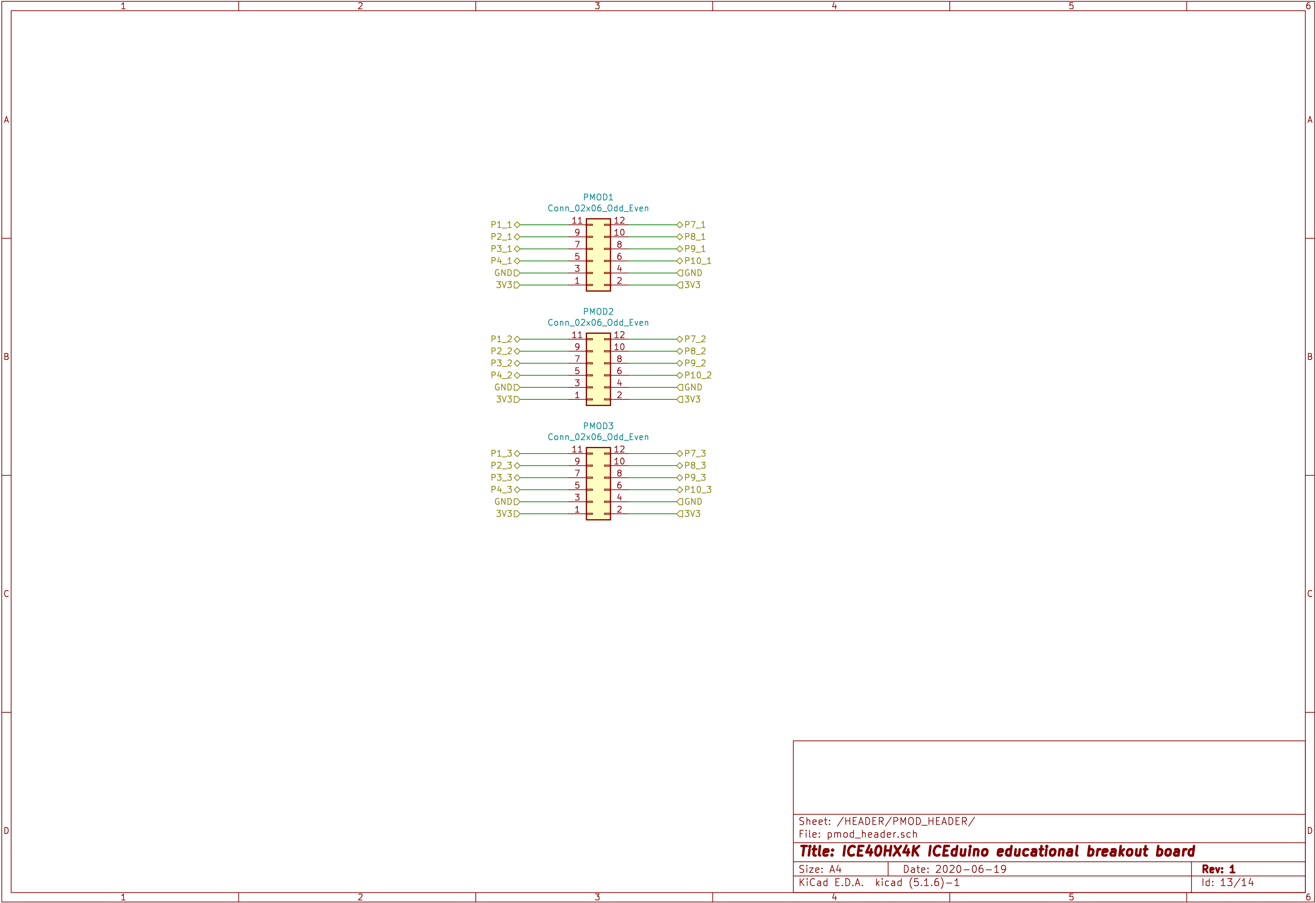
Id: 7/14











PMOD2

Conn_02x06_Odd_Even

P1_2

11

12

P7_2

P2_2

9

10

P8_2

P3_2

7

8

P9_2

P4_2

5

6

P10_2

GND

3

4

GND

3V3

1

2

3V3

PMOD3

Conn_02x06_Odd_Even

P1_3

11

12

P7_3

P2_3

9

10

P8_3

P3_3

7

8

P9_3

P4_3

5

6

P10_3

GND

3

4

GND

3V3

1

2

3V3

Sheet: /HEADER/PMOD_HEADER/

File: pmod_header.sch

Title: ICE40HX4K ICEduino educational breakout board

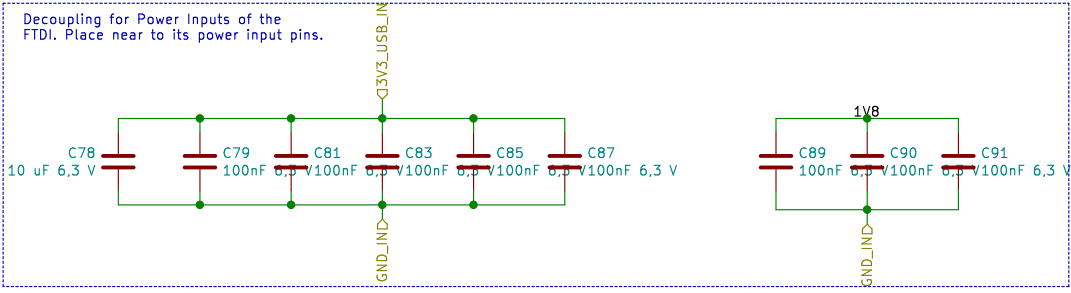
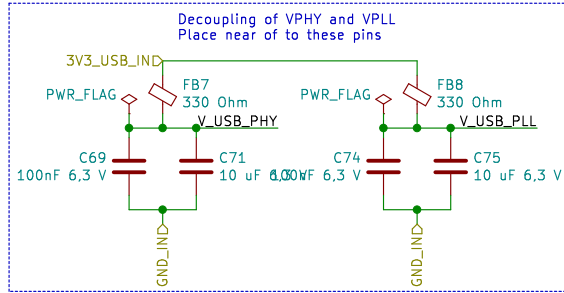
Size: A4

Date: 2020-06-19

Rev: 1

KiCad E.D.A. kicad (5.1.6)-1

Id: 13/14



Place R66 and R67 for quad-SPI setup.
Place R63 and R68 instead to
disable write protection and hold.

