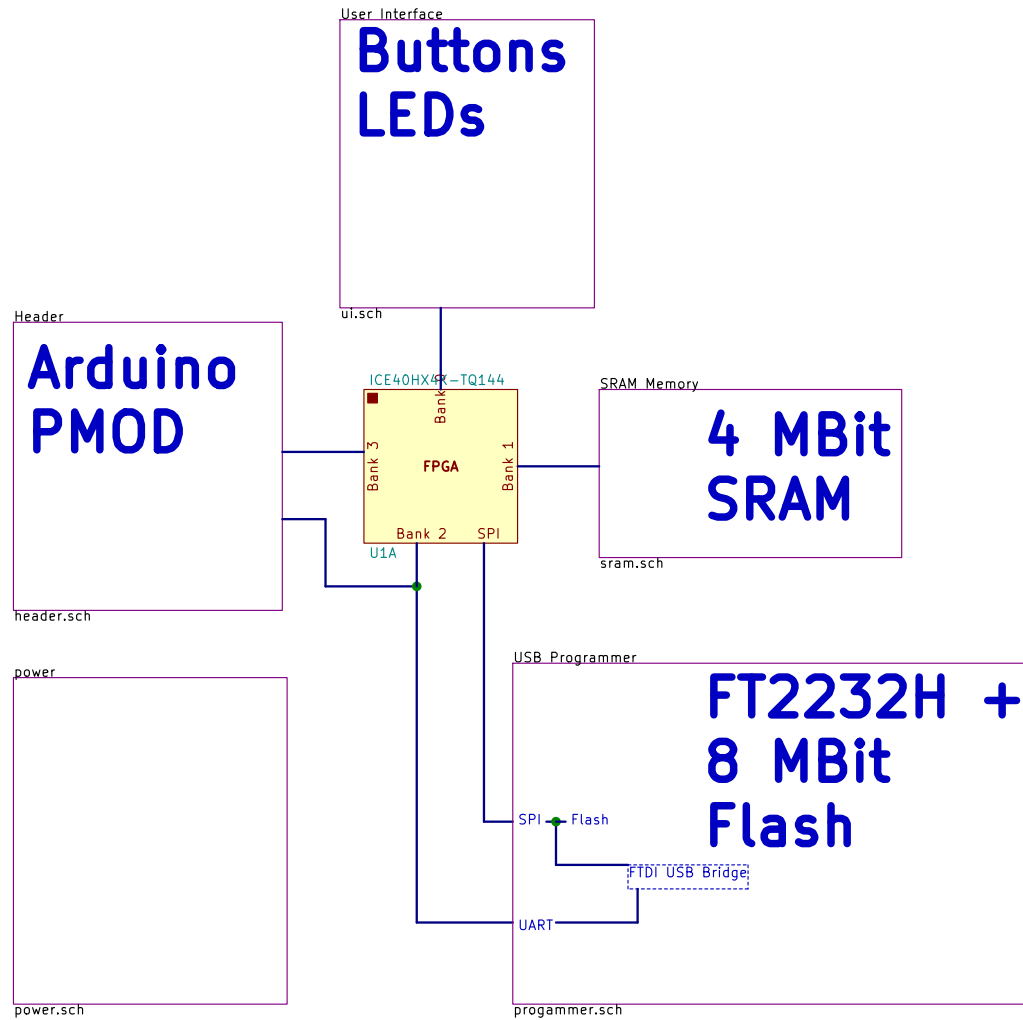


- H1 MountingHole
- H2 MountingHole
- H3 MountingHole
- H4 MountingHole



<https://www.latticesemi.com/en/Products/FPGAandCPLD/ICE40>

<http://www.latticesemi.com/-/media/LatticeSemi/Documents/UserManuals/EI/icestickusermanual.pdf>

http://www.latticesemi.com/-/media/LatticeSemi/Documents/DataSheets/ICE/FPGA-DS-02029-3-5-ICE40-LP-HX-Family-Data-Sheet.ashx?document_id=49312

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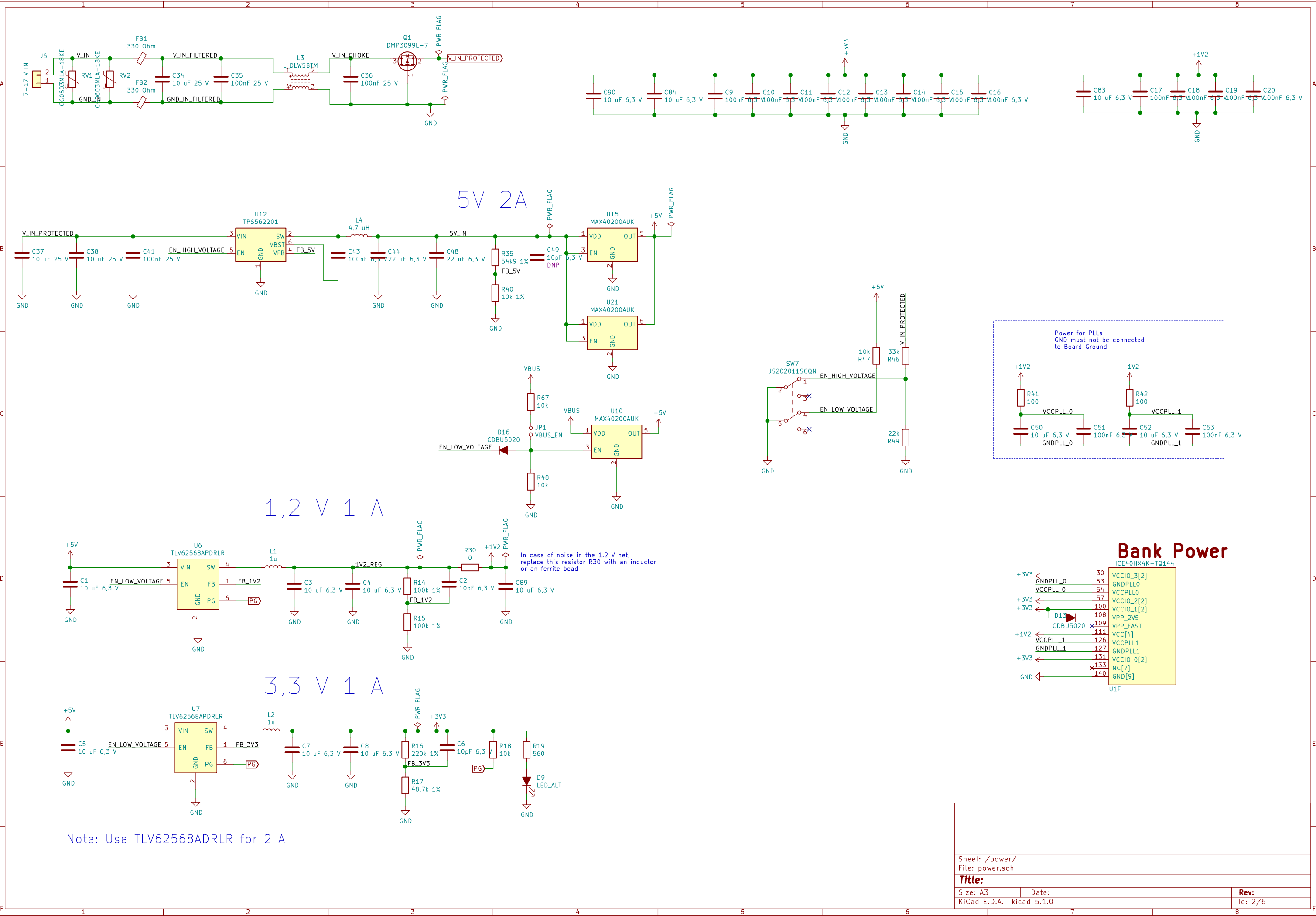
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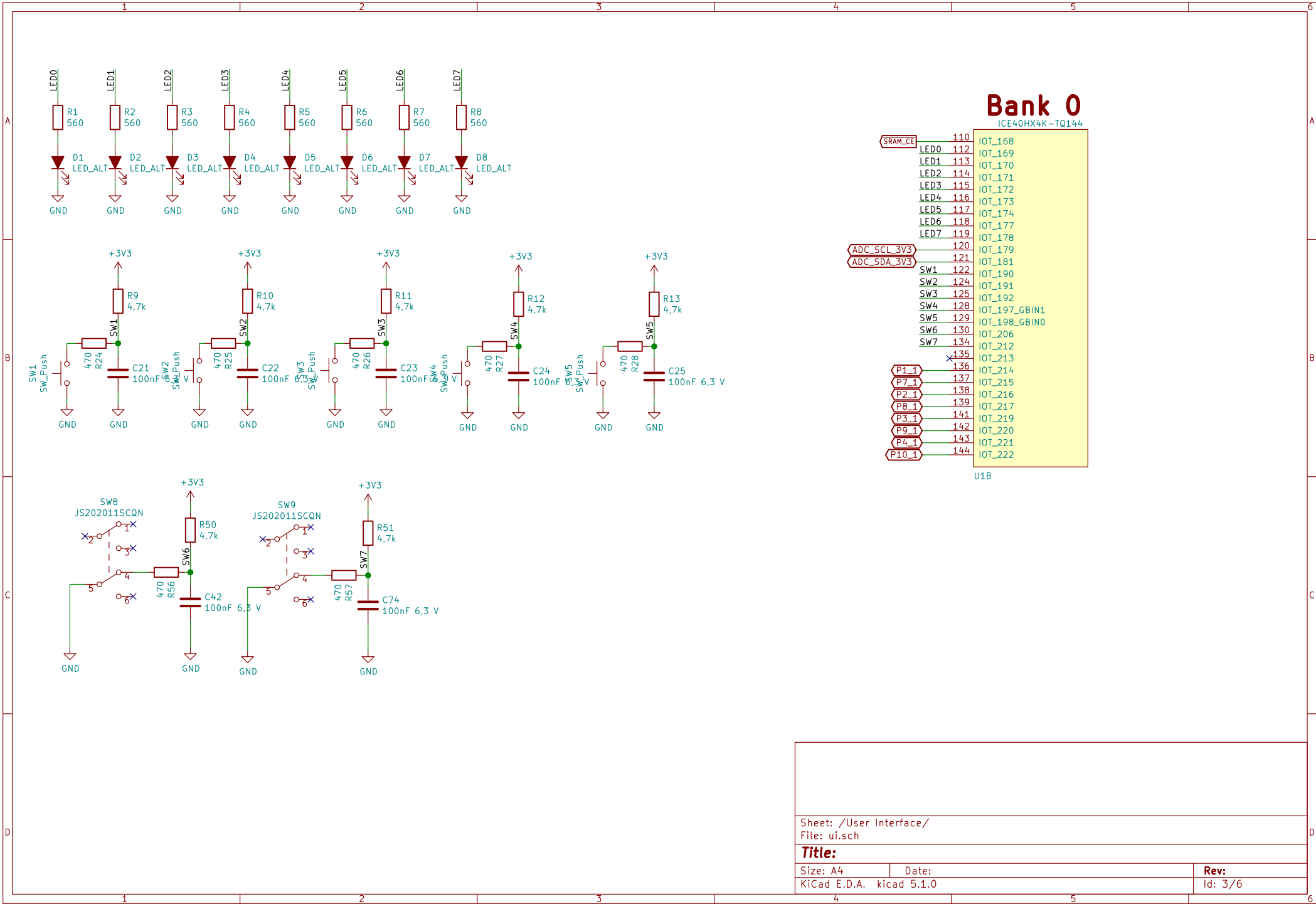
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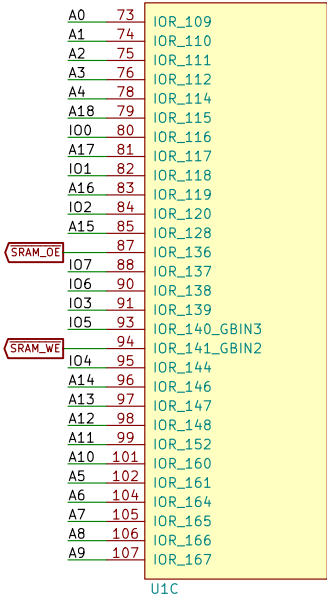


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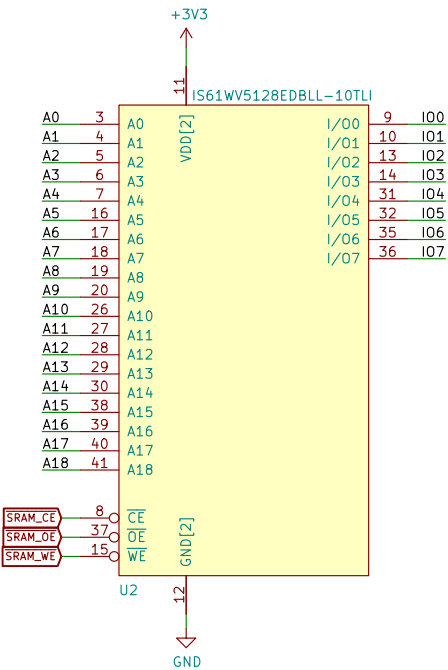


Bank 1

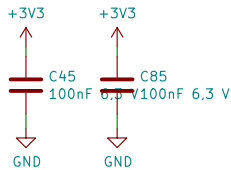
ICE40HX4K-TQ144



U1C



U2



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