Pynqrypt

A FPGA-accelerated encryption library for PYNQ

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FPGA101 - Politecnico di Milano

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The hardware

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Encryption algorithms

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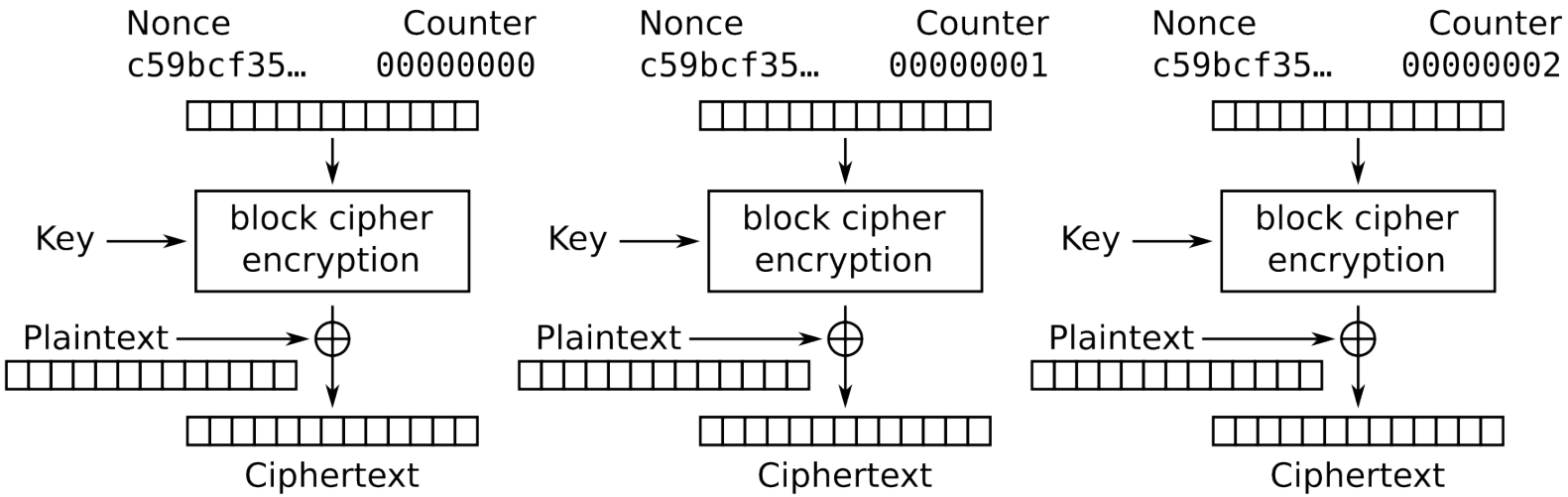
AES-CTR

AES-CTR is a mode of operation for the AES block cipher. It is a highly-parallelizable and efficient encryption algorithm, well suited for hardware acceleration.

AES-CTR



Counter (CTR) mode encryption

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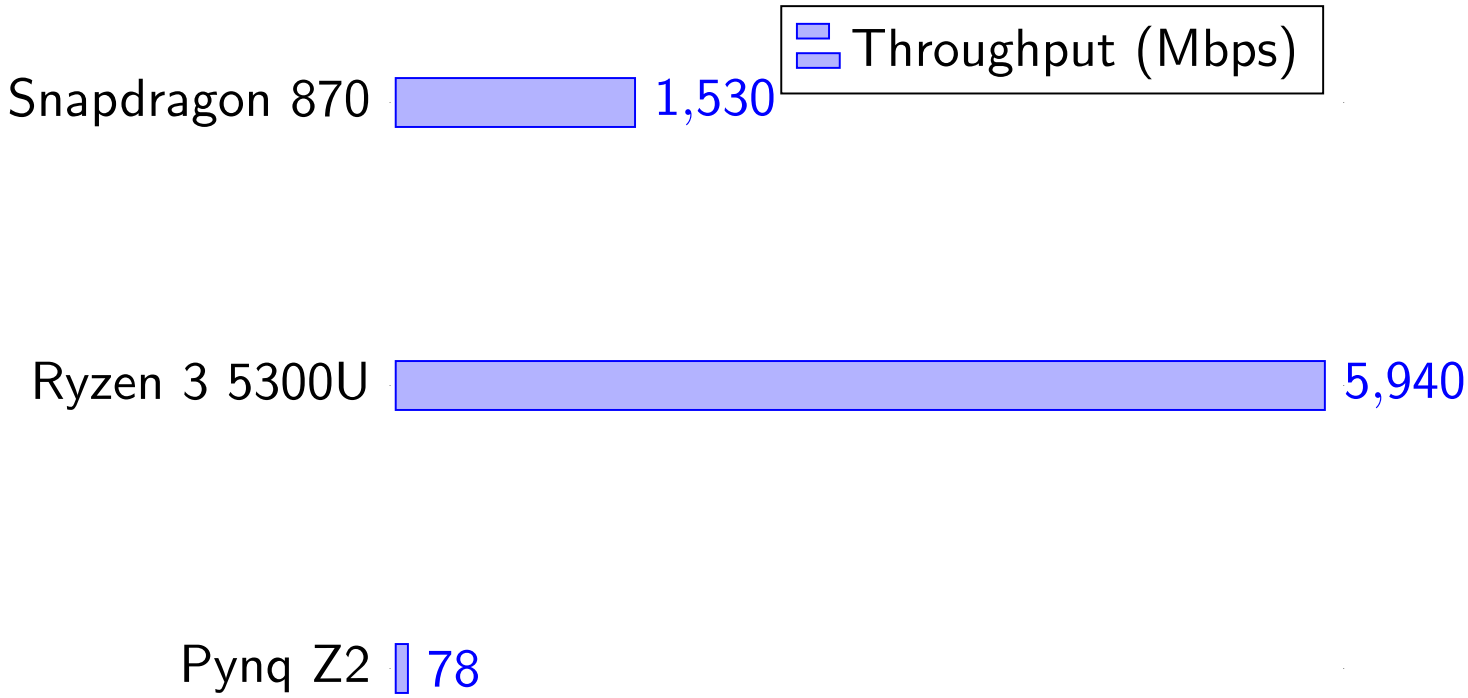

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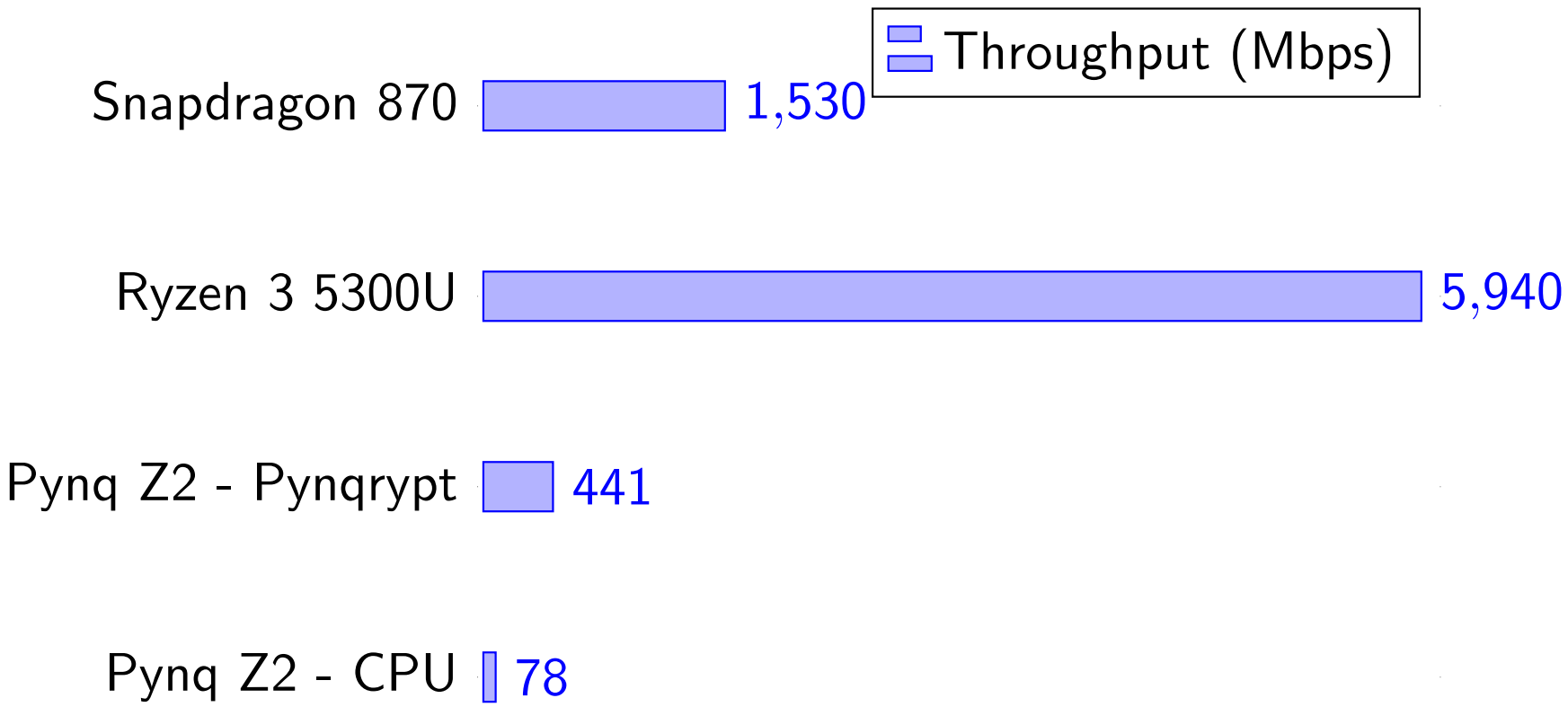

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```
data = np.frombuffer(..., np.uint8)
key = ...
nonce = ...

pynqrypt.set_key(key)
pynqrypt.set_nonce(nonce)
pynqrypt.set_length(len(data))
```

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```
input_buffer = pynqrypt.get_input_array()
output_buffer = pynqrypt.get_output_array()

input_buffer[:] = data[:]

pynqrypt.prepare()
pynqrypt.run_blocking()
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```
output_buffer.invalidate()

... = bytes(output_buffer)

pynqrypt.cleanup()
```

USBCrypt: let's see it in action!

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  Maybe rewatch the lesson?

What did I learn?