Electronics Cheat Sheet

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Miscellaneous

Time Constants

For capacitors
$$V_C = V_s \left[1 - e^{-\frac{t}{RC}}\right]$$
 $V_R = V_s e^{-\frac{t}{RC}}$ And for inductors $V_R = V_s \left[1 - e^{-\frac{tR}{L}}\right]$ $V_L = V_s e^{-\frac{tR}{L}}$

Thévenin

Solve circuit for voltage across port $(V_A - V_B)$, solve for equivalent resistance by replacing batteries with wires and current sources with opens and trace a current path from A to B. Put the voltage source in **series** with the equivalent resistor.

Norton

Short the port and find the current through the port short. Find resistance using the same method as Thévenin. Put the current source in **parallel** (across the port) with the equivalent resistor.

Differentials

$$V_R = IR = R \frac{dQ}{dt}$$

$$V_L = L \frac{dI}{dt} = L \frac{d^2Q}{dt^2}$$

$$V_C = \frac{Q}{C}$$

Diode

$$A - \boxed{p \mid n} - C \quad A - \boxed{\downarrow} C$$

SCR

SCRs act like normal diodes when off and do not conduct when reverse biased. When forward biased the device has two states, one where there is a very high resistance and therefore little current and another

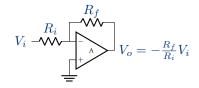
where there is very little reistance in which **Subtractor** the SCR acts like a conventional diode. An SCR is switched from the above "off" state to an "on" state by applying a gate voltage $V_G > V_{trig}$ and only drops back to the off state when $V_G < V_{trig}$ and the current drops below some known holding current (very small)

Triac

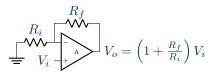
The triac is kind of like an SCR for both parts of an AC wave. It is triggered with $|V_G| > V_{trig}$ and stays on until the voltage drops and $I_{through} < |V_{hold}|$

Op-Amps

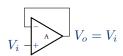
Inverting Amplifier



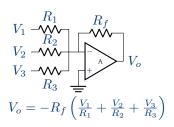
Non-Inverting Amplifier

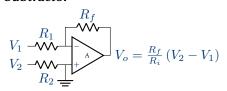


Voltage Follower

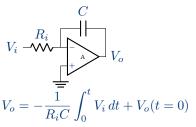


(Inverting) Adder

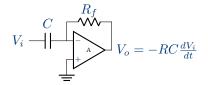




Integrator



Differentiator



Transistors

$$\beta = \frac{I_C}{I_B} \Longrightarrow I_C = \beta I_B \implies I_E = (\beta + 1) I_B$$

V	NPN	PNP
E < B < C	Active	Reverse
E < B > C	Saturation	Cut of f
E > B < C	Cut of f	Saturation
E > B > C	Reverse	Active

Logic

$$\begin{array}{l}
\cdot \Longrightarrow AND \\
+ \Longrightarrow OR \\
\oplus \Longrightarrow XOR \text{ (either !both)} \\
\underline{A + (AB)} = A, \ A(A+B) = A \\
\underline{(A+A)} = \underline{(AA)} = \overline{A} \\
\underline{(A+B)} = \overline{A} \cdot \overline{B} \\
\underline{(AB)} = \overline{A} + \overline{B}
\end{array}$$

Binary

$$1011_2=1\cdot 2^3+0\cdot 2^2+1\cdot 2^1+1\cdot 2^0$$
 (note: leading digit read first), decimals are just done with negative powers of 2

 $11_{10} \rightarrow x_2$ is done by repeatedly dividing by 2 and taking note of the remainders, then reading them backwards $11/2 = 5R1 \rightarrow 5/2 = 2R1 \rightarrow 2/2 =$ $1R0 \to 1/2 = 0R1 \implies 1011$ $0.34_{10} \rightarrow x_2$ here you multiply by two repeatedly, taking note of when you roll over to 1.n, then reading the result forwards. If the number you are trying to convert when expressed as a fraction does not have a denominator of 2^n then the number cannot fully be represented, so stop at some point. $0.34 \cdot 2 = 0 + 0.68 \rightarrow 0.68 \cdot 2 =$ $1 + 0.36 \rightarrow 0.36 \cdot 2 = 0 + 0.72 \rightarrow$

Complex Numbers and AC circuits

 $0.72 \cdot 2 = 1 + 0.44 \implies 0.34_{10} \approx 0.0101$

a + bj can be represented as $\sqrt{a^2+b^2}e^{j\arctan\frac{b}{a}}$

Given two signals their phase difference is

$$\arctan \left[\frac{\Im \left[\frac{\widetilde{V}_2}{\widetilde{V}_1} \right]}{\Re \left[\frac{\widetilde{V}_2}{\widetilde{V}_1} \right]} \right]$$

$$\widetilde{Z}_R = R, \widetilde{Z}_C = \frac{1}{j\omega C}, \widetilde{Z}_L = j\omega L$$

$$\langle P \rangle = \frac{1}{2} \Re \left[\widetilde{V}^*(t) \widetilde{I}(t) \right] =$$

$$\frac{1}{2} \Re \left[\widetilde{V}(t) \widetilde{I}^*(t) \right] = \frac{1}{2} \widetilde{I}^*(t) \widetilde{I}(t) \Re \left[\widetilde{Z} \right] =$$

$$\frac{1}{2} \frac{\widetilde{V}^*(t) \widetilde{V}(t)}{\Re \left[\widetilde{Z} \right]}$$

Impedance adds like resistance

Power gain in dB is $10 \log_{10} \left| \frac{P_{out}}{P_{in}} \right|$ and in

voltage
$$20 \log_{10} \left| \frac{V_{out}}{V_{in}} \right|$$

Cutoff is when $\left| \frac{V_{out}}{V_{in}} \right| = \frac{1}{\sqrt{2}}$

Transducers

Thermocouples operate with "hot" (measured) and "cold" (reference) junctions. The measured voltage of a thermocouple is given by $V_{meas} = V_{hot} - V_{cold}$