

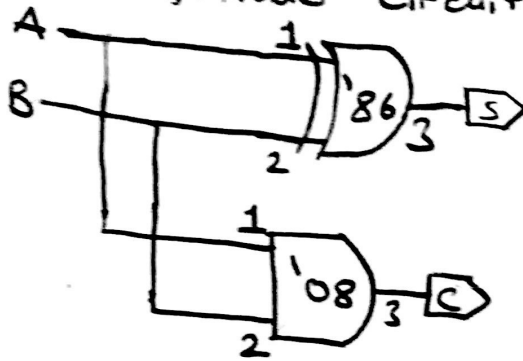
Preliminary Work - Lab 01

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CS-223 sec.05

* Half-Adder Circuit Schematics



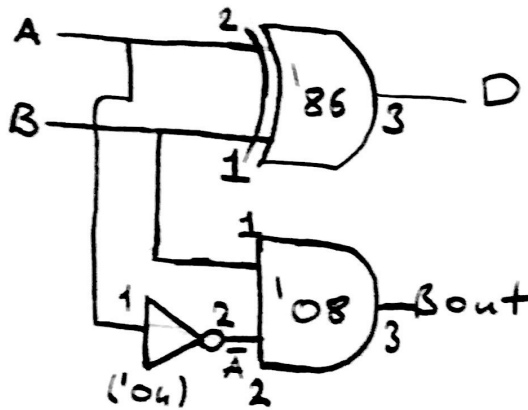
* 74HC86
GND(0V) - 7
V_{CC}(+5V) - 14

* 74HC08
GND(0V) - 7
V_{CC}(+5V) - 14

IC List

- ① One IC-7408 Quad 2-input AND Gate
- ② One IC-7486 Quad 2-input X-OR Gate

* Half-Subtractor Circuit Schematics:



IC List

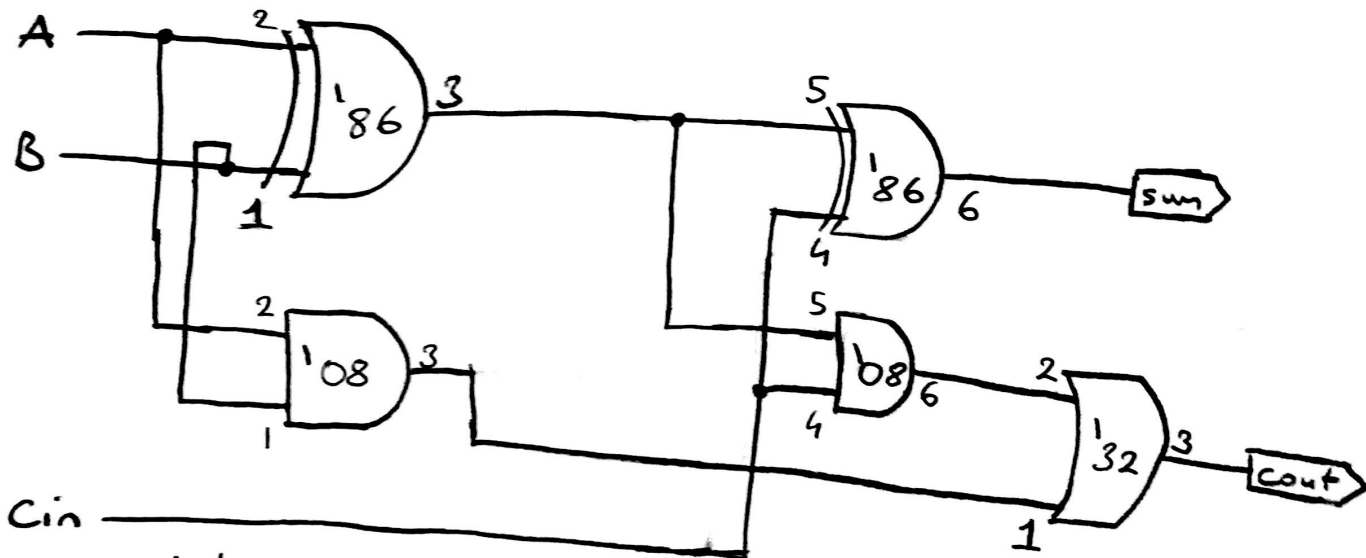
- ① One IC-7486 Quad 2-input XOR Gate
- ② One IC-7408 Quad 2-input AND Gate
- ③ One IC-7404 Inverter Gate

* 74HC86
GND(0V) - 7
V_{CC}(+5V) - 14

* 74HC08
GND(0V) - 7
V_{CC}(+5V) - 14

* 74LS04
GND(0V) - 7
V_{CC}(+5V) - 14

* Full-Adder Circuit Schematics:



IC List

- ① One IC-7486 X-OR Gate
- ② One IC-7408 AND Gate
- ③ One IC-7432 OR Gate

* 74HC86
GND(0V) - 7
V_{CC}(+5V) - 14

* 74HC08
GND(0V) - 7
V_{CC}(+5V) - 14

* 74HC32
GND(0V) - 7
V_{CC}(+5V) - 14

