

Change Order Form (COF) <small>Rev 1</small>	
Date	11/1/2020
Subsystem	1.3 (Audio Input Circuit)
Component	1.3 (Audio Input Circuit)
Change	Will remove audio input circuit all together.
Reasoning	After consulting with our mentors, we realized that the process of recovering the clock and data from an AFSK signal using solely digital logic was quite difficult and they overlooked this when assigning the task.
Rationale	Nolan is providing a modem board that will simply output binary values for us to interpret the data and clock from. We will complete our project utilizing this board but will lay a foundation for the C.A.P.E. research group to build on to achieve a minimal hardware solution for recovering the data in the future.
Mod. Details	Remove amplifier and filtering circuit from the design and will now utilize a modem board provided by Nolan.