

Weekly Status Report

Week 1

EECE 443

MCU TNC Design

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Accomplished tasks for this week:

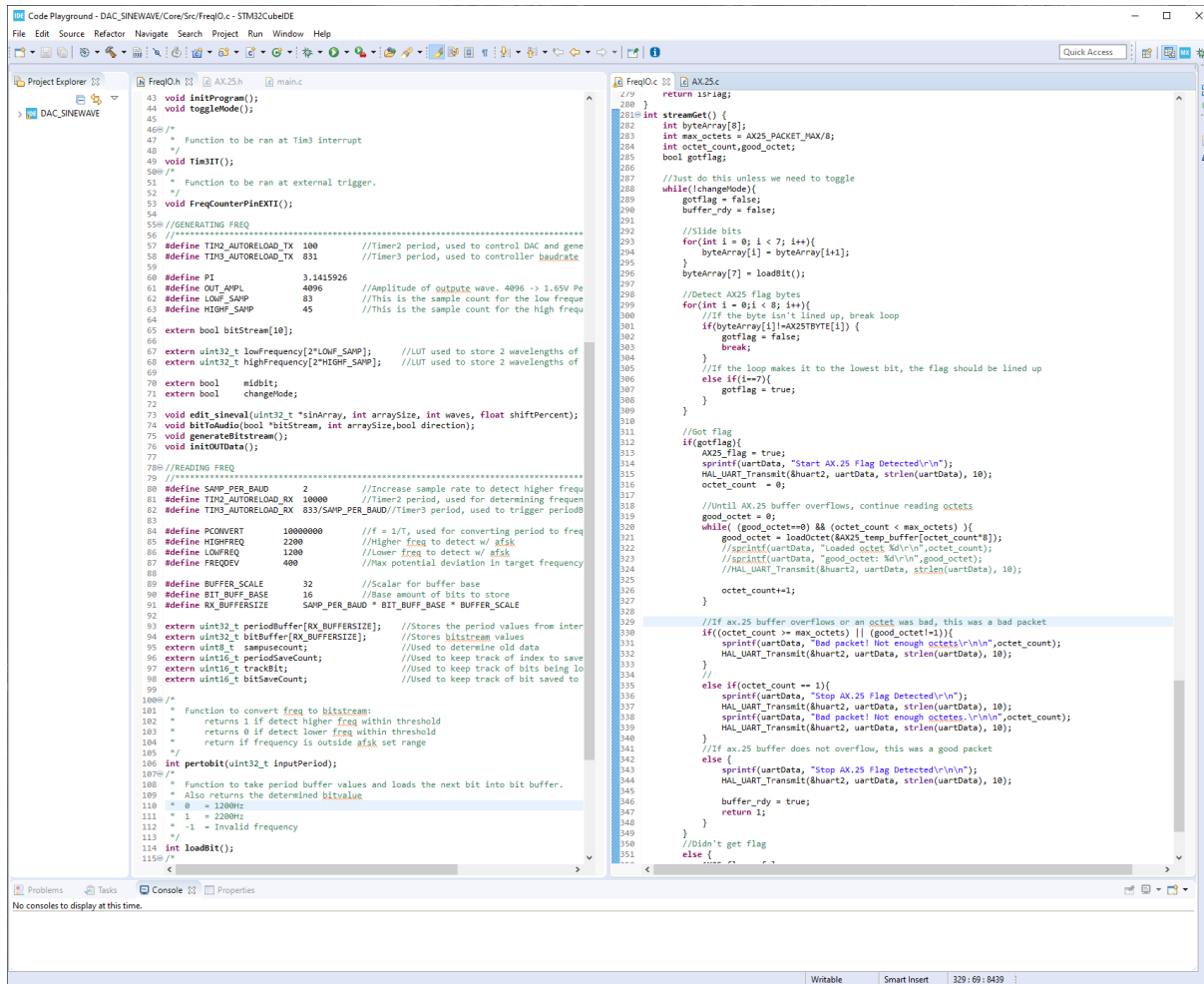
- Create reference experiment document
- Create code to read bits from incoming bitstream of data
- Create code to receive packets of AX.25 and store them into a buffer
- Create code to generate and output an AFSK signal by incrementing through a Boolean array
- Ordered prototyping boards for creating our circuits to connect to the TNC

Planned tasks for next week:

- Fix the AFSK generation code due to phase errors in wave shape
- Run thorough testing on code subsystems and record results with the newly created experimenting sheet
- Flesh out more of the AX.25 protocol interpretation
- Pass Kobe some of the perfboards so he can build the input circuit and PTT circuit

Examples of Completed Task

Example of some code to read in bitstreams:



```
43 void initProgram();
44 void toggleMode();
45
46//
47 * Function to be ran at Tim3 interrupt
48 */
49 void Tim3IT();
50//
51 * Function to be ran at external trigger.
52 */
53 void FreqCounterPinEXTI();
54
55//GENERATING FREQ
56 //*****
57 #define TIM2_AUTORELOAD_TX 100 //Timer2 period, used to control DAC and gene
58 #define TIM3_AUTORELOAD_TX 831 //Timer3 period, used to controller baudRate
59
60 #define P1 3.1415926
61 #define OUT_AMPL 4096 //Amplitude of output wave. 4096 -> 1.65V Pe
62 #define LOWF_SAMP 83 //This is the sample count for the low freq
63 #define HIGHF_SAMP 45 //This is the sample count for the high freq
64
65 extern bool bitStream[10];
66
67 extern uint32_t lowFrequency[2*LOWF_SAMP]; //LUT used to store 2 wavelengths of
68 extern uint32_t highFrequency[2*HIGHF_SAMP]; //LUT used to store 2 wavelengths of
69
70 extern bool midbit;
71 extern bool changeMode;
72
73 void edit_sineval(uint32_t *sinArray, int arraySize, int waves, float shiftPercent);
74 void bitLoadaio(bool *bitStream, int arraySize, bool direction);
75 void generateBitstream();
76 void initOUTData();
77
78//READING FREQ
79 //*****
80 #define SAMP_PER_BAUD 2 //Increase sample rate to detect higher freq
81 #define TIM2_AUTORELOAD_RX 10000 //Timer2 period, used for determining frequen
82 #define TIM3_AUTORELOAD_RX 833/SAMP_PER_BAUD//Timer3 period, used to trigger periodb
83
84 #define PCONVERT 10000000 //T = 1/T, used for converting period to freq
85 #define HIGHFREQ 2200 //Higher freq to detect w/ afsk
86 #define LOWFREQ 1200 //Lower freq to detect w/ afsk
87 #define FREQDEV 400 //Max potential deviation in target frequency
88
89 #define BUFFER_SCALE 32 //Scalar for buffer base
90 #define BIT_BUFF_BASE 16 //Base amount of bits to store
91 #define RX_BUFFER_SIZE SAMP_PER_BAUD * BIT_BUFF_BASE * BUFFER_SCALE
92
93 extern uint32_t periodBuffer[RX_BUFFER_SIZE]; //Stores the period values from inter
94 extern uint32_t bitBuffer[RX_BUFFER_SIZE]; //Stores bitstream values
95 extern uint8_t sampuseCount; //Used to determine old data
96 extern uint16_t periodSaveCount; //Used to keep track of index to save
97 extern uint16_t trackBit; //Used to keep track of bits being lo
98 extern uint16_t bitSaveCount; //Used to keep track of bit saved to
99
100//
101 * Function to convert freq to bitstream:
102 * returns 1 if detect higher freq within threshold
103 * returns 0 if detect lower freq within threshold
104 * return if frequency is outside afsk set range
105 */
106 int pertobit(uint32_t inputPeriod);
107//
108 * Function to take period buffer values and loads the next bit into bit buffer.
109 * Also returns the determined bitvalue
110 * 0 = 1200Hz
111 * 1 = 2200Hz
112 * -1 = Invalid frequency
113 */
114 int loadBit();
115//
```

```
279
280 return 157*lag;
281 }
282
283 int streamGet() {
284 int byteArray[8];
285 int max_octets = AX25_PACKET_MAX/8;
286 int octet_count, good_octet;
287 bool gotflag;
288
289 //Just do this unless we need to toggle
290 while(!changeMode){
291 gotflag = false;
292 buffer_rdy = false;
293
294 //Slide bits
295 for(int i = 0; i < 7; i++){
296 byteArray[i] = byteArray[i+1];
297 }
298 byteArray[7] = loadBit();
299
300 //Detect AX25 flag bytes
301 for(int i = 0; i < 8; i++){
302 if(byteArray[i] == 0x25){
303 gotflag = false;
304 break;
305 }
306 //If the loop makes it to the lowest bit, the flag should be lined up
307 else if(i==7){
308 gotflag = true;
309 }
310 }
311
312 //Got flag
313 if(gotflag){
314 AX25_flag = true;
315 sprintf(uartData, "Start AX.25 Flag Detected\r\n");
316 HAL_UART_Transmit(&uart2, uartData, strlen(uartData), 10);
317 octet_count = 0;
318
319 //Until AX.25 buffer overflows, continue reading octets
320 good_octet = 0;
321 while( (good_octet==0) && (octet_count < max_octets) ){
322 good_octet = loadOctet(&AX25_temp_buffer[octet_count*8]);
323 //sprintf(uartData, "Loaded octet %d\r\n", octet_count);
324 //sprintf(uartData, "good octet: %d\r\n", good_octet);
325 //HAL_UART_Transmit(&uart2, uartData, strlen(uartData), 10);
326 octet_count++;
327 }
328
329 //If ax.25 buffer overflows or an octet was bad, this was a bad packet
330 if((octet_count >= max_octets) || (good_octet!=1)){
331 sprintf(uartData, "Bad packet! Not enough octets\r\n\r\n", octet_count);
332 HAL_UART_Transmit(&uart2, uartData, strlen(uartData), 10);
333 }
334 //
335 else if(octet_count == 1){
336 sprintf(uartData, "Stop AX.25 Flag Detected\r\n");
337 HAL_UART_Transmit(&uart2, uartData, strlen(uartData), 10);
338 sprintf(uartData, "Bad packet! Not enough octets\r\n\r\n", octet_count);
339 HAL_UART_Transmit(&uart2, uartData, strlen(uartData), 10);
340 }
341 //If ax.25 buffer does not overflow, this was a good packet
342 else {
343 sprintf(uartData, "Stop AX.25 Flag Detected\r\n\r\n");
344 HAL_UART_Transmit(&uart2, uartData, strlen(uartData), 10);
345 buffer_rdy = true;
346 return 1;
347 }
348 }
349 //Didn't get flag
350 else {
351 ...
352 }
```

Serial Output from controller reading an incoming bitstream:

```
Start AX.25 Flag Detected
Stop AX.25 Flag Detected
Bad packet! Not enough octetes.

Start AX.25 Flag Detected
Stop AX.25 Flag Detected
Bad packet! Not enough octetes.

Start AX.25 Flag Detected
Printing octet = 0 1 0 1 0 1 0 1
Printing octet = 1 0 0 0 0 0 0 1
Printing octet = 0 0 0 0 0 0 0 0
Stop AX.25 Flag Detected

Running streamGet() 3 time
Start AX.25 Flag Detected
Printing octet = 0 1 0 1 0 1 0 1
Printing octet = 1 0 0 0 0 0 0 1
Printing octet = 0 0 0 0 0 0 0 0
Stop AX.25 Flag Detected

Running streamGet() 4 time
Start AX.25 Flag Detected
Printing octet = 0 1 0 1 0 1 0 1
Printing octet = 1 0 0 0 0 0 0 1
Printing octet = 0 0 0 0 0 0 0 0
Stop AX.25 Flag Detected

Running streamGet() 5 time
Start AX.25 Flag Detected
Printing octet = 0 1 0 1 0 1 0 1
Printing octet = 1 0 0 0 0 0 0 1
Printing octet = 0 0 0 0 0 0 0 0
Stop AX.25 Flag Detected

Running streamGet() 6 time
Start AX.25 Flag Detected
Printing octet = 0 1 0 1 0 1 0 1
Printing octet = 1 0 0 0 0 0 0 1
Printing octet = 0 0 0 0 0 0 0 0
Stop AX.25 Flag Detected

Running streamGet() 7 time
Start AX.25 Flag Detected
Printing octet = 0 1 0 1 0 1 0 1
Printing octet = 1 0 0 0 0 0 0 1
Printing octet = 0 0 0 0 0 0 0 0
Stop AX.25 Flag Detected

Running streamGet() 8 time
Start AX.25 Flag Detected
Printing octet = 0 1 0 1 0 1 0 1
Printing octet = 1 0 0 0 0 0 0 1
Printing octet = 0 0 0 0 0 0 0 0
Stop AX.25 Flag Detected

Running streamGet() 9 time
Start AX.25 Flag Detected
Printing octet = 0 1 0 1 0 1 0 1
Printing octet = 1 0 0 0 0 0 0 1
Printing octet = 0 0 0 0 0 0 0 0
Stop AX.25 Flag Detected

Port closed
```

Experiment Sheet:

TNC TESTING FORM _(REV1)	
Leaf on the Tree:	
Device Under Test (Testing Tree Number):	
Date:	
Person(s) Conducting Experiment:	
Signature:	
Experiment Purpose:	
Experiment Procedure:	
ent Settings/Software Settings (w Revision):	
Testing Diagram/Picture:	
Data Points:	
Pass/Fail:	
Interpreted Notes:	
Recommendations for Modification:	

Prototype Board Order Form:

Your order's in!

You should get it by Oct 1.

Order total: \$7.49

Shipping to:

Order number: 24-05773-92472



10X Double Side 5x7cm PCB Strip board Printed Circuit Pro...

You should get this by Oct 1.

[See order details](#)

Time Sheet

Item	Date/Time	Description	Hours
1	9/14/2020 5:30pm-7pm	Begin creating experiment sheet by overlooking sheet provided by Dr. Darby.	1.5
2	9/14/2020 3:30pm-6pm	Use pre-written bitLoading operation to create function for converting an AFSK digital bitstream based on AX.25 flag detection.	2.5
3	9/16/2020 5:30-7pm	Using the previous digital bitstream conversion software, implement functional logic to create an AX.25 packet.	1.5
4	2/16/2020 12pm-3pm	Create software to output an AFSK bitstream. This was done by storing the bitstream as an array of Booleans and instruct the DAC to output a sine.	3
5	2/17/2020 12pm-1pm	Spend a bit of time looking for good prototyping boards to assemble the circuits on and connect to our microcontroller.	1
Total:			8.5