Weekly Status Report Week 4

EECE 443
MCU TNC Design

David Cain C00043561

Submittal Date: March 19th, 2020

Accomplished tasks for this week:

- Recreated Level 0 Diagram
- Created Level 1 Diagram
- Corrected Gantt Chart
- Create/Simulate PTT Circuit
- Create/Simulate Audio Output Circuit
- Figured out how Schmitt Triggers are built into the digital GPIO of STM32

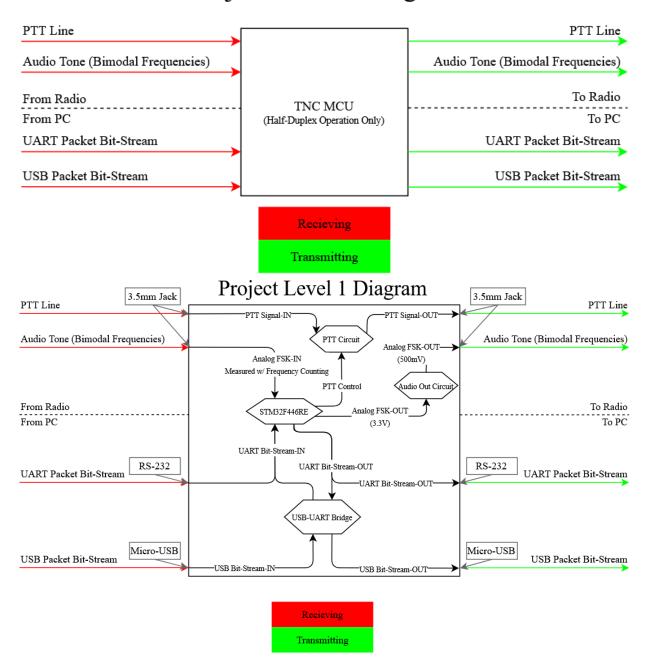
Planned tasks for next week:

- Begin discussing software methodology
- When team receives hardware from mentors, meet with them to ensure we're comfortable with coding in the STM32CubeIDE

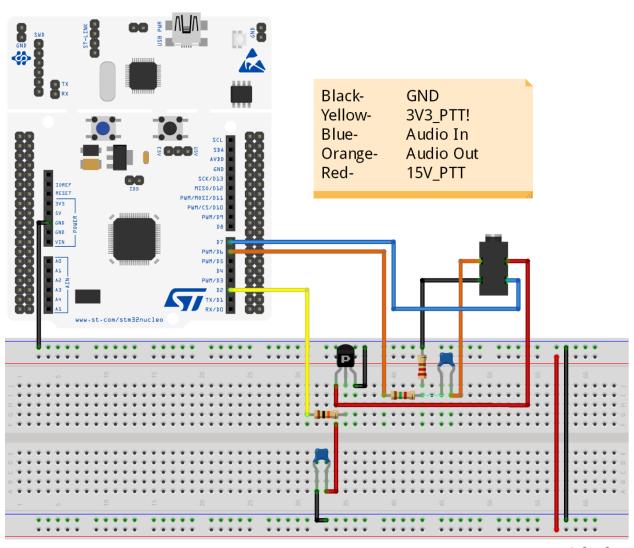
Examples of Completed Task

Level 0/1 Diagram:

Project Level 0 Diagram



Breadboard Fritzing Schematic:



fritzing

Time Sheet

Item	Date/Time	Description	Hours
1	3/17/2020 4pm-7pm	Create audio and PTT circuit. This consisted of figuring out how these systems should I/O then design/simulate. This also consisted of presenting the circuits in a novice friendly way using the Fritzing program.	3
2	3/18/2020 5pm-7pm	Recreate Level 0 then Level 1 diagram. This was done using a draw.io program. At the same time, I also corrected out file structures to have easy access to our simulation links and general structure maintenance.	2
3	3/18/2020 7pm-11pm	Work on putting together assignment 2 with team. This consisted of taking the information we gathered through the week and putting it into necessary formats for assignment 2.	4
Total:			9