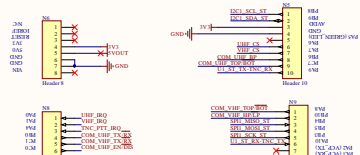


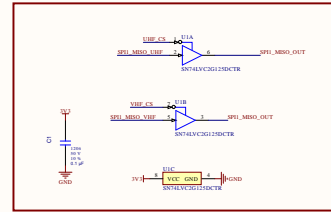
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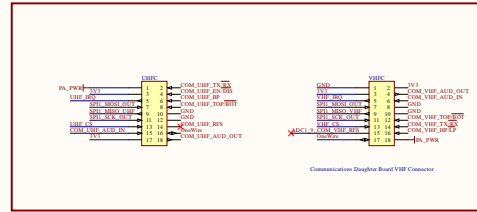
STM32NUCLEO-L476



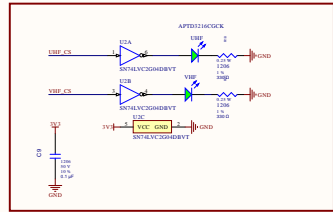
MISO Select Buffer



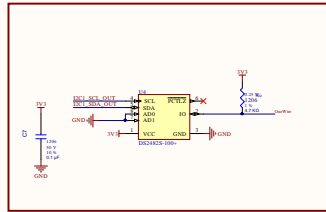
CAPE-3 Radio Board



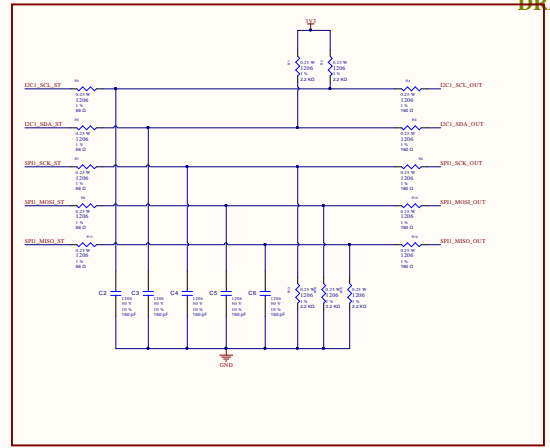
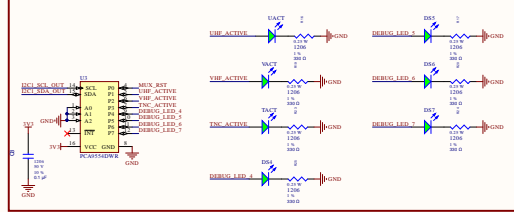
SPI LEDs



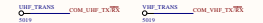
OneWire to I2C Master



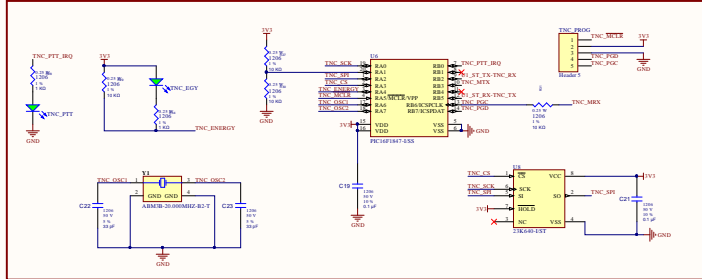
Debug LEDs



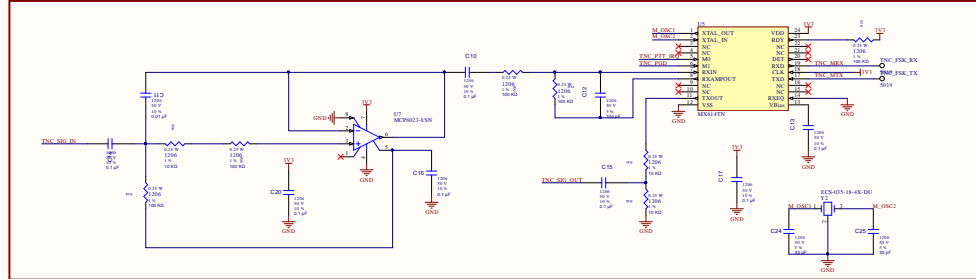
Interface Filtering



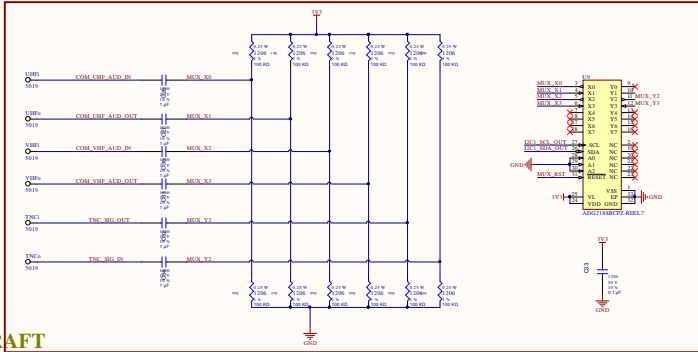
TNC MCU



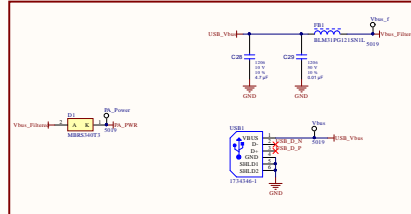
FSK Modem/Amplifier



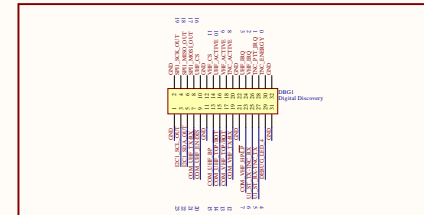
Analog Multiplexer



USB Power Input



Logic Analyzer



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Title: Caion Advanced Picosatellite Experiment - CAPE-3 - AX5043 Nucleo Interface Board - V1R0				Author: Caion
Number: SCH-CAPE-Nucleo-Interface-V1R0SchDoc	Version: 1	Revision: 0	Drawn: Walter Edwards	Date: 09/05/2019
Sheet: 1	Of: 1	Approved: 1	Checked: 1	Released: 1

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