

FQP6N60C/FQPF6N60C

600V N-Channel MOSFET

General Description

These N-Channel enhancement mode power field effect transistors are produced using Fairchild's proprietary, planar stripe, DMOS technology.

This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switched mode power supplies, active power factor correction, electronic lamp ballasts based on half bridge topology.

Features

- 5.5A, 600V, $R_{DS(on)}$ = 2.0 Ω @V_{GS} = 10 V Low gate charge (typical 16 nC)
- Low Crss (typical 7 pF)
- Fast switching
- · 100% avalanche tested
- · Improved dv/dt capability



Absolute Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Parameter		FQP6N60C	FQPF6N60C	Units
V_{DSS}	Drain-Source Voltage		600		V
I _D	Drain Current - Continuous (T _C = 25°C)		5.5	5.5 *	Α
	- Continuous (T _C = 100°C)		3.3	3.3 *	Α
I _{DM}	Drain Current - Pulsed	(Note 1)	22	22 *	Α
V _{GSS}	Gate-Source Voltage		± 30		V
E _{AS}	Single Pulsed Avalanche Energy (N		300		mJ
I _{AR}	Avalanche Current	(Note 1)	5.5		Α
E _{AR}	Repetitive Avalanche Energy (Note 1)		12.5		mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		4.5		V/ns
P_D	Power Dissipation (T _C = 25°C)		125	40	W
	- Derate above 25°C		1.0	0.31	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150		°C
T _L	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds		300		°C

^{*} Drain current limited by maximum junction temperature.

Thermal Characteristics

Symbol	Parameter	FQP6N60C	FQPF6N60C	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	1.0	3.2	°C/W
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink Typ.	0.5		°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62.5	62.5	°C/W

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Cha	aracteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$				V
ΔBV _{DSS} / ΔΤ _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C		0.6		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 600 V, V _{GS} = 0 V			1	μΑ
		V _{DS} = 480 V, T _C = 125°C			10	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30 V, V _{DS} = 0 V			-100	nA
On Cha	racteristics					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$	2.0		4.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 2.75 A		1.7	2.0	Ω
9 _{FS}	Forward Transconductance	V _{DS} = 40 V, I _D = 2.75 A (Note 4)		4.8		S
	ic Characteristics	I	1 1			_
C _{iss}	Input Capacitance	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$		620	810	pF
Coss	Output Capacitance	f = 1.0 MHz		65	85	pF
C _{rss}	Reverse Transfer Capacitance			7	10	pF
Switchi	ing Characteristics					
$t_{d(on)}$	Turn-On Delay Time	V _{DD} = 300 V, I _D = 5.5A,		15	40	ns
t _r	Turn-On Rise Time	$R_G = 25 \Omega$		45	100	ns
t _{d(off)}	Turn-Off Delay Time			45	100	ns
t _f	Turn-Off Fall Time	(Note 4, 5)		45	100	ns
Q_g	Total Gate Charge	V _{DS} = 480 V, I _D = 5.5A,		16	20	nC
Q_{gs}	Gate-Source Charge	V _{GS} = 10 V		3.5		nC
Q_{gd}	Gate-Drain Charge	(Note 4, 5)		6.5		nC
Drain-S	Source Diode Characteristics ar	nd Maximum Ratings				
I _S	Maximum Continuous Drain-Source Diode Forward Current				5.5	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current				22	Α
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 5.5 A			1.4	V
t _{rr}	Reverse Recovery Time	$V_{GS} = 0 \text{ V}, I_S = 5.5 \text{ A},$		310		ns
Q _{rr}	Reverse Recovery Charge	$dI_F / dt = 100 A/\mu s$ (Note 4)		2.1		μС

- Notes:
 1. Repetitive Rating : Pulse width limited by maximum junction temperature 2. L = 18.2mH, I_{AS} = 5.5 A, V_{DD} = 50V, R_{G} = 25 Ω, Starting T_{J} = 25°C 3. I_{SD} ≤ 5.5A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_{J} = 25°C 4. Pulse Test : Pulse width ≤ 300μs, Duty cycle ≤ 2% 5. Essentially independent of operating temperature

Typical Characteristics Typical Characteristics (Continued)

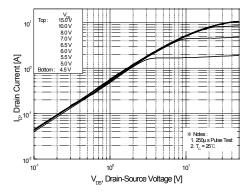


Figure 1. On-Region Characteristics

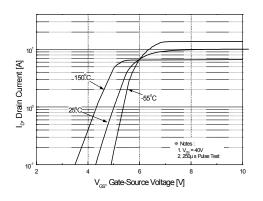


Figure 2. Transfer Characteristics

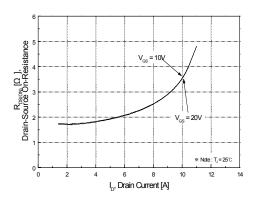


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

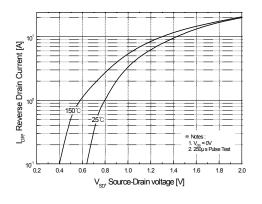


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

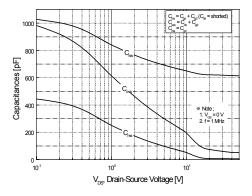


Figure 5. Capacitance Characteristics

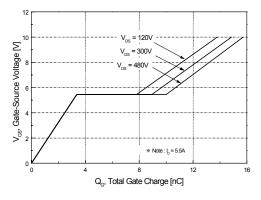


Figure 6. Gate Charge Characteristics

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Typical Characteristics (Continued)

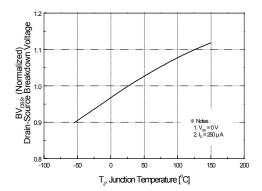


Figure 7. Breakdown Voltage Variation vs Temperature

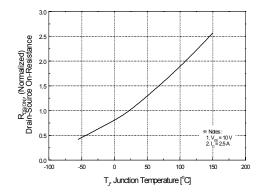


Figure 8. On-Resistance Variation vs Temperature

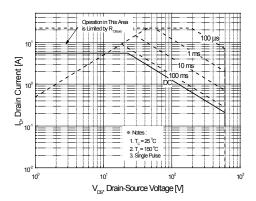


Figure 9-1. Maximum Safe Operating Area for FQP6N60C

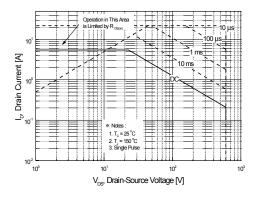


Figure 9-2. Maximum Safe Operating Area for FQPF6N60C

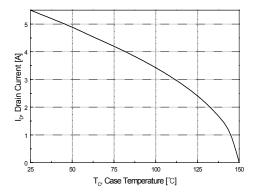


Figure 10. Maximum Drain Current vs Case Temperature

Typical Characteristics (Continued)

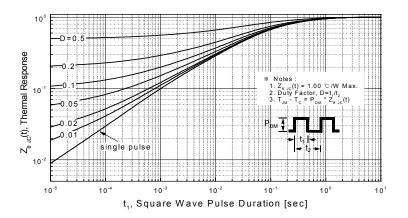


Figure 11-1. Transient Thermal Response Curve for FQP6N60C

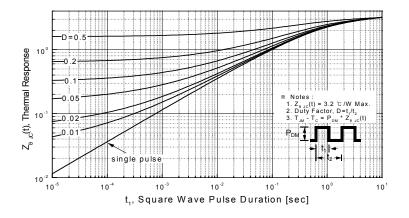
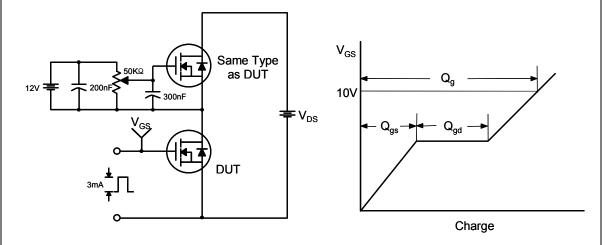


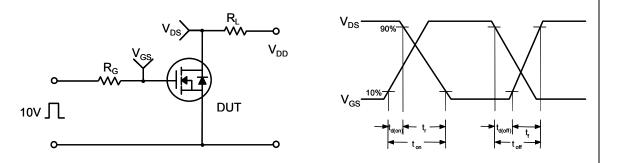
Figure 11-2. Transient Thermal Response Curve for FQPF6N60C

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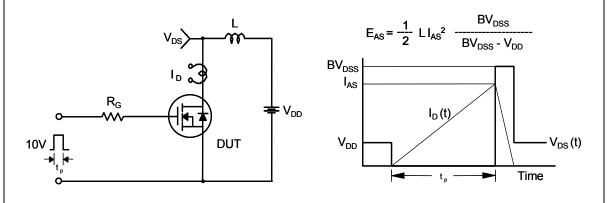
Gate Charge Test Circuit & Waveform



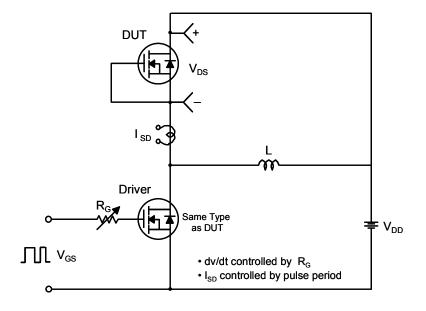
Resistive Switching Test Circuit & Waveforms

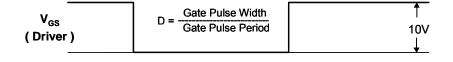


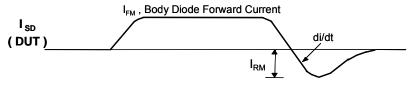
Unclamped Inductive Switching Test Circuit & Waveforms



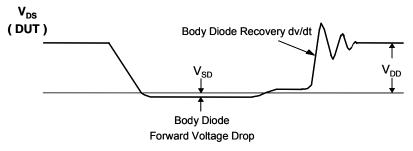
Peak Diode Recovery dv/dt Test Circuit & Waveforms

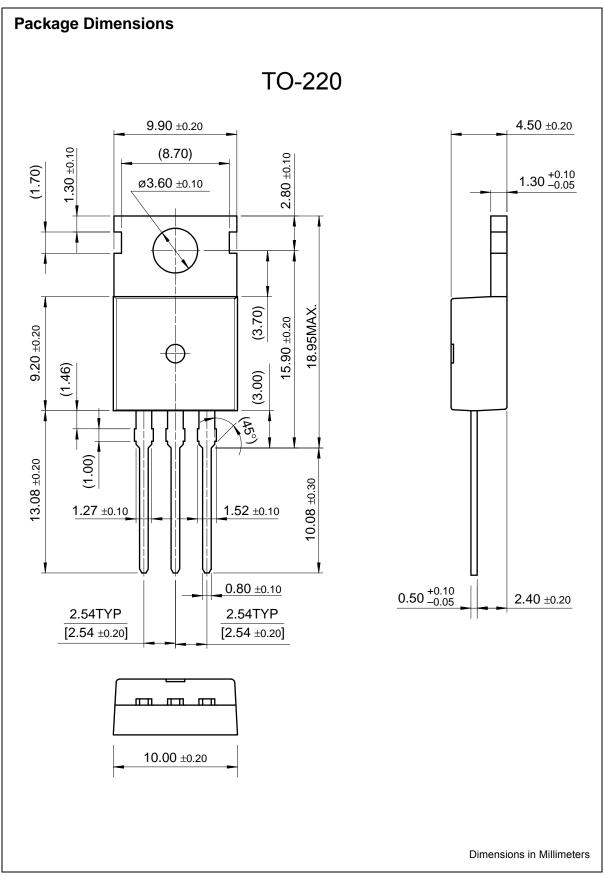


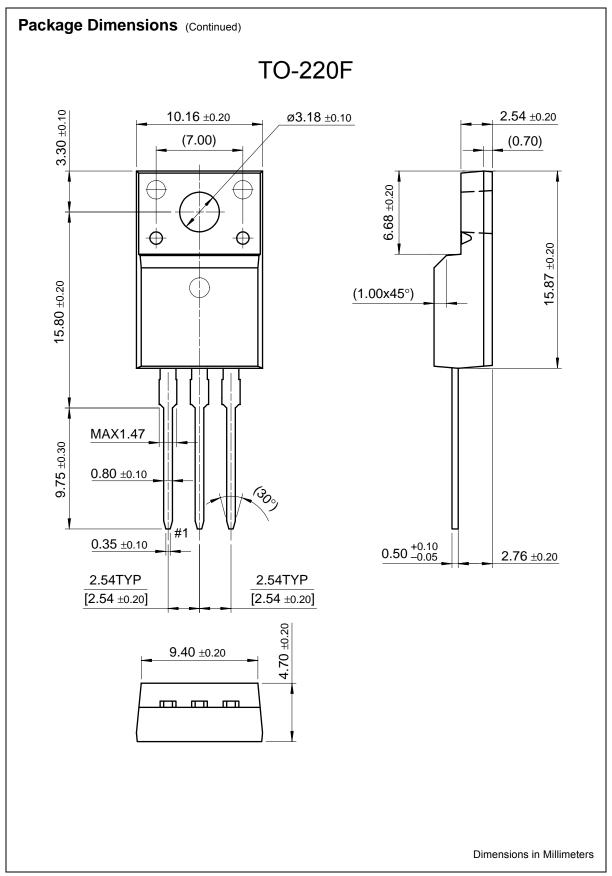




Body Diode Reverse Current







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