

[Dashboard](#) / [Courses](#) / [Winter 2021-22](#) / [BTech Semester 4](#) / [CS268](#) / [Assignment 7 22-04-2022](#) / [Assignment 7 22-04-2022](#)

Started on Friday, 22 April 2022, 1:50 PM

State Finished

Completed on Friday, 22 April 2022, 1:55 PM

Time taken 4 mins 33 secs

Marks 4.00/4.00

Grade **10.00** out of 10.00 (**100%**)

Question **1**

Complete

Mark 1.00 out of 1.00

if we run the following program on the pipeline to execute it. Select the correct option.

Program:

start: ADD R2, R0, R1

stall

ADD R4, R2, R3

stall

stall

.end start

- ☒ a. Two stalls are required between first ADD instruction and second ADD instruction
- ☐ b. Three stalls are required between first ADD instruction and second ADD instruction
- ☐ c. one stalls is required between first ADD instruction and second ADD instruction
- ☐ d. Noe of the mentione.

Question 2

Complete

Mark 1.00 out of 1.00

If we would like to execute the following incomplete program by 5-stage pipeline simulator then error will be there, choose the correct option to complete the program for smoothly execution of it.

Code/program:

```
#include <iregdef.h>

.set noreorder

.text

.globl start

.ent start
start: add t2, t0, t1
nop
nop
```

- ☐ a. nop
nop
.end start
- ☐ b. add t4, t2, t3
nop
.end start
- ☐ c. add t4, t2, t3
nop
nop
- ☒ d. add t4, t2, t3
nop
nop
.end start

Question 3

Complete

Mark 1.00 out of 1.00

Software pipeline is the example of ?

- ☐ a. Forwarding
- ☐ b. Bypassing
- ☒ c. Instruction level parallelism
- ☐ d. Register renaming

Question 4

Complete

Mark 1.00 out of 1.00

Instruction level parallelism can be done?

- ☒ a. All of the mentioned
- ☐ b. Loop unrolling
- ☐ c. Code restructuring
- ☐ d. with smart compiler

◀ CS-268 II 15% Mid Term Online Test II 09-04-2022

Jump to...

[Assignment 8_22-04-2022 ▶](#)