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Started on Wednesday, 6 April 2022, 12:10 PM

State Finished

Completed on Wednesday, 6 April 2022, 12:14 PM

Time taken 4 mins 14 secs

Marks 3.00/4.00

Grade 7.50 out of 10.00 (75%)

Question **1**

Complete

Mark 1.00 out of 1.00

Dynamic pipeline scheduling chooses which instructions to execute next, possibly reordering them to avoid stalls?

- ☐ a. Branch prediction
- ☒ b. Dynamic pipeline
- ☐ c. Prediction branch
- ☐ d. Dynamic branch prediction

Question **2**

Complete

Mark 0.00 out of 1.00

For Dynamic Scheduling, select the correct option.

- ☐ a. The hardware determines the order in which instructions execute
- ☐ b. The DMA determines the order in which instructions execute
- ☐ c. The software determines the order in which instructions execute
- ☒ d. None of the mentioned

Question **3**

Complete

Mark 1.00 out of 1.00

Consider the following code:

CODE:

Load R1,Loc1; Load R1 from memory location Loc1

Load R2,Loc2; Load R2 from memory location Loc2

Add R1,R2,R1; Add R1 and R2 and save result in R1

Dec R2; Decrement R2

Dec R1; Decrement R1

Mpy R1,R2,R3; Multiply R1 and R2 and store in R3

Store R3, Loc3; Store r3 in Memory Location Loc3

What is the number of cycles needed to execute the above code assuming each instruction takes one cycle to execute?

- ☒ a. 10
- ☐ b. 9
- ☐ c. 5
- ☐ d. 7

Question **4**

Complete

Mark 1.00 out of 1.00

For the Software Pipelining, select the correct option.

- ☐ a. select order of instructions from different iterations to pipeline
- ☒ b. All of the mentioned
- ☐ c. unroll loop body with an unroll factor of n
- ☐ d. "paste" instructions from different iterations into the new pipelined loop body

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