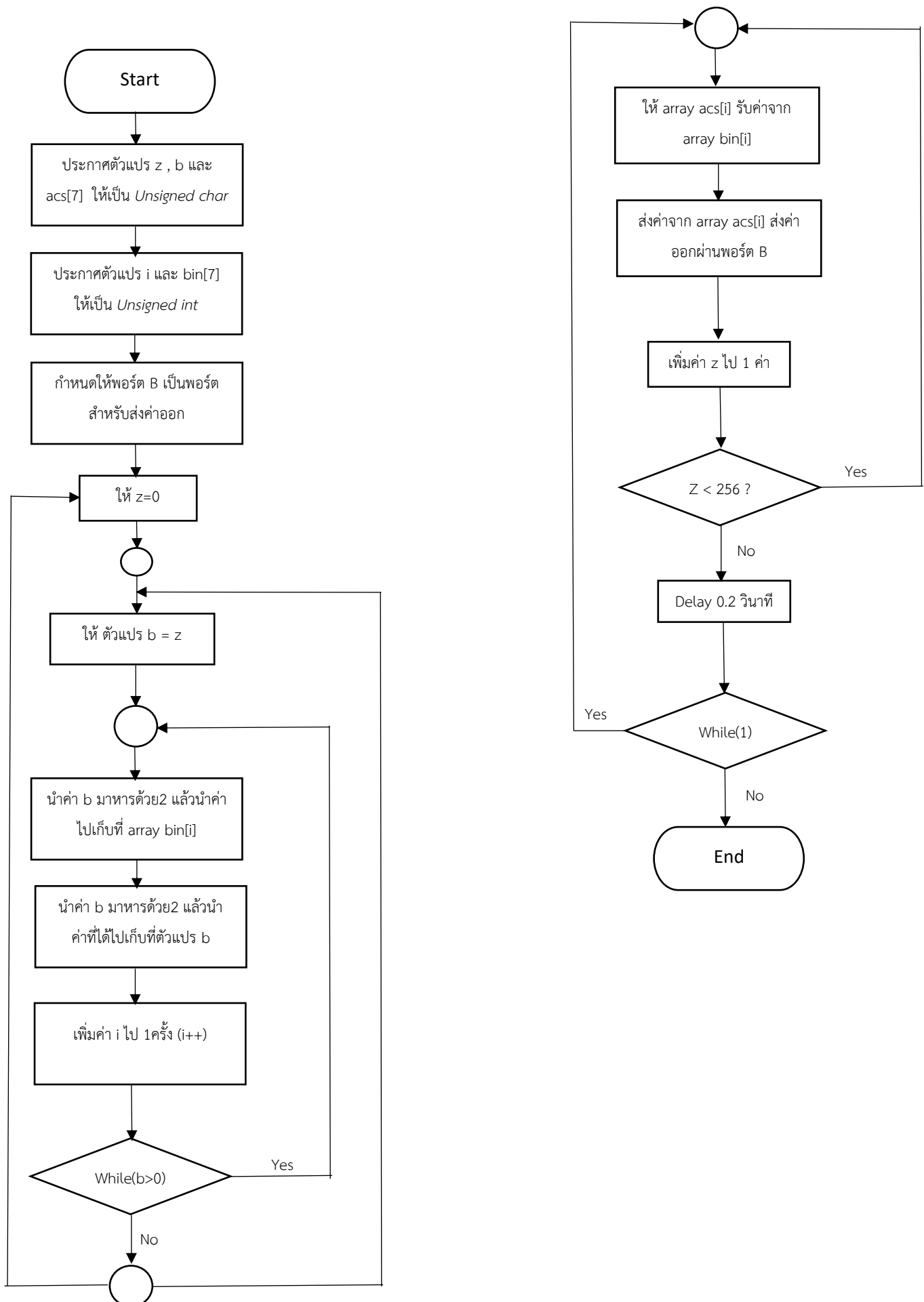


## Conceptual Design

## Flowchart



## Code

```

#define F_CPU 16000000UL
#include <avr/io.h>
#include <util/delay.h>

int main(void)
{
    unsigned char z ;
    unsigned char b ;
    unsigned char acs[7] ;
    unsigned int i=0 ;
    unsigned int bin[7];
    DDRB = 0xFF;
    for (z=0; z<256; z++)
    {
        do
        {
            b=z;
            bin[i]= b%2;
            b/=2;
            i++;
        } while (b>0);
        acs[i]=bin[i];
        PORTB=acs[i];
    }
    _delay_ms(200);
    while (1) ;
}

```